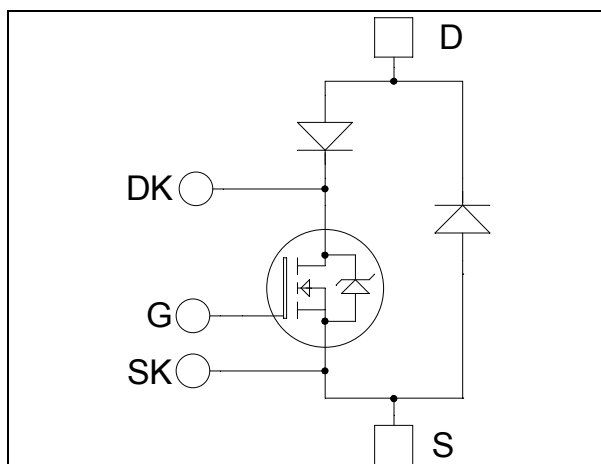


*Single switch
Series & SiC parallel diodes
MOSFET Power Module*

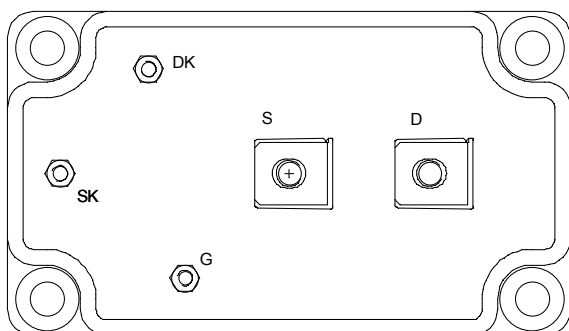
$V_{DSS} = 1000V$

$R_{DS(on)} = 65m\Omega$ typ @ $T_j = 25^\circ C$

$I_D = 145A$ @ $T_c = 25^\circ C$



G, SK and DK terminals are for control signals only
(not for power)



Application

- Welding converters
- Switched Mode Power Supplies
- Uninterruptible Power Supplies
- Motor control

Features

- **Power MOS 7[®] MOSFETs**
 - Low $R_{DS(on)}$
 - Low input and Miller capacitance
 - Low gate charge
 - Avalanche energy rated
 - Very rugged
- **SiC Parallel Schottky Diode**
 - Zero reverse recovery
 - Zero forward recovery
 - Temperature Independent switching behavior
 - Positive temperature coefficient on VF
- Kelvin source for easy drive
- Kelvin drain for voltage monitoring
- Very low stray inductance
 - Symmetrical design
 - M5 power connectors
 - M3 power connectors
- High level of integration
- AlN substrate for improved MOSFET thermal performance

Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Low profile
- RoHS Compliant

All ratings @ $T_j = 25^\circ C$ unless otherwise specified



CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com

Absolute maximum ratings

<i>Symbol</i>	<i>Parameter</i>	<i>Max ratings</i>	<i>Unit</i>
V_{DS}	Drain - Source Breakdown Voltage	1000	V
I_D	Continuous Drain Current	$T_c = 25^\circ\text{C}$	A
		$T_c = 80^\circ\text{C}$	
I_{DM}	Pulsed Drain current	580	
V_{GS}	Gate - Source Voltage	± 30	V
$R_{DS(on)}$	Drain - Source ON Resistance	78	m Ω
P_D	Maximum Power Dissipation	$T_c = 25^\circ\text{C}$	W
I_{AR}	Avalanche current (repetitive and non repetitive)	30	A
E_{AR}	Repetitive Avalanche Energy	50	mJ
E_{AS}	Single Pulse Avalanche Energy	3200	

Electrical Characteristics

<i>Symbol</i>	<i>Characteristic</i>	<i>Test Conditions</i>	<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
I_{DSS}	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 1000V$ $T_j = 25^\circ\text{C}$			400	μA
		$V_{GS} = 0V, V_{DS} = 800V$ $T_j = 125^\circ\text{C}$			2	mA
$R_{DS(on)}$	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 72.5A$		65	78	m Ω
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 20mA$	3		5	V
I_{GSS}	Gate – Source Leakage Current	$V_{GS} = \pm 30V, V_{DS} = 0V$			± 400	nA

Dynamic Characteristics

<i>Symbol</i>	<i>Characteristic</i>	<i>Test Conditions</i>	<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
C_{iss}	Input Capacitance	$V_{GS} = 0V$ $V_{DS} = 25V$ $f = 1MHz$		28.5		nF
C_{oss}	Output Capacitance			5.08		
C_{rss}	Reverse Transfer Capacitance			0.9		
Q_g	Total gate Charge	$V_{GS} = 10V$ $V_{Bus} = 500V$ $I_D = 145A$		1068		nC
Q_{gs}	Gate – Source Charge			136		
Q_{gd}	Gate – Drain Charge			692		
$T_{d(on)}$	Turn-on Delay Time	$V_{GS} = 15V$ $V_{Bus} = 670V$ $I_D = 145A$ $R_G = 0.75\Omega$		18		ns
T_r	Rise Time			14		
$T_{d(off)}$	Turn-off Delay Time			140		
T_f	Fall Time			55		
E_{on}	Turn-on Switching Energy	Inductive switching @ 25°C $V_{GS} = 15V, V_{Bus} = 670V$ $I_D = 145A, R_G = 0.75\Omega$		2.9		mJ
E_{off}	Turn-off Switching Energy			2.9		
E_{on}	Turn-on Switching Energy	Inductive switching @ 125°C $V_{GS} = 15V, V_{Bus} = 670V$ $I_D = 145A, R_G = 0.75\Omega$		4.8		mJ
E_{off}	Turn-off Switching Energy			3.9		
R_{thJC}	Junction to Case Thermal Resistance				0.038	$^\circ\text{C/W}$

Series diode ratings and characteristics

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
V _{RRM}	Maximum Peak Repetitive Reverse Voltage			1000			V
I _{RM}	Maximum Reverse Leakage Current	V _R =1000V				500	μA
I _F	DC Forward Current		T _c = 100°C		240		A
V _F	Diode Forward Voltage	I _F = 240A			1.9	2.5	V
		I _F = 480A			2.2		
		I _F = 240A	T _j = 125°C		1.7		
t _{rr}	Reverse Recovery Time	I _F = 240A V _R = 667V di/dt = 800A/μs	T _j = 25°C		280		ns
	T _j = 125°C			350			
Q _{rr}	Reverse Recovery Charge		T _j = 25°C		3		μC
			T _j = 125°C		14.4		
R _{thJC}	Junction to Case Thermal Resistance					0.19	°C/W

SiC Parallel diode ratings and characteristics

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
V_{RRM}	Maximum Peak Repetitive Reverse Voltage			1200			V
I_{RM}	Maximum Reverse Leakage Current	$V_R = 1200V$	$T_j = 25^\circ C$		384	2400	μA
			$T_j = 125^\circ C$		672	12000	
I_F	DC Forward Current		$T_c = 125^\circ C$		120		A
V_F	Diode Forward Voltage	$I_F = 120A$	$T_j = 25^\circ C$		1.6	1.8	V
			$T_j = 175^\circ C$		2.3	3.0	
Q_C	Total Capacitive Charge	$I_F = 120A, V_R = 1200V$ $di/dt = 5000A/\mu s$			960		nC
C	Total Capacitance	$f = 1MHz, V_R = 200V$			1152		pF
		$f = 1MHz, V_R = 400V$			828		
R_{thJC}	Junction to Case Thermal Resistance					0.18	$^\circ C/W$

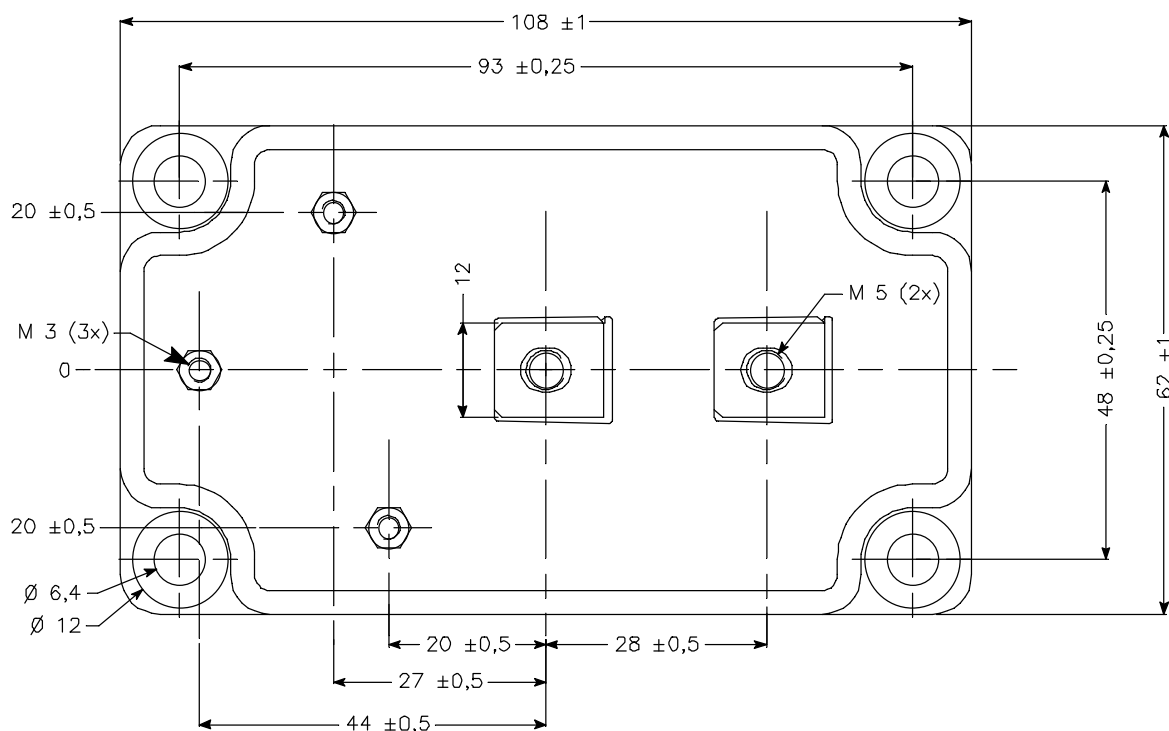
Thermal and package characteristics

Symbol	Characteristic			Min	Max	Unit
V _{ISOL}	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz			4000		V
T _J	Operating junction temperature range			-40	150	°C
T _{JOP}	Recommended junction temperature under switching conditions			-40	T _J max -25	
T _{STG}	Storage Temperature Range			-40	125	
T _C	Operating Case Temperature			-40	100	
Torque	Mounting torque	To heatsink	M6	3	5	N.m
		For terminals	M5	2	3.5	
			M3	1	1.5	
Wt	Package Weight				300	g



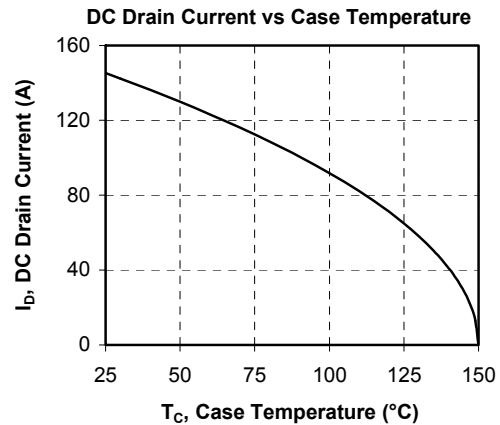
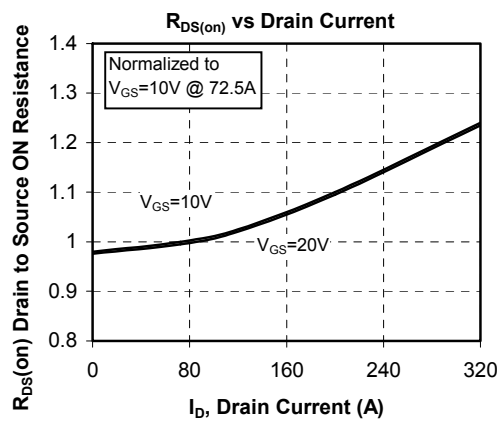
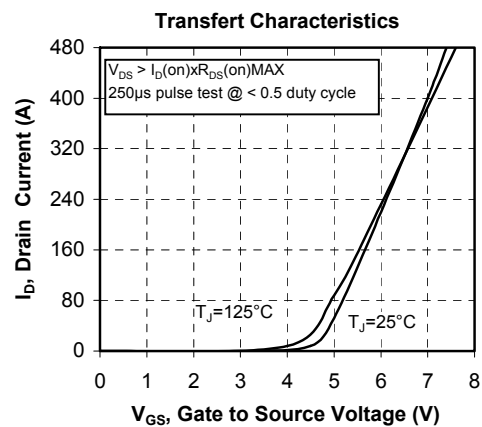
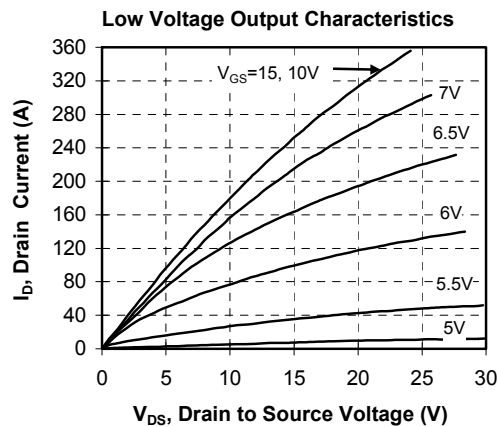
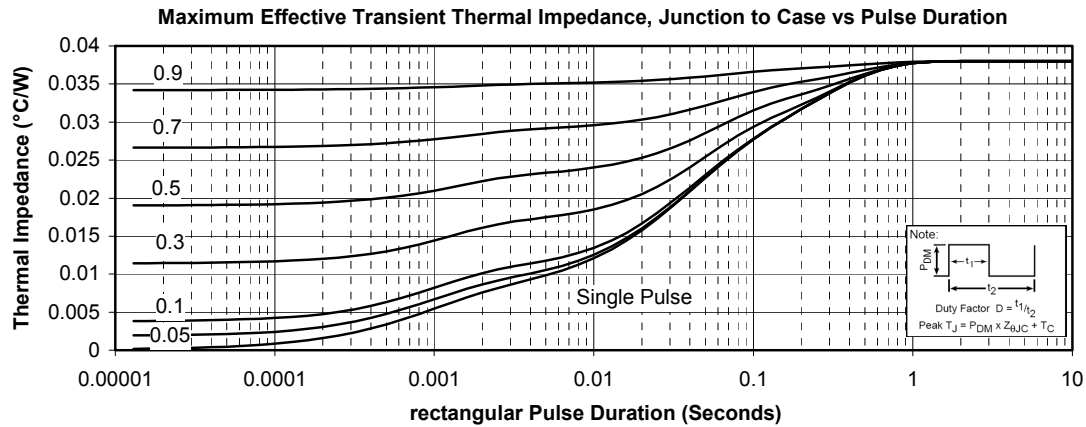
Technical drawing of a mechanical assembly. The drawing shows a side view of a component with a central shaft and two end flanges. The dimensions are as follows:

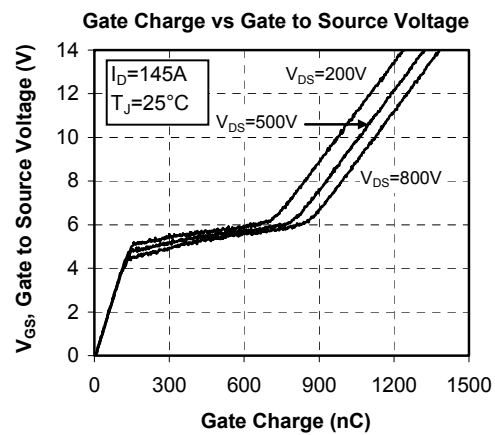
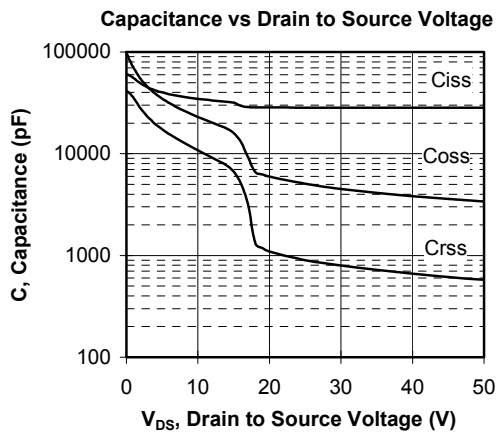
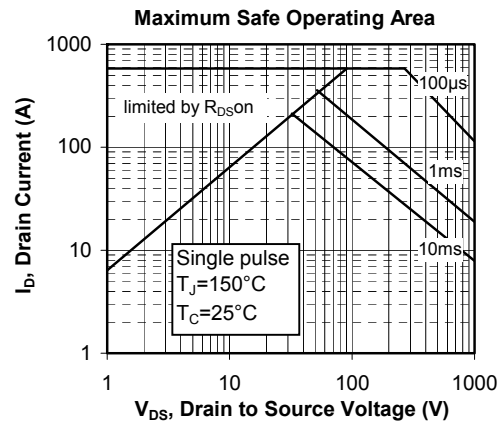
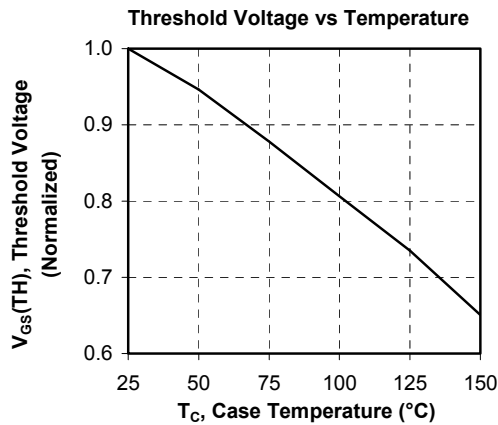
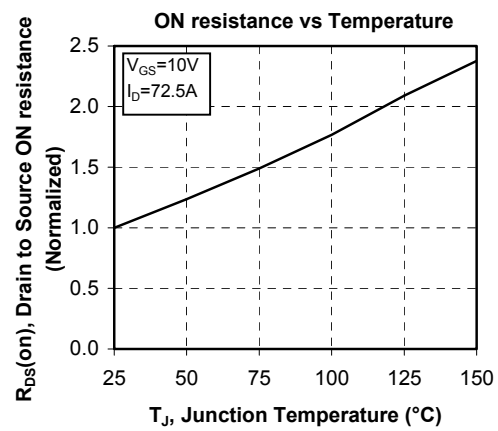
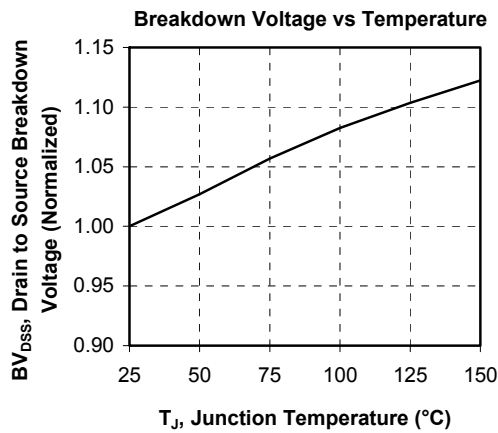
- Overall length: $22,6 \pm 0,5$
- Distance from left end to first flange: $15 \pm 0,5$
- Distance from first flange to second flange: $6,5$
- Distance from second flange to right end: $7,8$
- Distance from left end to first flange (alternative dimension): 6
- Distance from first flange to second flange (alternative dimension): 14
- Distance from second flange to right end (alternative dimension): $17 \pm 0,5$

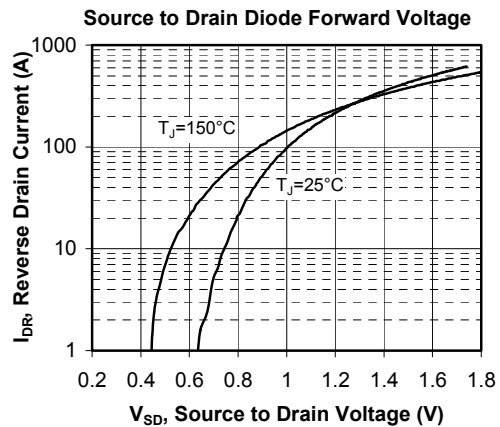
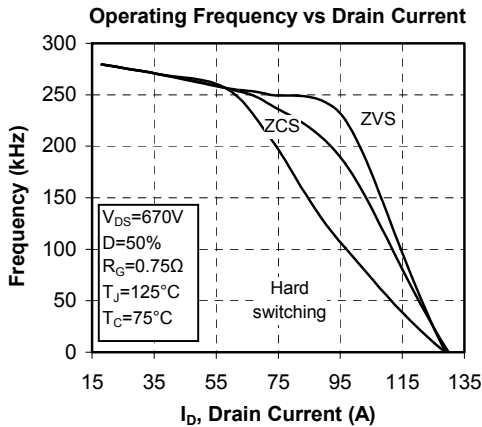
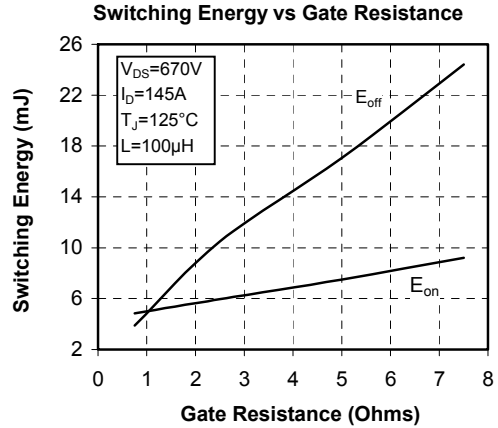
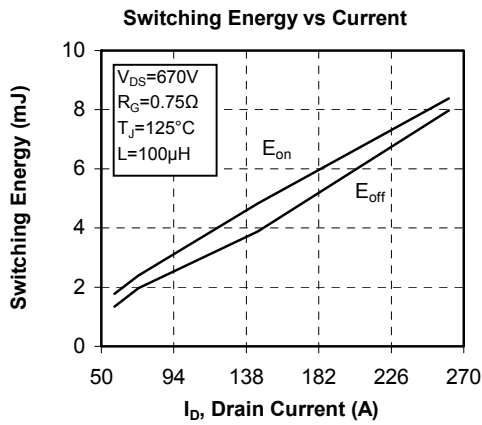
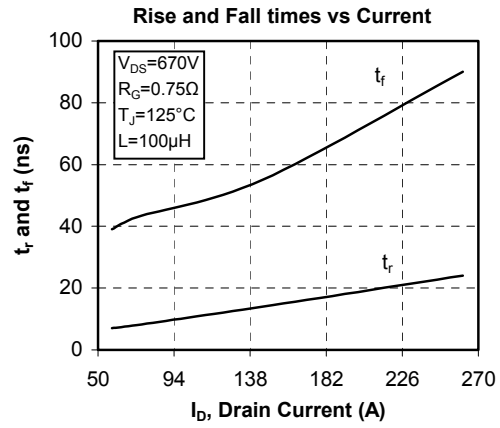
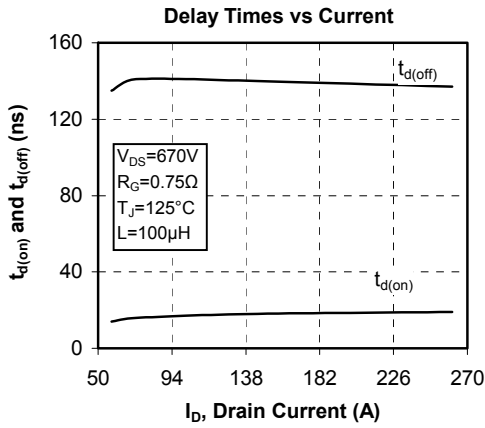


See application note APT0601 - Mounting Instructions for SP6 Power Modules on www.microsemi.com

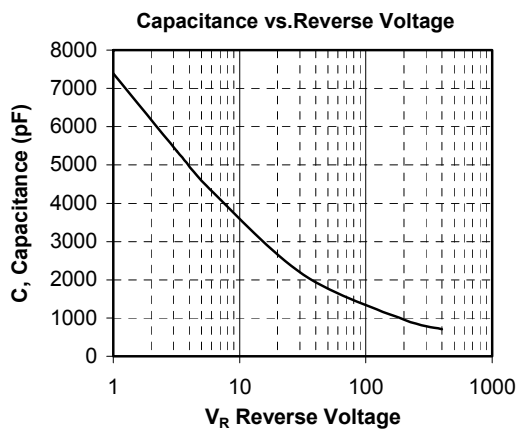
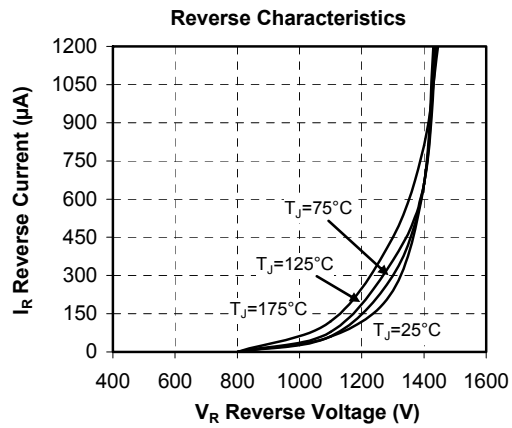
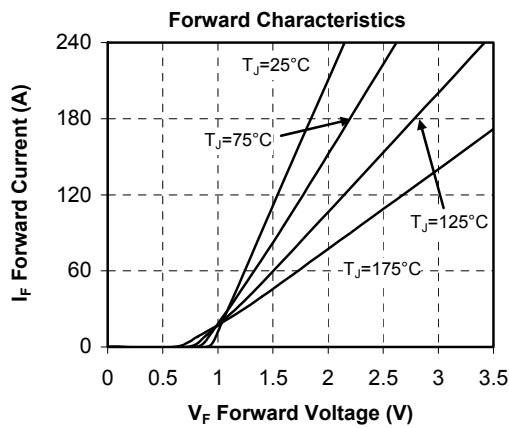
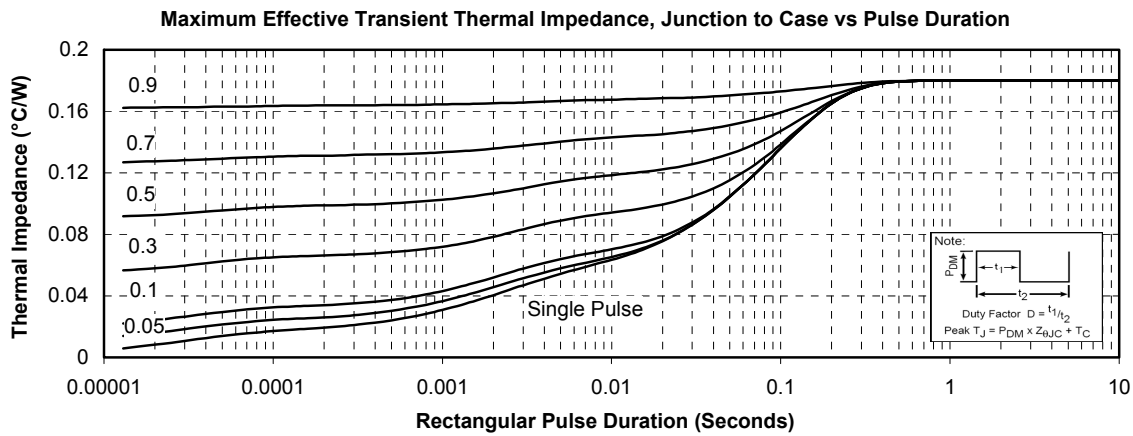
Typical MOSFET Performance Curve







Typical SiC Diode Performance Curve



DISCLAIMER

The information contained in the document (unless it is publicly available on the Web without access restrictions) is PROPRIETARY AND CONFIDENTIAL information of Microsemi and cannot be copied, published, uploaded, posted, transmitted, distributed or disclosed or used without the express duly signed written consent of Microsemi. If the recipient of this document has entered into a disclosure agreement with Microsemi, then the terms of such Agreement will also apply. This document and the information contained herein may not be modified, by any person other than authorized personnel of Microsemi. No license under any patent, copyright, trade secret or other intellectual property right is granted to or conferred upon you by disclosure or delivery of the information, either expressly, by implication, inducement, estoppels or otherwise. Any license under such intellectual property rights must be approved by Microsemi in writing signed by an officer of Microsemi.

Microsemi reserves the right to change the configuration, functionality and performance of its products at anytime without any notice. This product has been subject to limited testing and should not be used in conjunction with life-support or other mission-critical equipment or applications. Microsemi assumes no liability whatsoever, and Microsemi disclaims any express or implied warranty, relating to sale and/or use of Microsemi products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright or other intellectual property right. Any performance specifications believed to be reliable but are not verified and customer or user must conduct and complete all performance and other testing of this product as well as any user or customers final application. User or customer shall not rely on any data and performance specifications or parameters provided by Microsemi. It is the customer's and user's responsibility to independently determine suitability of any Microsemi product and to test and verify the same. The information contained herein is provided "AS IS, WHERE IS" and with all faults, and the entire risk associated with such information is entirely with the User. Microsemi specifically disclaims any liability of any kind including for consequential, incidental and punitive damages as well as lost profit. The product is subject to other terms and conditions which can be located on the web at <http://www.microsemi.com/legal/tnc.asp>

Life Support Application

Seller's Products are not designed, intended, or authorized for use as components in systems intended for space, aviation, surgical implant into the body, in other applications intended to support or sustain life, or for any other application in which the failure of the Seller's Product could create a situation where personal injury, death or property damage or loss may occur (collectively "Life Support Applications").

Buyer agrees not to use Products in any Life Support Applications and to the extent it does it shall conduct extensive testing of the Product in such applications and further agrees to indemnify and hold Seller, and its officers, employees, subsidiaries, affiliates, agents, sales representatives and distributors harmless against all claims, costs, damages and expenses, and attorneys' fees and costs arising, directly or indirectly, out of any claims of personal injury, death, damage or otherwise associated with the use of the goods in Life Support Applications, even if such claim includes allegations that Seller was negligent regarding the design or manufacture of the goods.

Buyer must notify Seller in writing before using Seller's Products in Life Support Applications. Seller will study with Buyer alternative solutions to meet Buyer application specification based on Sellers sales conditions applicable for the new proposed specific part.