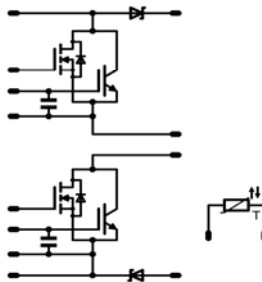


flowBoost0		600V/110A PS*	
Features • *PS: 2x 110A parallel switch (100A IGBT and 99mΩ MOSFET) • high speed IGBT with C6 MOSFET and SiC buck diodes • high efficiency dual booster • ultra fast switching frequency • low inductance layout		flow0 12mm housing 	
Target Applications • solar inverter • UPS		Schematic 	
Types • 10-FZ06NBA110FP-M306L28			

Maximum Ratings

Tj=25°C, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
Input Boost IGBT				
Collector-emitter break down voltage	V_{CE}		600	V
DC collector current	I_C	$T_j = T_{jmax}$ $T_h = 80^\circ\text{C}$ $T_c = 80^\circ\text{C}$	72 96	A
Repetitive peak collector current	I_{Cpulse}	t_p limited by T_{jmax}	400	A
Power dissipation per IGBT	P_{tot}	$T_j = T_{jmax}$ $T_h = 80^\circ\text{C}$ $T_c = 80^\circ\text{C}$	131 199	W
Gate-emitter peak voltage	V_{GE}		±20	V
Short circuit ratings	t_{SC} V_{CC}	$T_j \leq 125^\circ\text{C}$ $V_{GE} = 15V$	5 400	µs V
Maximum Junction Temperature	T_{jmax}		175	°C
Input Boost FWD				
Peak Repetitive Reverse Voltage	V_{RRM}	$T_j = 25^\circ\text{C}$	600	V
DC forward current	I_F	$T_j = T_{jmax}$ $T_h = 80^\circ\text{C}$ $T_c = 80^\circ\text{C}$	49 63	A
Repetitive peak forward current	I_{FRM}	t_p limited by T_{jmax}	210	A
Power dissipation per Diode	P_{tot}	$T_j = T_{jmax}$ $T_h = 80^\circ\text{C}$ $T_c = 80^\circ\text{C}$	88 133	W
Maximum Junction Temperature	T_{jmax}		175	°C

Maximum Ratings

$T_j=25^{\circ}\text{C}$, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
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Input Boost MOSFET

Drain to source breakdown voltage	V_{DS}		600	V
DC drain current	I_D	$T_j=T_{jmax}$ $T_h=80^{\circ}\text{C}$ $T_c=80^{\circ}\text{C}$	17 19	A
Pulsed drain current	I_{Dpulse}	t_p limited by T_{jmax} $T_c=25^{\circ}\text{C}$	112	A
Power dissipation	P_{tot}	$T_j=T_{jmax}$ $T_h=80^{\circ}\text{C}$ $T_c=80^{\circ}\text{C}$	67 101	W
Gate-source peak voltage	V_{GS}		± 20	V
Maximum Junction Temperature	T_{jmax}		150	$^{\circ}\text{C}$

Thermal Properties

Storage temperature	T_{stg}		$-40\dots+125$	$^{\circ}\text{C}$
Operation temperature under switching condition	T_{op}		$-40\dots+(T_{jmax} - 25)$	$^{\circ}\text{C}$

Insulation Properties

Insulation voltage	V_{IS}	$t=2s$ DC voltage	4000	V
Creepage distance			min 12,7	mm
Clearance			min 12,7	mm

Characteristic Values

Parameter	Symbol	Conditions					Value			Unit
			V _{GE} [V] or V _{GS} [V]	V _r [V] or V _{CE} [V] or V _{DS} [V]	I _c [A] or I _F [A] or I _b [A]	T _j	Min	Typ	Max	
Input Boost IGBT *										
Gate emitter threshold voltage	V _{GE(th)}	V _{CE} =V _{GE}			0.0008	T _j =25°C T _j =150°C	4.1	5.1	5.7	V
Collector-emitter saturation voltage	V _{CE(sat)}		15		100	T _j =25°C T _j =150°C		1.85	2.3	V
Collector-emitter cut-off current incl. Diode	I _{CES}		0	600		T _j =25°C T _j =150°C			40	uA
Gate-emitter leakage current	I _{GES}		±20	0		T _j =25°C T _j =150°C			100	nA
Integrated Gate resistor	R _{gint}							tbd.		Ω
Input capacitance	C _{ies}	f=1MHz	0	25		T _j =25°C		5920		pF
Output capacitance	C _{oss}							232		pF
Reverse transfer capacitance	C _{rss}							192		
Gate charge	Q _{Gate}		15	480	100	T _j =25°C		630		nC
Thermal resistance chip to heatsink per chip	R _{thJH}	Thermal grease thickness≤50um λ = 1 W/mK						0.72		K/W

* see dynamic characteristic at **MosFET**

Input Boost FWD

Diode forward voltage	V_F				48	$T_j=25^{\circ}C$ $T_j=125^{\circ}C$		1.55 1.69	1.75	V
Peak reverse recovery current	I_{RRM}	$R_{gon}=4 \Omega **$	15	350	77	$T_j=25^{\circ}C$ $T_j=125^{\circ}C$		tbd.		A
Reverse recovery time	t_{rr}					$T_j=25^{\circ}C$ $T_j=125^{\circ}C$		tbd.		ns
Reverse recovered charge	Q_{rr}					$T_j=25^{\circ}C$ $T_j=125^{\circ}C$		tbd.		μC
Peak rate of fall of recovery current	$di(rec)_{max}/dt$					$T_j=25^{\circ}C$ $T_j=125^{\circ}C$		tbd.		A/ μs
Reverse recovered energy	E_{rec}					$T_j=25^{\circ}C$ $T_j=125^{\circ}C$		tbd.		mWs
Thermal resistance chip to heatsink per chip	R_{thJH}	Thermal grease thickness $\leq 50\mu m$ $\lambda = 1 W/mK$						1.10		K/W

Input Boost MOSFET

Static drain to source ON resistance	$R_{ds(on)}$		10		18.1	$T_j=25^{\circ}C$ $T_j=150^{\circ}C$		90 230	99	m Ω
Gate threshold voltage	$V_{(GS)th}$		$V_{DS}=V_{GS}$		0.00121	$T_j=25^{\circ}C$ $T_j=125^{\circ}C$	2.5	3	3.5	V
Gate to Source Leakage Current	I_{gss}		20	0		$T_j=25^{\circ}C$ $T_j=125^{\circ}C$			100	nA
Zero Gate Voltage Drain Current	I_{dss}		0	600		$T_j=25^{\circ}C$ $T_j=125^{\circ}C$			5	μA
Turn On Delay Time	$t_{d(ON)}$	$R_{gon}=1,7 \Omega **$ $R_{goff}=1,7 \Omega **$	± 13	350	77	$T_j=25^{\circ}C$ $T_j=125^{\circ}C$		tbd.		ns
Rise Time	t_r					$T_j=25^{\circ}C$ $T_j=125^{\circ}C$		tbd.		
Turn off delay time	$t_{d(OFF)}$					$T_j=25^{\circ}C$ $T_j=125^{\circ}C$		tbd.		
Fall time	t_f					$T_j=25^{\circ}C$ $T_j=125^{\circ}C$		tbd.		
Turn-on energy loss per pulse	E_{on}					$T_j=25^{\circ}C$ $T_j=125^{\circ}C$		tbd.		mWs
Turn-off energy loss per pulse	E_{off}					$T_j=25^{\circ}C$ $T_j=125^{\circ}C$		tbd.		
Total gate charge	Q_g		10	480	18.1	$T_j=25^{\circ}C$		119		nC
Gate to source charge	Q_{gs}							14		
Gate to drain charge	Q_{gd}							61		
Input capacitance	C_{iss}	f=1MHz	0	100		$T_j=25^{\circ}C$		2660		pF
Output capacitance	C_{oss}							154		
Thermal resistance chip to heatsink per chip	R_{thJH}	Thermal grease thickness $\leq 50\mu m$ $\lambda = 1 W/mK$						1.05		K/W

Characteristic Values

Parameter	Symbol	Conditions					Value			Unit
			V_{GE} [V] or V_{GS} [V]	V_i [V] or V_{CE} [V] or V_{DS} [V]	I_c [A] or I_F [A] or I_b [A]	T_j	Min	Typ	Max	
IGBT gate capacitor										
C value	C							4.7		nF
Thermistor										
Rated resistance*	R_{25}	Tol. $\pm 13\%$				$T_j=25^\circ\text{C}$		22000		Ω
	R_{100}	Tol. $\pm 5\%$				$T_j=100^\circ\text{C}$		1486		Ω
Deviation of R_{100}	$D_{R/R}$	$R_{100}=1503\Omega$				$T_c=100^\circ\text{C}$	+5		-5	%/K
Power dissipation	P					$T_j=25^\circ\text{C}$		200		mW
B-value	$B_{(25/100)}$	Tol. $\pm 3\%$				$T_j=25^\circ\text{C}$		3950		K

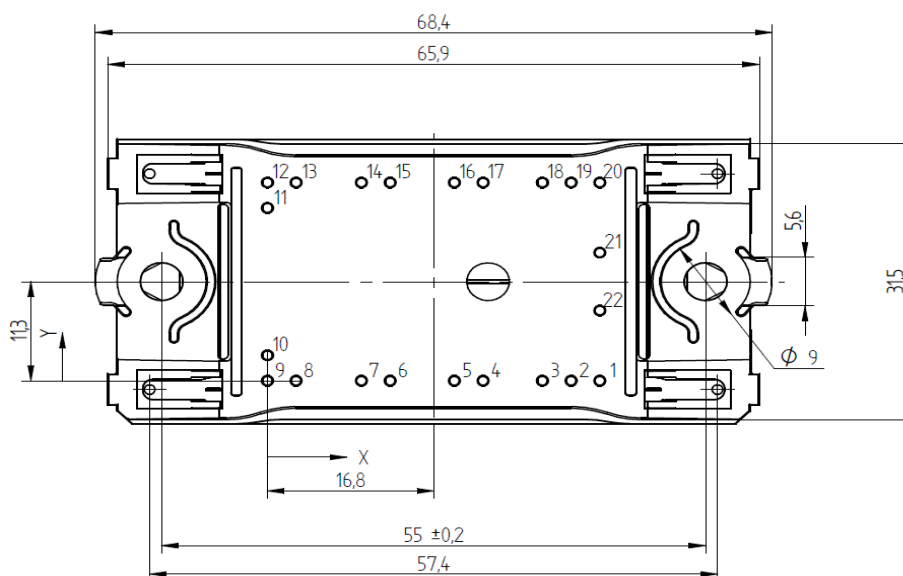
Ordering Code and Marking - Outline - Pinout

Ordering Code & Marking

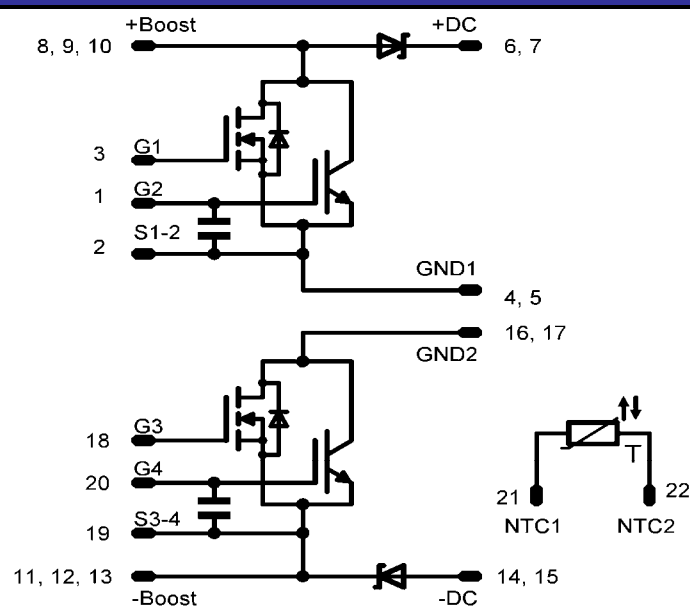
Version	Ordering Code	in DataMatrix as	in packaging barcode as
without thermal paste 12mm housing	10-FZ06NBA110FP-M306L28	M306L28	M306L28

Outline

Pin table		
Pin	X	Y
1	33,6	0
2	30,7	0
3	27,8	0
4	21,8	0
5	18,9	0
6	12,4	0
7	9,5	0
8	2,9	0
9	0	0
10	0	2,9
11	0	19,7
12	0	22,6
13	2,9	22,6
14	9,5	22,6
15	12,4	22,6
16	18,9	22,6
17	21,8	22,6
18	27,8	22,6
19	30,7	22,6
20	33,6	22,6
21	33,6	14,6
22	33,6	8



Pinout



PRODUCT STATUS DEFINITIONS

Datasheet Status	Product Status	Definition
Target	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice. The data contained is exclusively intended for technically trained staff.
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