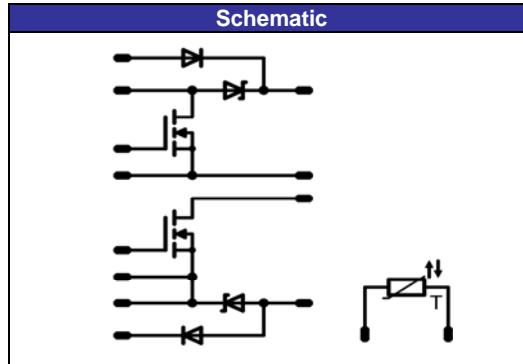


flowBOOST0
600V/41mΩ

Features
• High efficiency symmetric boost
• Ultra fast switching with MOSFET and SiC diodes
• Low Inductance Layout
• Tandem to NPC and MNPC modules



Target Applications
• Neutral point solar inverters
• Solar inverters
• UPS



Maximum Ratings

T_j=25°C, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
Bypass Diode				
Repetitive peak reverse voltage	V _{RRM}		1600	V
Forward current per diode	I _{FAV}	DC current T _h =80°C T _c =80°C	42 57	A
Surge forward current	I _{FSM}	T _j =25°C	370	A
I ² t-value	I ² t	t _p =10ms T _j =150°C	370	A ² s
Power dissipation per Diode	P _{tot}	T _j =T _j max T _h =80°C T _c =80°C	49 75	W
Maximum Junction Temperature	T _j max		150	°C

Input Boost MOSFET

Drain to source breakdown voltage	V _{DS}		600	V
DC drain current	I _D	T _j =T _j max T _h =80°C T _c =80°C	32 39	A
Pulsed drain current	I _{Dpulse}	t _p limited by T _j max	272	A
Power dissipation	P _{tot}	T _j =T _j max T _h =80°C T _c =80°C	97 147	W
Gate-source peak voltage	V _{GS}		±20	V
Maximum Junction Temperature	T _j max		150	°C

Maximum Ratings

T_j=25°C, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
Peak Repetitive Reverse Voltage	V _{RRM}	T _j =25°C	600	V
DC forward current	I _F	T _j =T _{jmax} T _c =80°C	20 24	A
Repetitive peak forward current	I _{FRM}	t _p limited by T _{jmax}	114	A
Power dissipation	P _{tot}	T _j =T _{jmax} T _c =80°C	41 63	W
Maximum Junction Temperature	T _{jmax}		150	°C

Thermal Properties

Storage temperature	T _{stg}		-40...+125	°C
Operation temperature under switching condition	T _{op}		-40...+(T _{jmax} - 25)	°C

Insulation Properties

Insulation voltage	V _{is}	t=2s	DC voltage	4000	V
Creepage distance				min 12,7	mm
Clearance				min 12,7	mm

Characteristic Values

Parameter	Symbol	Conditions					Value			Unit
			V _{GE} [V] or V _{GS} [V]	V _I [V] or V _{CE} [V] or V _{DS} [V]	I _C [A] or I _F [A] or I _D [A]	T _J	Min	Typ	Max	
Bypass Diode										
Forward voltage	V _F				35	T _J =25°C T _J =125°C	0,8	1,14 1,09	1,3	V
Threshold voltage (for power loss calc. only)	V _{to}				35	T _J =25°C T _J =125°C		0,92 0,81		V
Slope resistance (for power loss calc. only)	r _t				35	T _J =25°C T _J =125°C		0,006 0,008		Ω
Reverse current	I _r			1600		T _J =25°C T _J =125°C			0,1	mA
Thermal resistance chip to heatsink per chip	R _{thJH}	Thermal grease thickness≤50um λ = 1 W/mK						1,42		K/W
Input Boost MOSFET										
Static drain to source ON resistance	R _{DS(on)}		10		44,4	T _J =25°C T _J =125°C		0,04 0,08		Ω
Gate threshold voltage	V _{(GS)th}	V _{GS} =V _{DS}			0,00296	T _J =25°C T _J =125°C	2,4	3	3,6	V
Gate to Source Leakage Current	I _{gss}		0	600		T _J =25°C T _J =125°C			100	nA
Zero Gate Voltage Drain Current	I _{dss}		20	0		T _J =25°C T _J =125°C			5	μA
Turn On Delay Time	t _{d(ON)}	R _{gooff} =8 Ω R _{gon} =8 Ω	10/0	400	15	T _J =25°C T _J =125°C		33 30		ns
Rise Time	t _r					T _J =25°C T _J =125°C		9 10		
Turn off delay time	t _{d(OFF)}					T _J =25°C T _J =125°C		290 317		
Fall time	t _f					T _J =25°C T _J =125°C		14 5		
Turn-on energy loss per pulse	E _{on}					T _J =25°C T _J =125°C		0,13 0,14		mWs
Turn-off energy loss per pulse	E _{off}					T _J =25°C T _J =125°C		0,06 0,07		
Total gate charge	Q _g					T _J =25°C		290		
Gate to source charge	Q _{gs}	R _{gon} =8 Ω	10/0	480	44	T _J =25°C		36		nC
Gate to drain charge	Q _{gd}					T _J =25°C		150		
Input capacitance	C _{iss}							6530		
Output capacitance	C _{oss}	f=1MHz	0	100		T _J =25°C		360		pF
Reverse transfer capacitance	C _{rss}							tbd.		
Thermal resistance chip to heatsink per chip	R _{thJH}	Thermal grease thickness≤50um λ = 1 W/mK						0,72		K/W
Input Boost Diode										
Forward voltage	V _F				15	T _J =25°C T _J =125°C	0,9	1,50 1,76	1,9	V
Reverse leakage current	I _{rm}		10/0	400		T _J =25°C T _J =125°C			100	μA
Peak recovery current	I _{RRM}	R _{gon} =8 Ω	10/0	400	15	T _J =25°C T _J =125°C		8 7		A
Reverse recovery time	t _{rr}					T _J =25°C T _J =125°C		9 10		ns
Reverse recovery charge	Q _{rr}					T _J =25°C T _J =125°C		0,11 0,14		μC
Reverse recovered energy	E _{rec}					T _J =25°C T _J =125°C		0,03 0,04		mWs
Peak rate of fall of recovery current	d(i(rec))/dt					T _J =25°C T _J =125°C		2512 1984		A/μs
Thermal resistance chip to heatsink per chip	R _{thJH}	Thermal grease thickness≤50um λ = 1 W/mK						1,69		K/W
Thermistor										
Rated resistance*	R ₂₅	Tol. ±5%				T _J =25°C	20,9	22	23,1	kΩ
	R ₁₀₀							1486		Ω
Power dissipation	P					T _J =25°C		200		mW
B-value	B _(25/100)	Tol. ±3%				T _J =25°C		3950		K

* see details on Thermistor charts on Figure 2.

INPUT BOOST

Figure 1
BOOST MOSFET
Typical output characteristics

$$I_D = f(V_{DS})$$

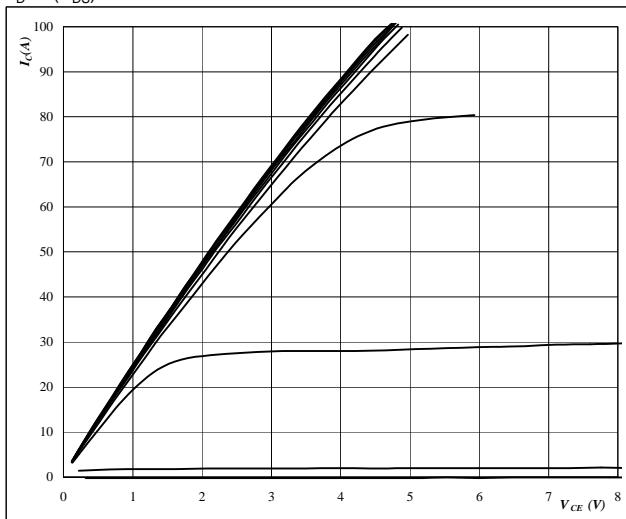

 $t_p = 250 \mu\text{s}$
 $T_j = 25^\circ\text{C}$
 V_{DS} from 3 V to 13 V in steps of 1 V

Figure 2
BOOST MOSFET
Typical output characteristics

$$I_D = f(V_{DS})$$

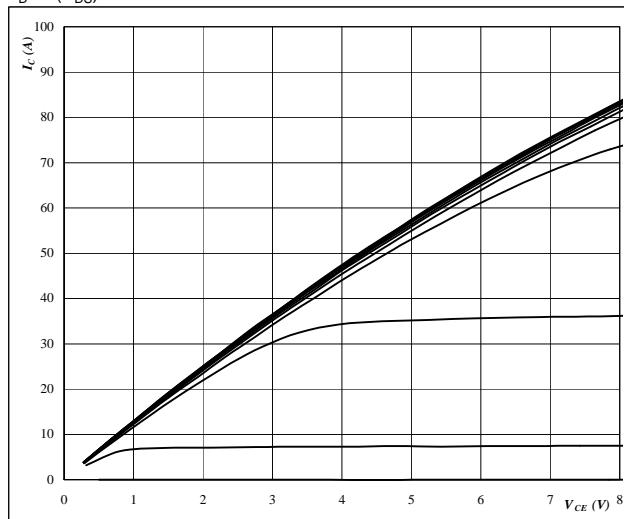
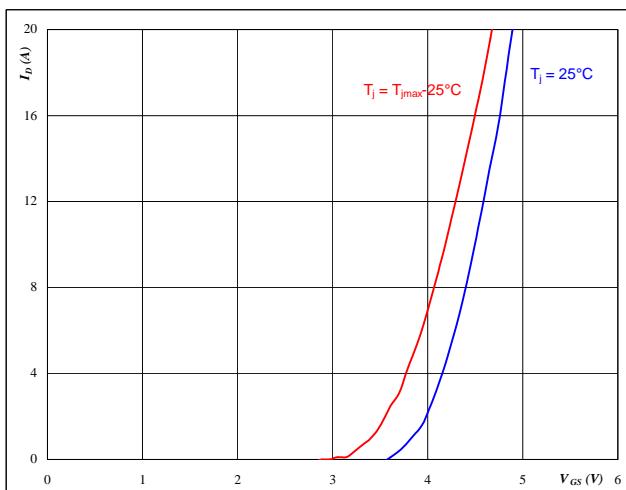
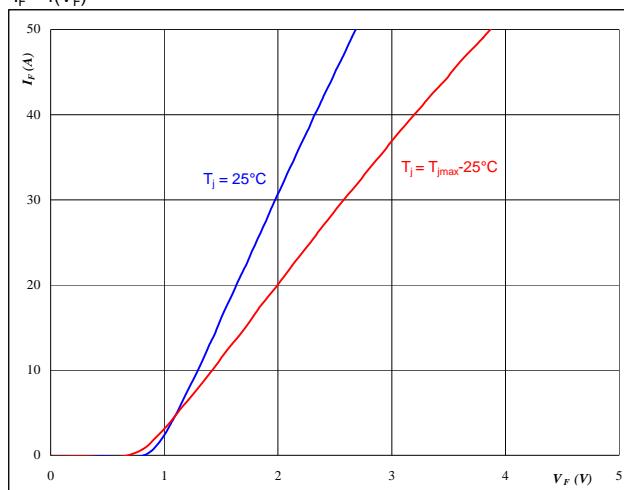

 $t_p = 250 \mu\text{s}$
 $T_j = 125^\circ\text{C}$
 V_{DS} from 3 V to 13 V in steps of 1 V

Figure 3
BOOST MOSFET
Typical transfer characteristics

$$I_D = f(V_{DS})$$


At
 $t_p = 250 \mu\text{s}$
 $V_{DS} = 10 \text{ V}$
Figure 4
BOOST FWD
Typical diode forward current as a function of forward voltage

$$I_F = f(V_F)$$

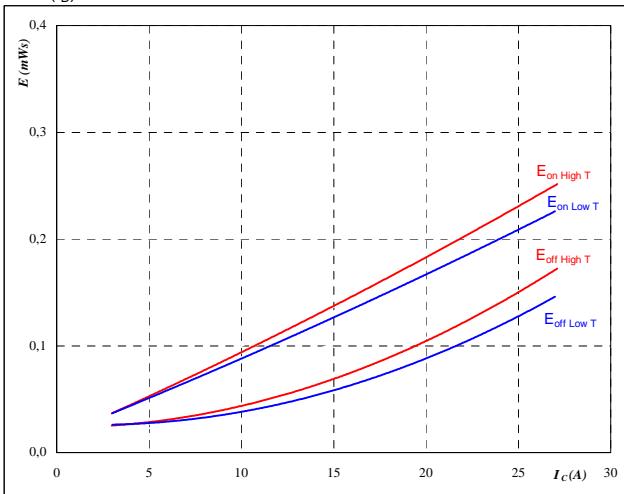

At
 $t_p = 250 \mu\text{s}$

INPUT BOOST

Figure 5

**Typical switching energy losses
as a function of collector current**

$$E = f(I_D)$$



With an inductive load at

$$T_j = 25/125 \quad ^\circ\text{C}$$

$$V_{DS} = 400 \quad \text{V}$$

$$V_{GS} = +10/0 \quad \text{V}$$

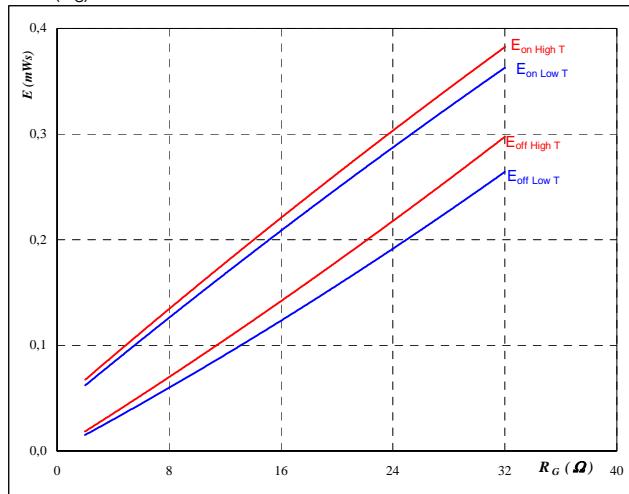
$$R_{gon} = 8 \quad \Omega$$

$$R_{goff} = 8 \quad \Omega$$

BOOST MOSFET
Figure 6

**Typical switching energy losses
as a function of gate resistor**

$$E = f(R_G)$$



With an inductive load at

$$T_j = 25/125 \quad ^\circ\text{C}$$

$$V_{DS} = 400 \quad \text{V}$$

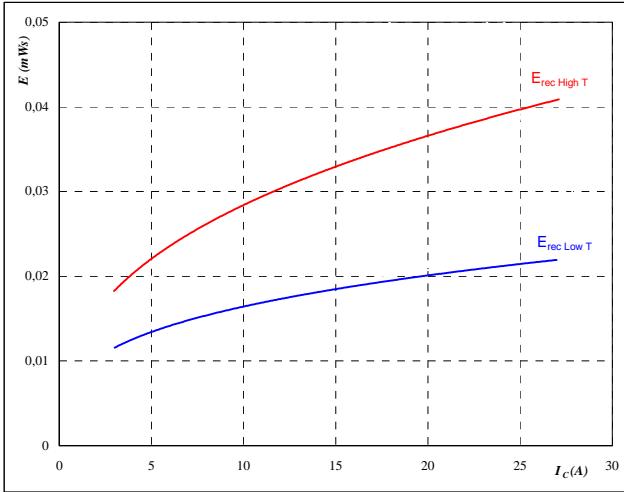
$$V_{GS} = +10/0 \quad \text{V}$$

$$I_D = 15 \quad \text{A}$$

Figure 7
BOOST MOSFET

**Typical reverse recovery energy loss
as a function of collector (drain) current**

$$E_{rec} = f(I_c)$$



With an inductive load at

$$T_j = 25/125 \quad ^\circ\text{C}$$

$$V_{DS} = 400 \quad \text{V}$$

$$V_{GS} = +10/0 \quad \text{V}$$

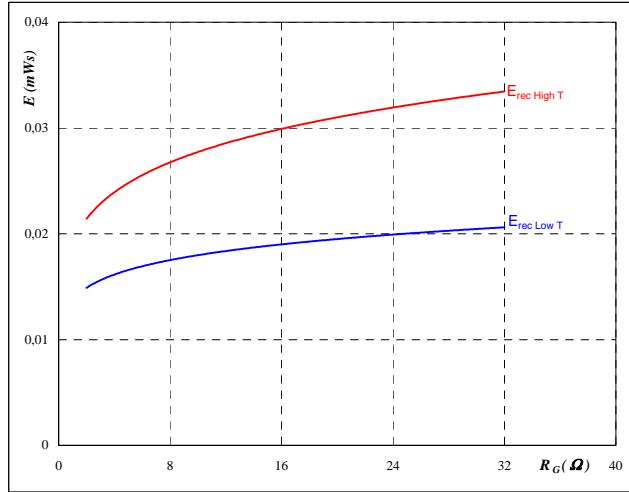
$$R_{gon} = 8 \quad \Omega$$

$$R_{goff} = 8 \quad \Omega$$

Figure 8
BOOST MOSFET

**Typical reverse recovery energy loss
as a function of gate resistor**

$$E_{rec} = f(R_G)$$



With an inductive load at

$$T_j = 25/125 \quad ^\circ\text{C}$$

$$V_{DS} = 400 \quad \text{V}$$

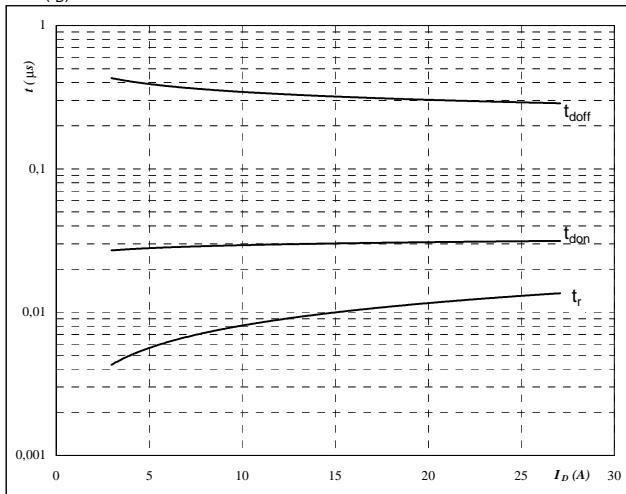
$$V_{GS} = +10/0 \quad \text{V}$$

$$I_D = 15 \quad \text{A}$$

INPUT BOOST

Figure 9
BOOST MOSFET
Typical switching times as a function of collector current

$t = f(I_D)$



With an inductive load at

T_j = 125 °C

V_{DS} = 400 V

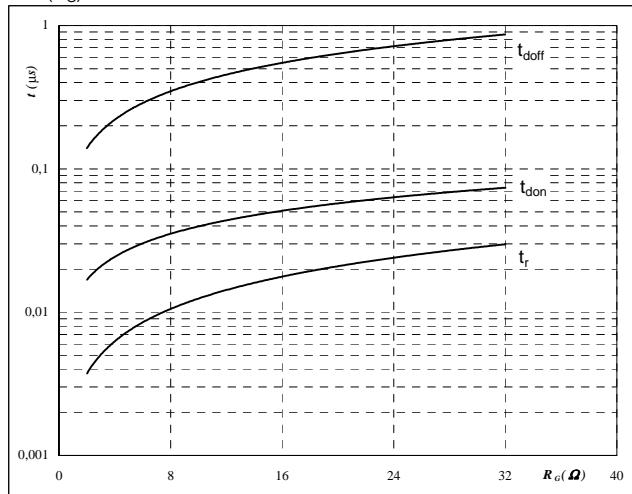
V_{GS} = +10/0 V

R_{gon} = 8 Ω

R_{goff} = 8 Ω

Figure 10
BOOST MOSFET
Typical switching times as a function of gate resistor

$t = f(R_G)$



With an inductive load at

T_j = 125 °C

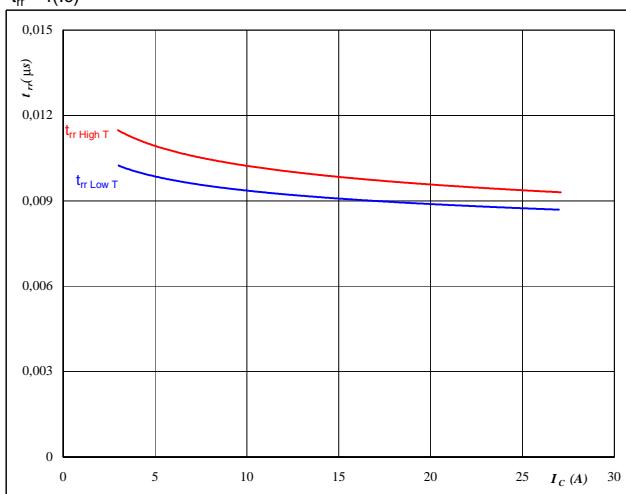
V_{DS} = 400 V

V_{GS} = +10/0 V

I_C = 15 A

Figure 11
BOOST FWD
Typical reverse recovery time as a function of collector current

t_{rr} = f(I_C)



At

T_j = 25/125 °C

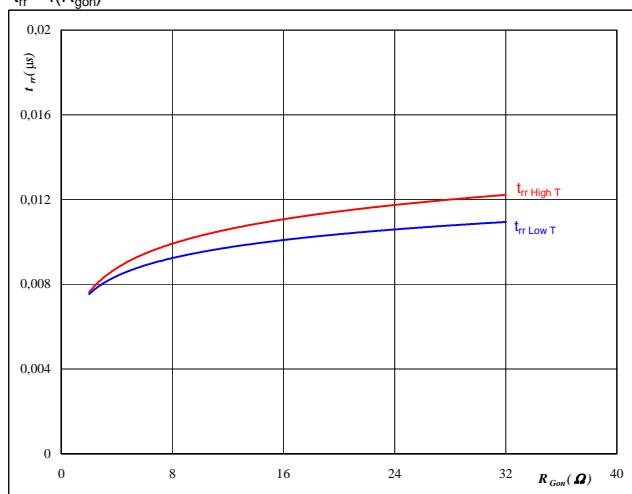
V_{DS} = 400 V

V_{GS} = +10/0 V

R_{gon} = 8 Ω

Figure 12
BOOST FWD
Typical reverse recovery time as a function of MOSFET turn on gate resistor

t_{rr} = f(R_{gon})



At

T_j = 25/125 °C

V_R = 400 V

I_F = 15 A

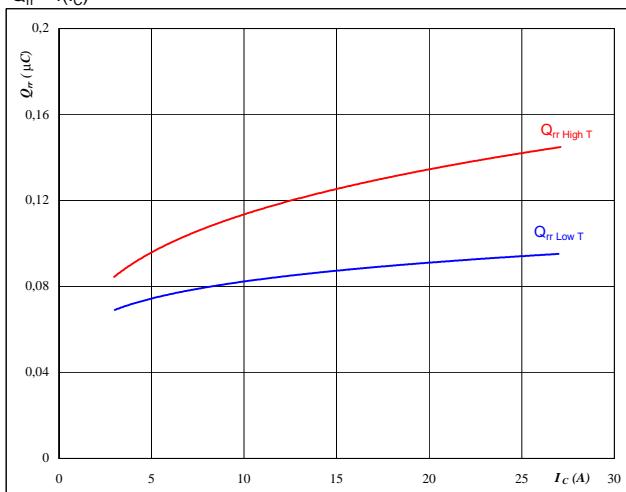
V_{GS} = +10/0 V

INPUT BOOST

Figure 13
BOOST FWD

Typical reverse recovery charge as a function of collector current

$$Q_{rr} = f(I_C)$$


At

$$T_j = 25/125 \quad ^\circ\text{C}$$

$$V_{DS} = 400 \quad \text{V}$$

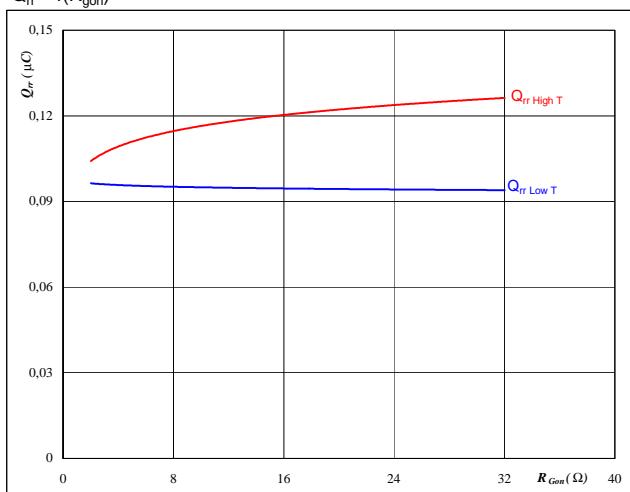
$$V_{GS} = +10/0 \quad \text{V}$$

$$R_{gon} = 8 \quad \Omega$$

Figure 14
BOOST FWD

Typical reverse recovery charge as a function of MOSFET turn on gate resistor

$$Q_{rr} = f(R_{gon})$$


At

$$T_j = 25/125 \quad ^\circ\text{C}$$

$$V_R = 400 \quad \text{V}$$

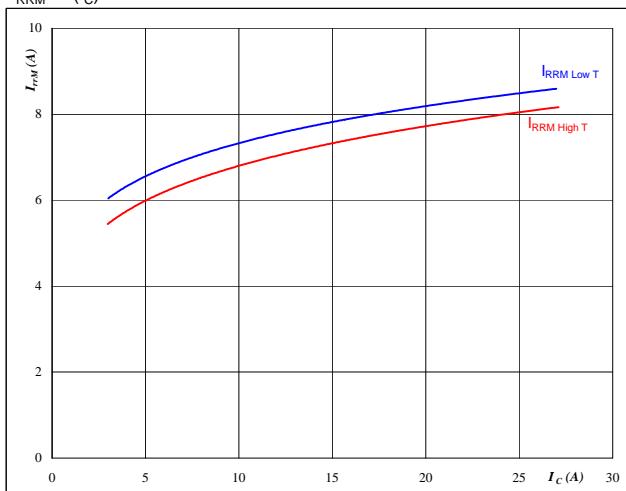
$$I_F = 15 \quad \text{A}$$

$$V_{GS} = +10/0 \quad \text{V}$$

Figure 15
BOOST FWD

Typical reverse recovery current as a function of collector current

$$I_{RRM} = f(I_C)$$


At

$$T_j = 25/125 \quad ^\circ\text{C}$$

$$V_{DS} = 400 \quad \text{V}$$

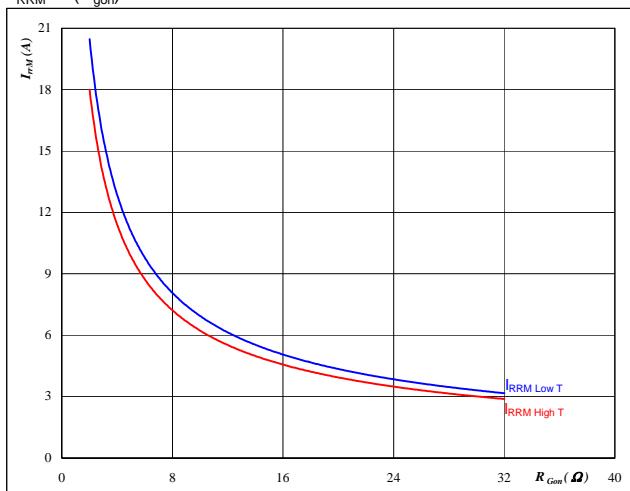
$$V_{GS} = +10/0 \quad \text{V}$$

$$R_{gon} = 8 \quad \Omega$$

Figure 16
BOOST FWD

Typical reverse recovery current as a function of IGBT turn on gate resistor

$$I_{RRM} = f(R_{gon})$$


At

$$T_j = 25/125 \quad ^\circ\text{C}$$

$$V_R = 400 \quad \text{V}$$

$$I_F = 15 \quad \text{A}$$

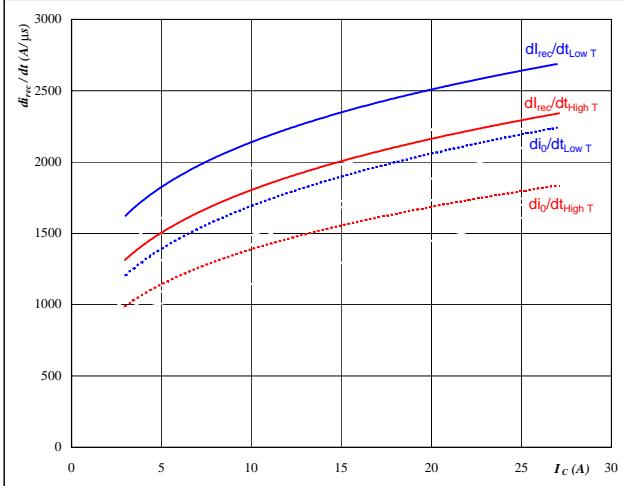
$$V_{GS} = +10/0 \quad \text{V}$$

INPUT BOOST

Figure 17
BOOST FWD

**Typical rate of fall of forward
and reverse recovery current as a
function of collector current**

$$dI_0/dt, dI_{rec}/dt = f(I_C)$$


At

$$T_j = 25/125 \quad {}^\circ\text{C}$$

$$V_{CE} = 400 \quad \text{V}$$

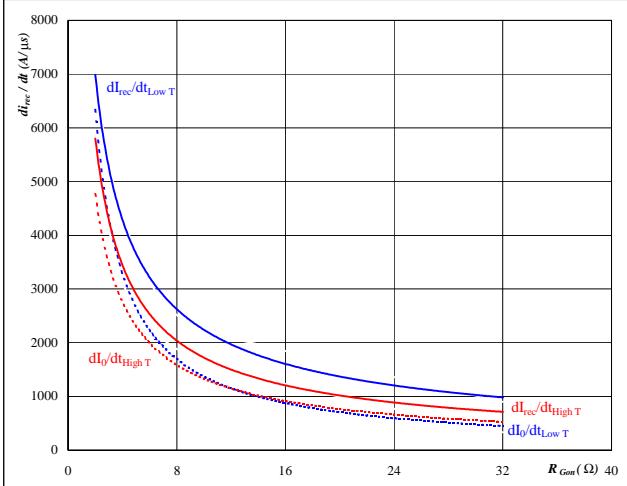
$$V_{GE} = +10/0 \quad \text{V}$$

$$R_{Gon} = 8 \quad \Omega$$

BOOST FWD
Figure 18
BOOST FWD

**Typical rate of fall of forward
and reverse recovery current as a
function of IGBT turn on gate resistor**

$$dI_0/dt, dI_{rec}/dt = f(R_{Gon})$$


At

$$T_j = 25/125 \quad {}^\circ\text{C}$$

$$V_R = 400 \quad \text{V}$$

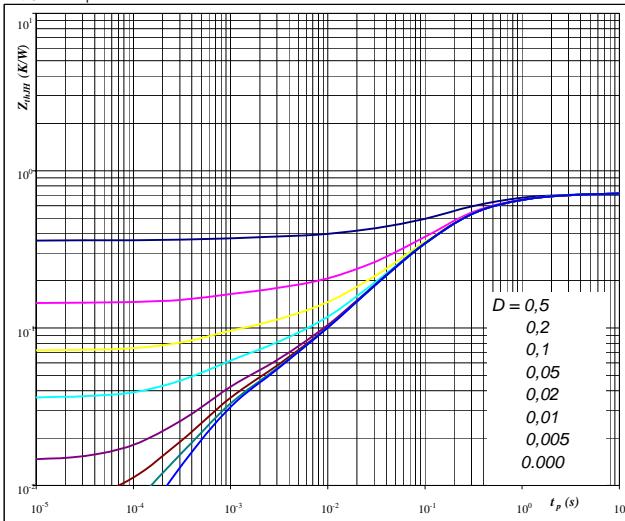
$$I_F = 15 \quad \text{A}$$

$$V_{GS} = +10/0 \quad \text{V}$$

BOOST FWD
Figure 19
BOOST MOSFET

**MOSFET transient thermal impedance
as a function of pulse width**

$$Z_{thJH} = f(t_p)$$


At

$$D = t_p / T$$

$$R_{thJH} = 0,72 \quad \text{K/W}$$

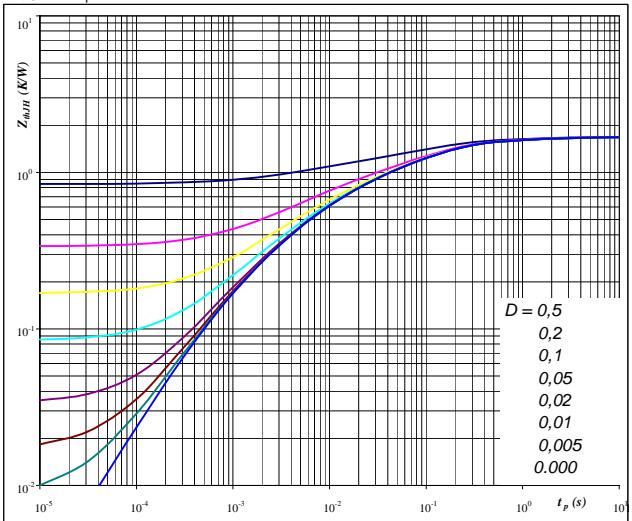
MOSFET thermal model values

R (C/W)	Tau (s)
0,019	8,77E+00
0,106	1,31E+00
0,352	2,19E-01
0,164	6,50E-02
0,049	1,06E-02
0,031	7,41E-04

Figure 20
BOOST FWD

**FWD transient thermal impedance
as a function of pulse width**

$$Z_{thJH} = f(t_p)$$


At

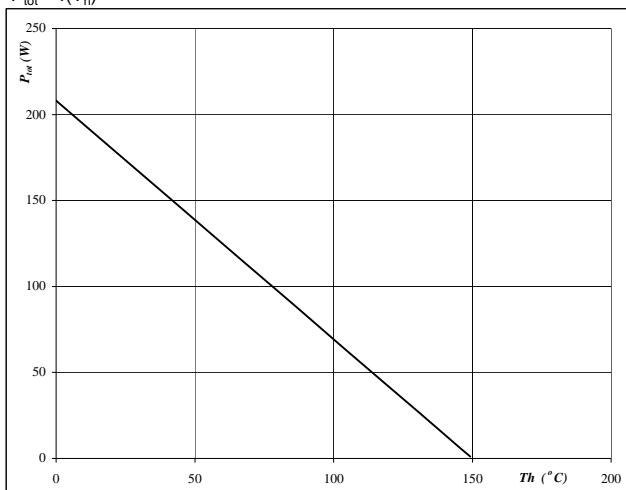
$$D = t_p / T$$

$$R_{thJH} = 1,69 \quad \text{K/W}$$

FWD thermal model values

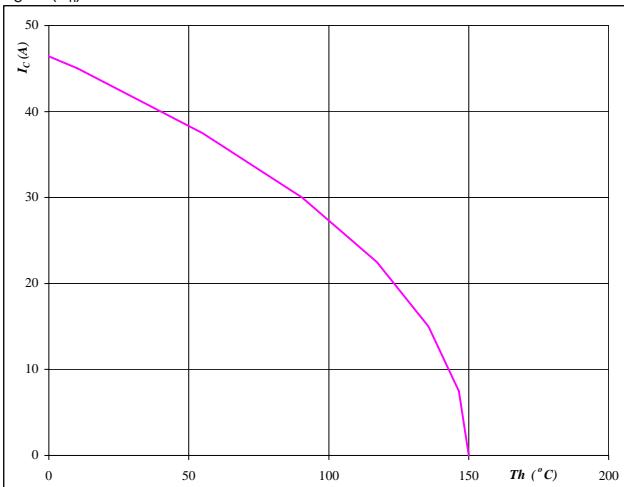
R (C/W)	Tau (s)
0,05	5,64E+00
0,17	6,62E-01
0,59	1,18E-01
0,47	2,15E-02
0,33	3,58E-03
0,07	5,72E-04

INPUT BOOST

Figure 21
BOOST MOSFET
Power dissipation as a function of heatsink temperature
 $P_{tot} = f(T_h)$

At

T_j = 150 °C

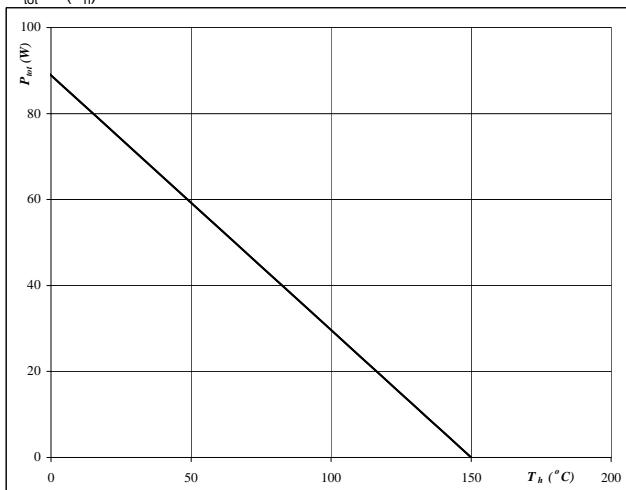
Figure 22
BOOST MOSFET
Collector/Drain current as a function of heatsink temperature
 $I_C = f(T_h)$

I_C = f(T_h)

At

T_j = 150 °C

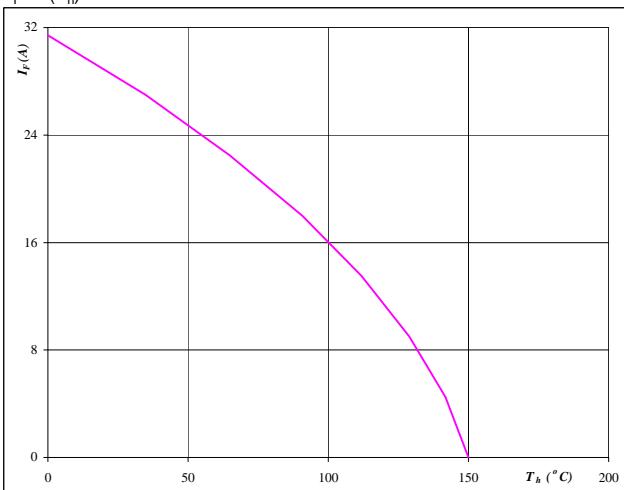
V_{GS} = 10 V

Figure 23
BOOST FWD
Power dissipation as a function of heatsink temperature
 $P_{tot} = f(T_h)$

P_{tot} = f(T_h)

At

T_j = 150 °C

Figure 24
BOOST FWD
Forward current as a function of heatsink temperature
 $I_F = f(T_h)$

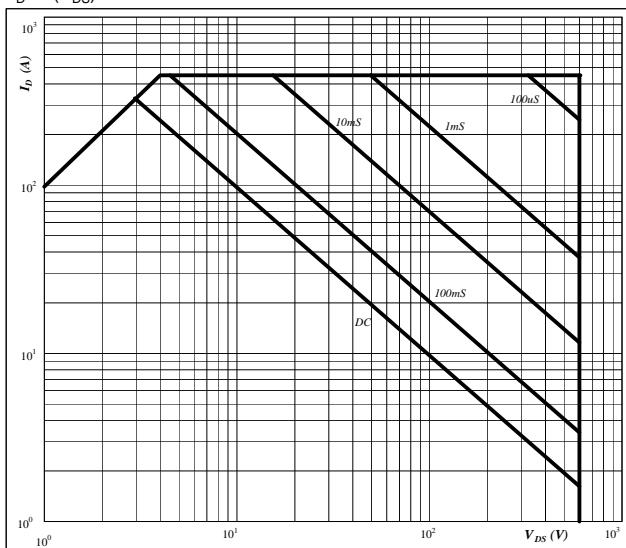
I_F = f(T_h)

At

T_j = 150 °C

INPUT BOOST

Figure 25
BOOST MOSFET
**Safe operating area as a function
of drain-source voltage**

$$I_D = f(V_{DS})$$


At

D = single pulse

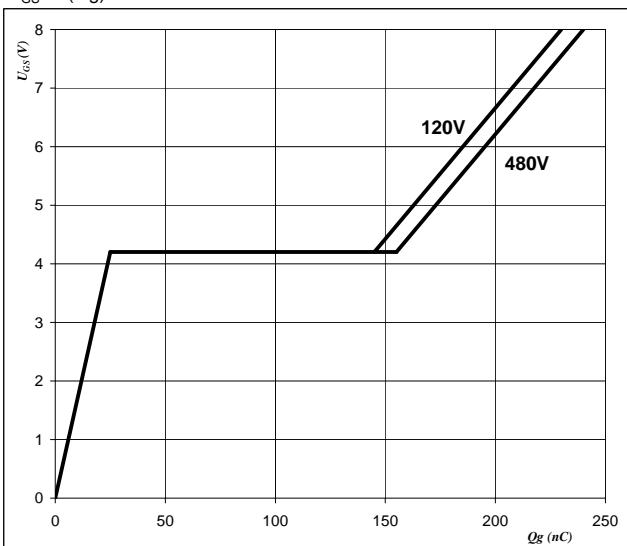
T_h = 80 °C

V_{GS} = +10/0 V

T_j = T_{jmax} °C

Figure 26
BOOST MOSFET
Gate voltage vs Gate charge

$$U_{GS} = f(Qg)$$


At

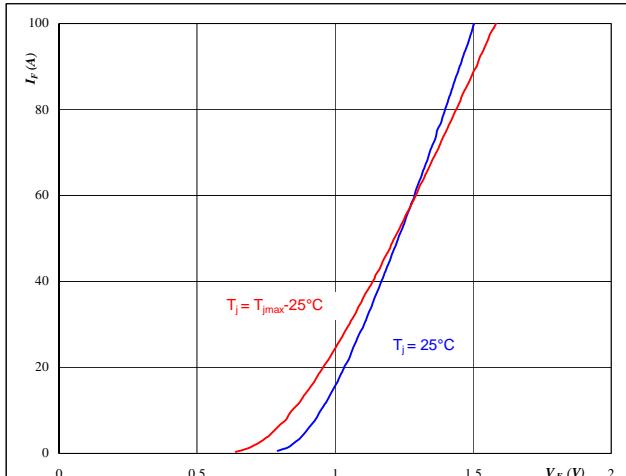
I_D = 15 A

Bypass Diode

Figure 1

Typical diode forward current as a function of forward voltage

$$I_F = f(V_F)$$

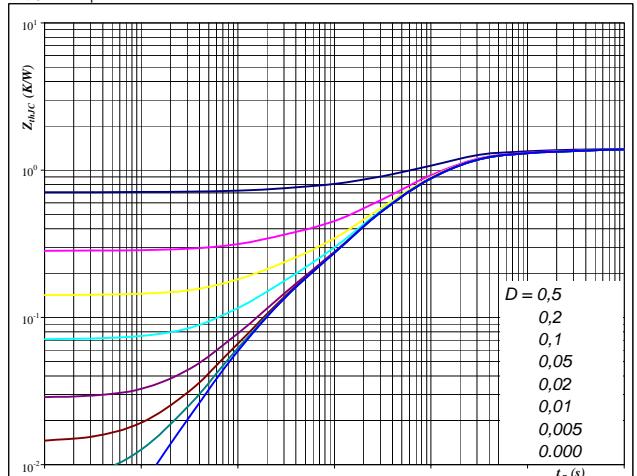

At

$$t_p = 250 \mu\text{s}$$

Bypass diode
Figure 2

Diode transient thermal impedance as a function of pulse width

$$Z_{thJH} = f(t_p)$$


At

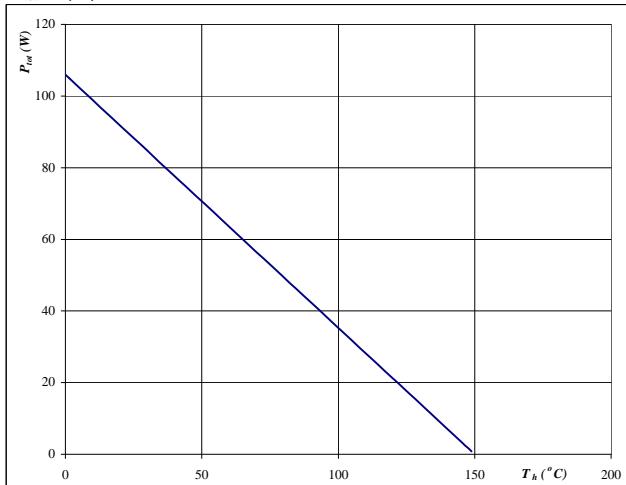
$$D = t_p / T$$

$$R_{thJH} = 1,42 \text{ K/W}$$

Figure 3

Power dissipation as a function of heatsink temperature

$$P_{tot} = f(T_h)$$

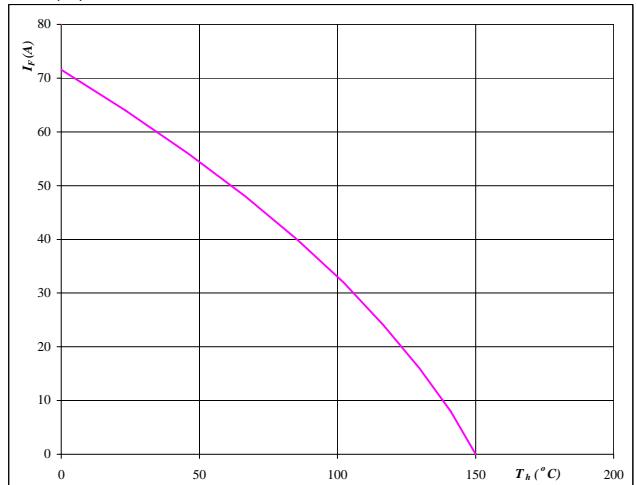

At

$$T_j = 150 ^\circ\text{C}$$

Bypass diode
Figure 4

Forward current as a function of heatsink temperature

$$I_F = f(T_h)$$


At

$$T_j = 150 ^\circ\text{C}$$

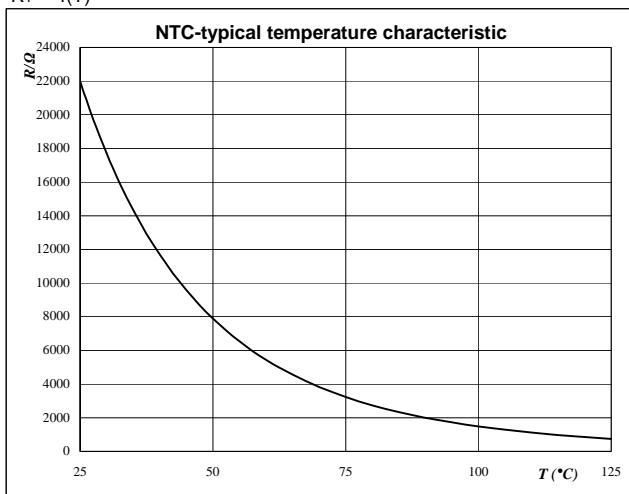
Thermistor

Figure 1

Thermistor

**Typical NTC characteristic
as a function of temperature**

$$R_T = f(T)$$

**Figure 2**

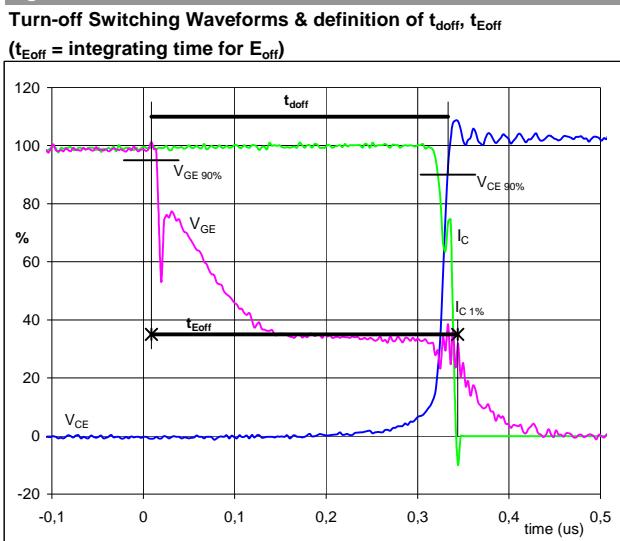
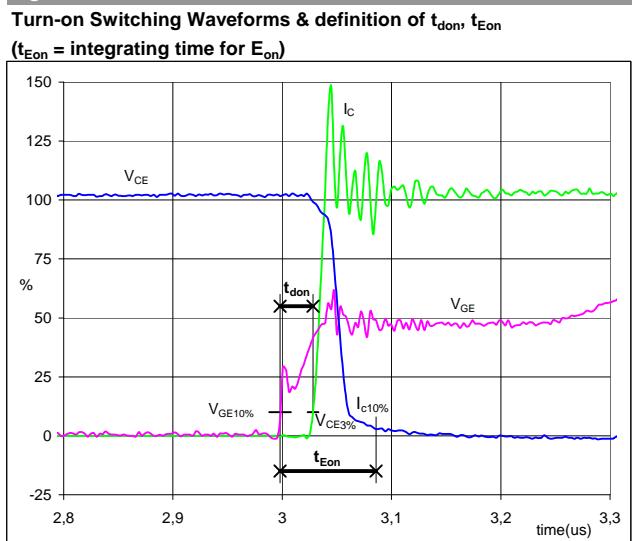
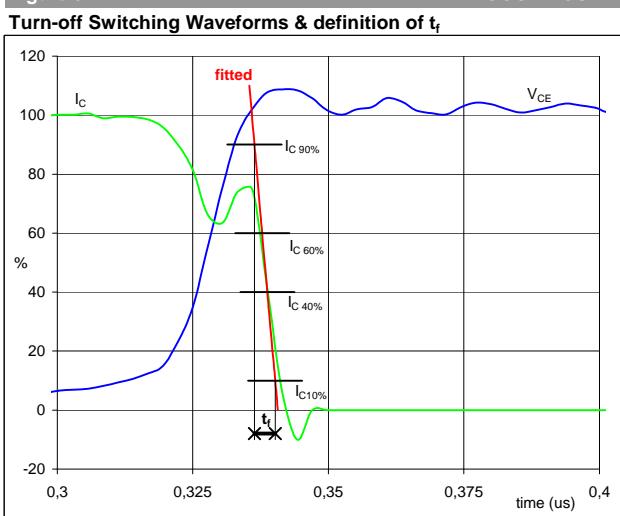
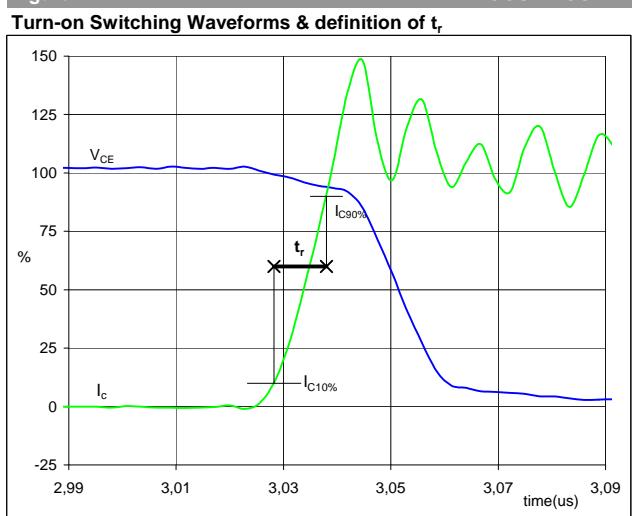
Thermistor

Typical NTC resistance values

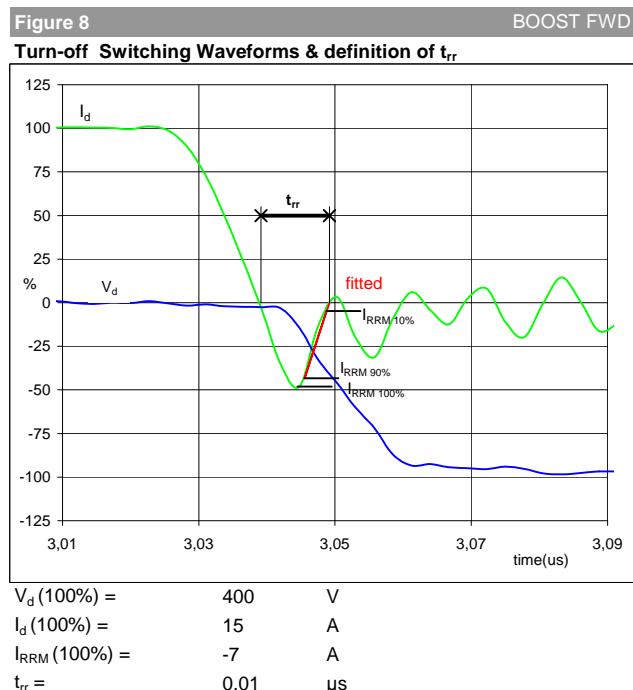
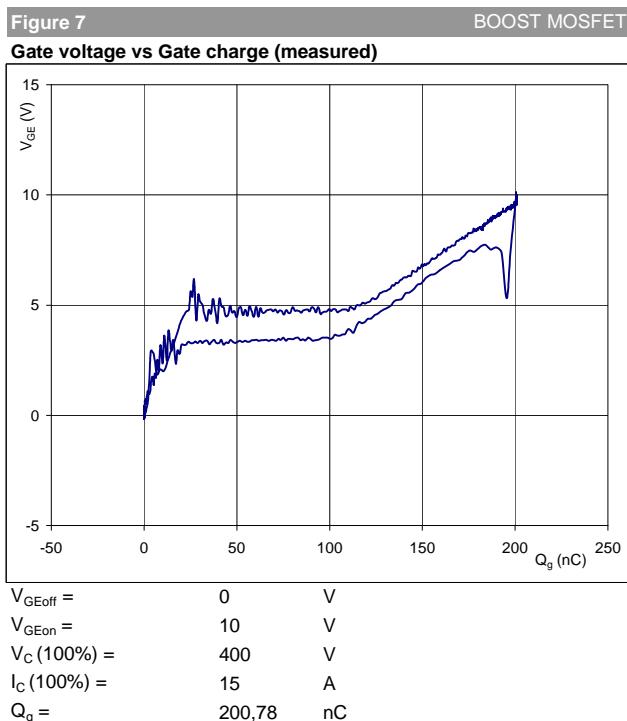
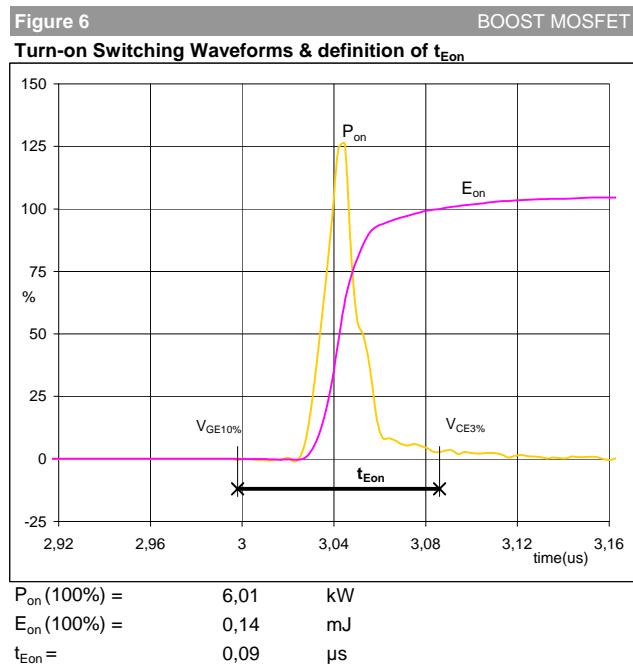
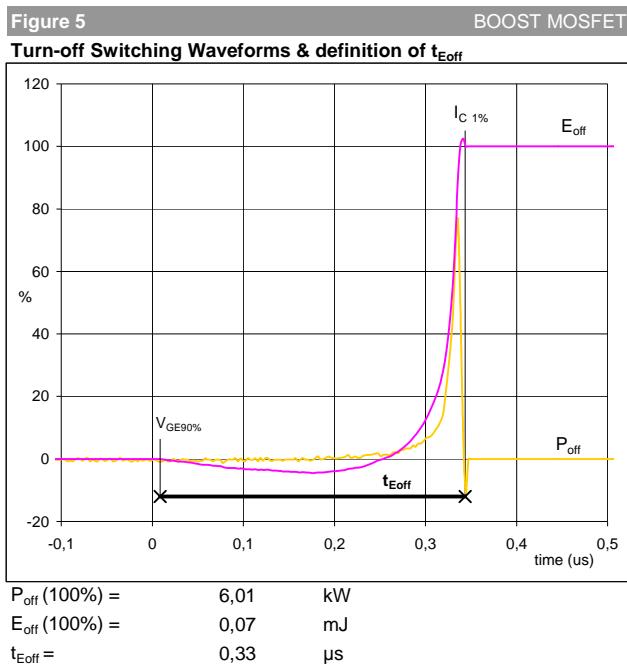
$$R(T) = R_{25} \cdot e^{\left(B_{25/100} \left(\frac{1}{T} - \frac{1}{T_{25}} \right) \right)} \quad [\Omega]$$

Switching Definitions Boost MOSFET

General conditions	
T_j	= 125 °C
R_{gon}	= 8 Ω
R_{goff}	= 8 Ω

Figure 1
BOOST MOSFET

Figure 2
BOOST MOSFET

Figure 3
BOOST MOSFET

Figure 4
BOOST MOSFET


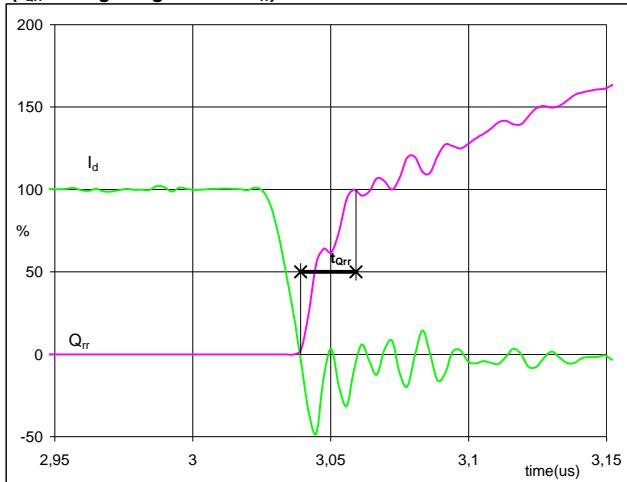
Switching Definitions Boost MOSFET



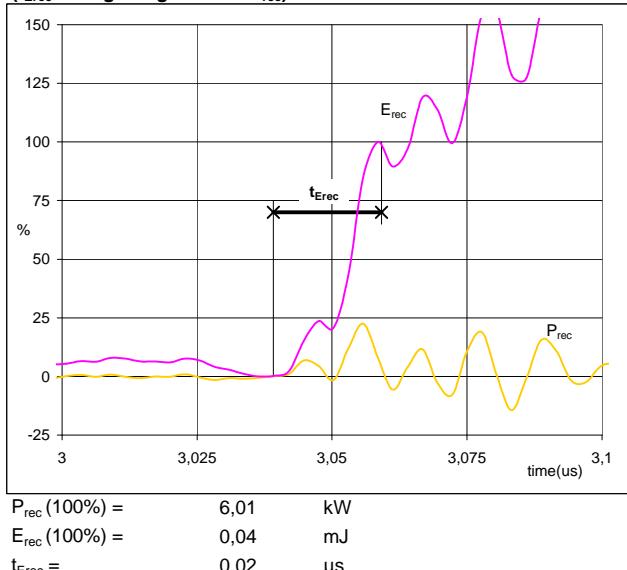
Switching Definitions Boost MOSFET

Figure 9

Turn-on Switching Waveforms & definition of t_{Qrr}
 $(t_{Qrr} = \text{integrating time for } Q_{rr})$

**Figure 10**

Turn-on Switching Waveforms & definition of t_{Erec}
 $(t_{Erec} = \text{integrating time for } E_{rec})$



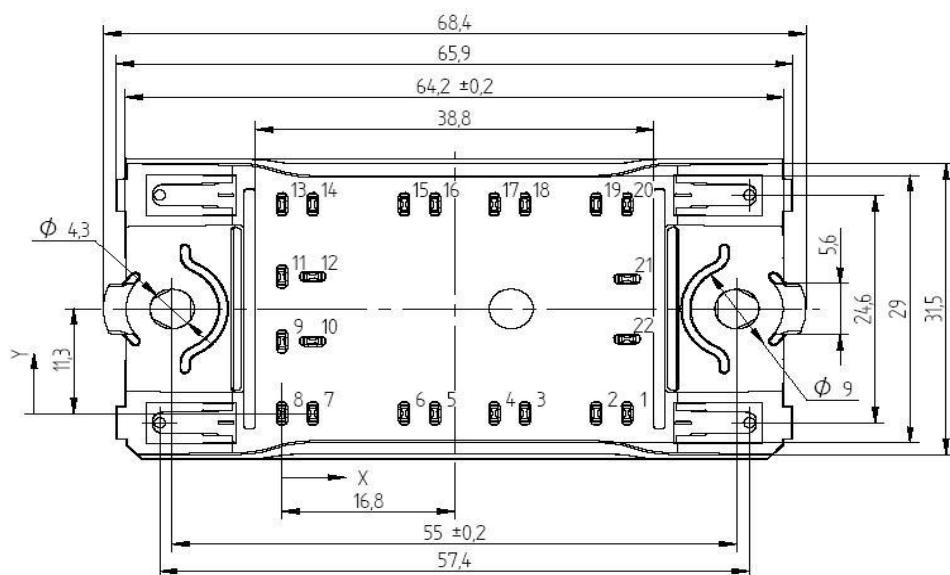
Ordering Code and Marking - Outline - Pinout

Ordering Code & Marking

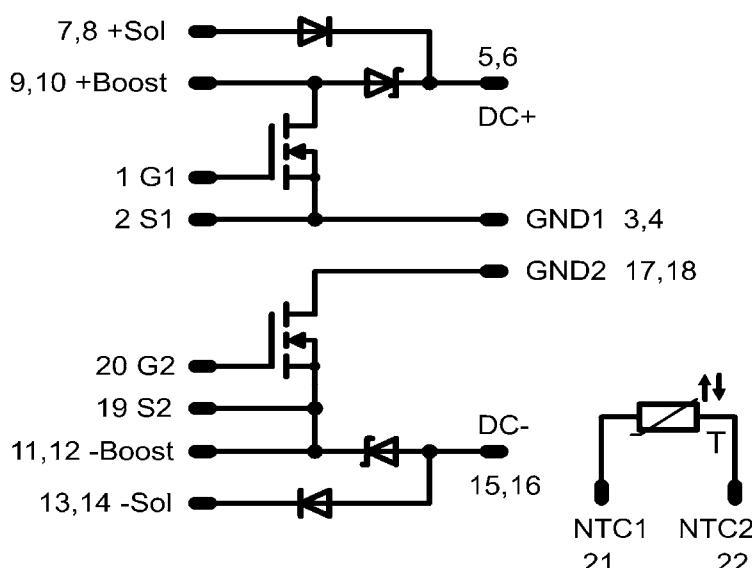
Version	Ordering Code	in DataMatrix as	in packaging barcode as
Standard in flow0 12mm housing	10-PZ06NBA041FS-P915L68Y	P915-L68Y	P915-L68Y

Outline

Pin table		
Pin	X	Y
1	33,6	0
2	30,6	0
3	23,65	0
4	20,65	0
5	14,9	0
6	11,9	0
7	3	0
8	0	0
9	0	7,8
10	3	7,8
11	0	14,8
12	3	14,8
13	0	22,6
14	3	22,6
15	11,9	22,6
16	14,9	22,6
17	20,65	22,6
18	23,65	22,6
19	30,6	22,6
20	33,6	22,6
21	33,6	14,55
22	33,6	8,05



Pinout



PRODUCT STATUS DEFINITIONS

Datasheet Status	Product Status	Definition
Target	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice. The data contained is exclusively intended for technically trained staff.
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