

FEATURES

- Double Side Cooling
- High Surge Capability

APPLICATIONS

- High Power Drives
- High Voltage Power Supplies
- Static Switches

VOLTAGE RATINGS

Part and Ordering Number	Repetitive Peak Voltages V_{DRM} and V_{RRM} V	Conditions
DCR2290V65* DCR2290V60 DCR2290V55	6500 6000 5500	$T_{vj} = -40^{\circ}\text{C}$ to 125°C , $I_{DRM} = I_{RRM} = 300\text{mA}$, $V_{DRM}, V_{RRM} t_p = 10\text{ms}$, $V_{DSM} \text{ \& } V_{RSM} =$ $V_{DRM} \text{ \& } V_{RRM} + 100\text{V}$ respectively

Lower voltage grades available.

* 6200V @ -40°C , 6500V @ 0°C

ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

DCR2290V55

Note: Please use the complete part number when ordering and quote this number in any future correspondence relating to your order.

KEY PARAMETERS

V_{DRM}	6500V
$I_{T(AV)}$	2290A
I_{TSM}	30000A
dV/dt^*	1500V/ μs
dI/dt	500A/ μs

* Higher dV/dt selections available

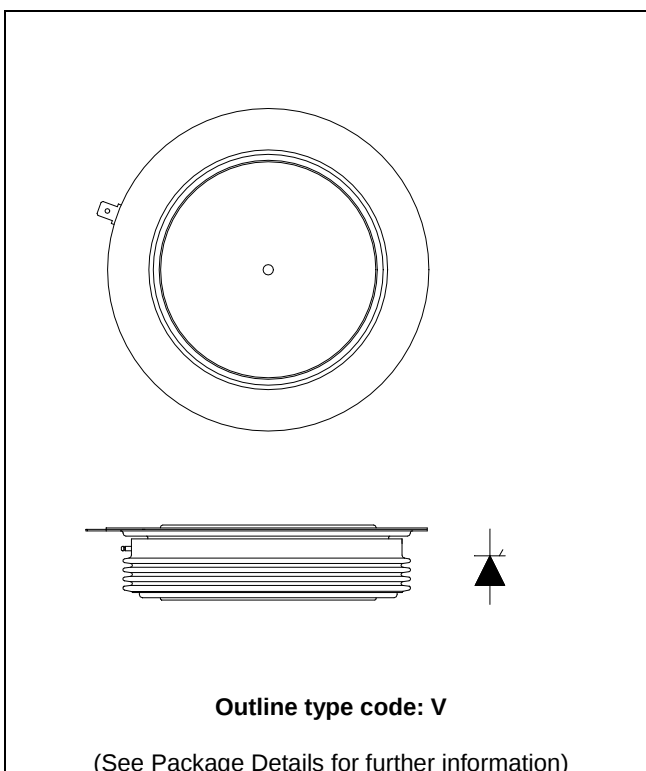


Fig. 1 Package outline

CURRENT RATINGS

$T_{case} = 60^{\circ}\text{C}$ unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
Double Side Cooled				
$I_{T(AV)}$	Mean on-state current	Half wave resistive load	2290	A
$I_{T(RMS)}$	RMS value	-	3597	A
I_T	Continuous (direct) on-state current	-	3520	A

SURGE RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
I_{TSM}	Surge (non-repetitive) on-state current	10ms half sine, $T_{case} = 125^{\circ}\text{C}$	30.0	kA
I^2t	I^2t for fusing	$V_R = 0$	4.50	MA^2s

THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions		Min.	Max.	Units
$R_{th(j-c)}$	Thermal resistance – junction to case	Double side cooled	DC	-	0.00746	$^{\circ}\text{C/W}$
		Single side cooled	Anode DC	-	0.0130	$^{\circ}\text{C/W}$
			Cathode DC	-	0.0178	$^{\circ}\text{C/W}$
$R_{th(c-h)}$	Thermal resistance – case to heatsink	Clamping force 54kN (with mounting compound)	Double side	-	0.002	$^{\circ}\text{C/W}$
			Single side	-	0.004	$^{\circ}\text{C/W}$
T_{vj}	Virtual junction temperature	Blocking V_{DRM} / V_{RRM}		-	125	$^{\circ}\text{C}$
T_{stg}	Storage temperature range			-55	125	$^{\circ}\text{C}$
F_m	Clamping force			48.0	59.0	kN

DYNAMIC CHARACTERISTICS

Symbol	Parameter	Test Conditions		Min.	Max.	Units
I _{RRM} /I _{DRM}	Peak reverse and off-state current	At V _{RRM} /V _{DRM} , T _{case} = 125°C		-	300	mA
dV/dt	Max. linear rate of rise of off-state voltage	To 67% V _{DRM} , T _j = 125°C, gate open		-	1500	V/μs
dI/dt	Rate of rise of on-state current	From 67% V _{DRM} to 2x I _{T(AV)} Gate source 30V, 10Ω, t _r < 0.5μs, T _j = 125°C	Repetitive 50Hz	-	150	A/μs
			Non-repetitive	-	500	A/μs
V _{T(TO)}	Threshold voltage – Low level	200A to 1700A at T _{case} = 125°C		-	1.0	V
	Threshold voltage – High level	1700A to 7000A at T _{case} = 125°C		-	1.237	V
r _T	On-state slope resistance – Low level	200A to 1700A at T _{case} = 125°C		-	0.4286	mΩ
	On-state slope resistance – High level	1700A to 7000A at T _{case} = 125°C		-	0.3518	mΩ
t _{gd}	Delay time	V _D = 67% V _{DRM} , gate source 30V, 10Ω t _r = 0.5μs, T _j = 25°C		-	3	μs
t _q	Turn-off time	T _j = 125°C, V _R = 200V, dI/dt = 1A/μs, dV _{DR} /dt = 20V/μs linear		-	1200	μs
Q _S	Stored charge	T _j = 125°C, dI/dt = 1A/μs, V _{R pk} =3900V, V _{RM} = 2600V		2400	6000	μC
I _L	Latching current	T _j = 25°C, V _D = 5V		-	3	A
I _H	Holding current	T _j = 25°C, R _{G-K} = ∞, I _{TM} = 500A, I _T = 5A		-	300	mA

GATE TRIGGER CHARACTERISTICS AND RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
V_{GT}	Gate trigger voltage	$V_{DRM} = 5V, T_{case} = 25^{\circ}C$	1.5	V
V_{GD}	Gate non-trigger voltage	At $V_{DRM}, T_{case} = 125^{\circ}C$	0.4	V
I_{GT}	Gate trigger current	$V_{DRM} = 5V, T_{case} = 25^{\circ}C$	400	mA
I_{GD}	Gate non-trigger current	$V_{DRM} = 5V, T_{case} = 25^{\circ}C$	15	mA

CURVES

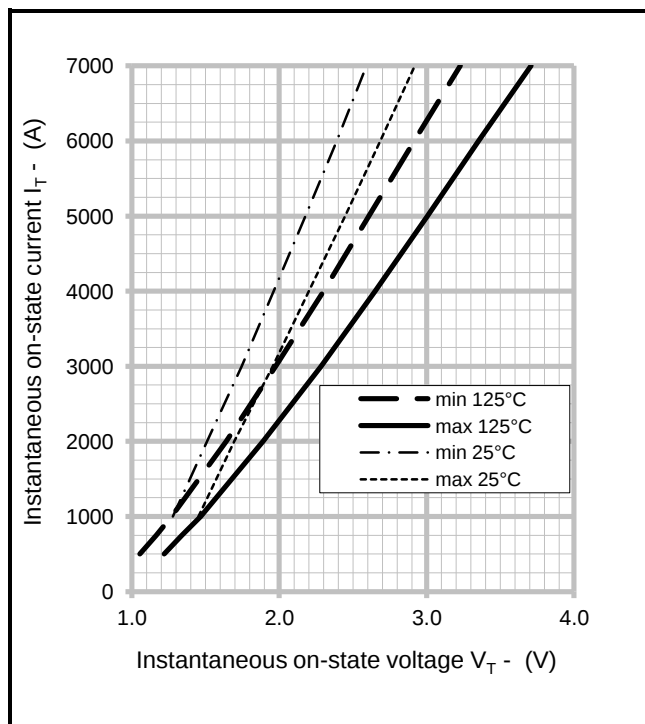


Fig.2 Maximum & minimum on-state characteristics

V_{TM} EQUATION

$$V_{TM} = A + B \ln(I_T) + C \cdot I_T + D \cdot \sqrt{I_T}$$

Where $A = 0.537658$

$B = 0.064222$

$C = 0.000301$

$D = 0.005935$

these values are valid for $T_j = 125^{\circ}C$ for I_T 100A to 7200A

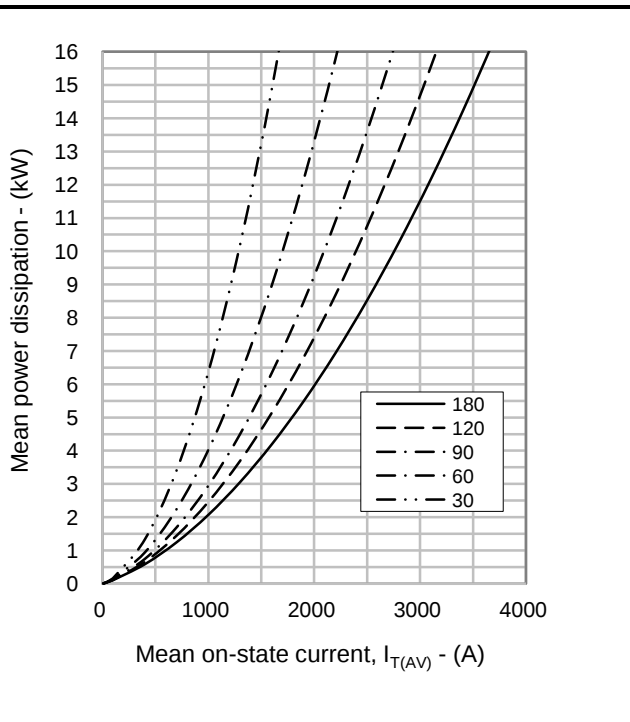


Fig.3 On-state power dissipation – sine wave

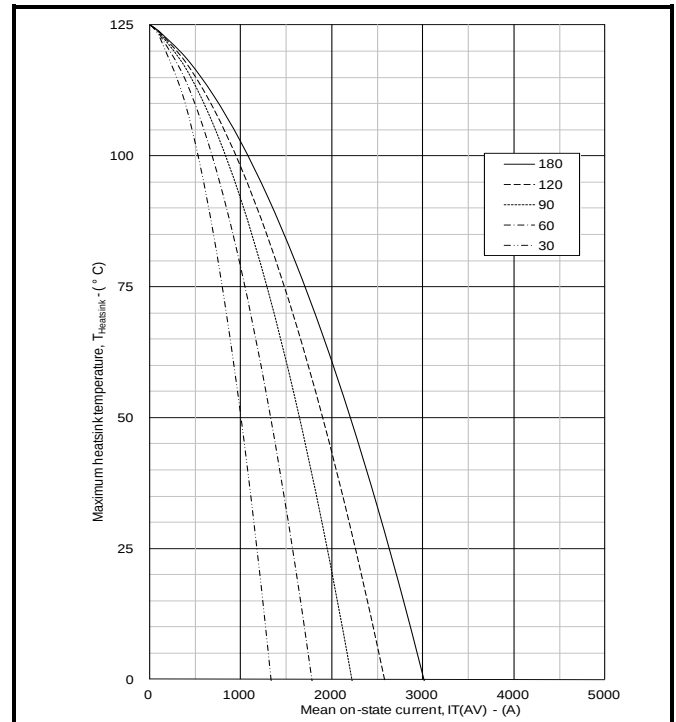


Fig.4 Maximum permissible case temperature, double side cooled – sine wave

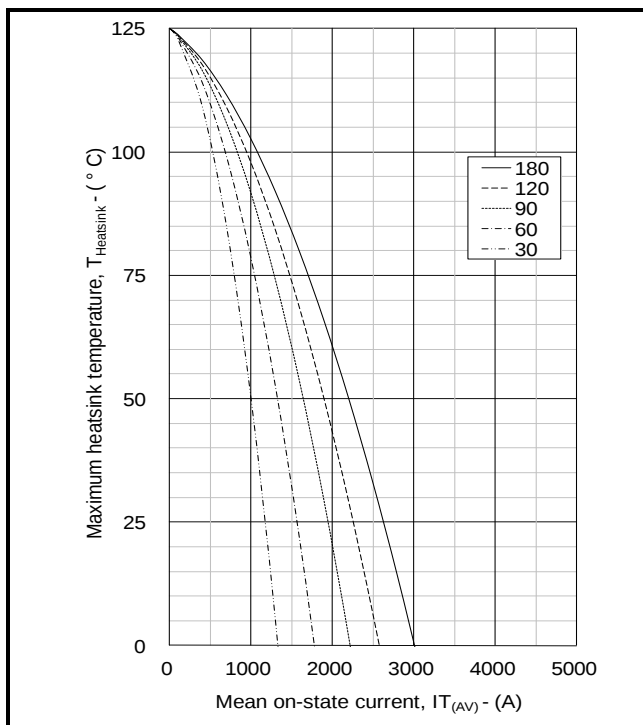


Fig.5 Maximum permissible heatsink temperature, double side cooled – sine wave

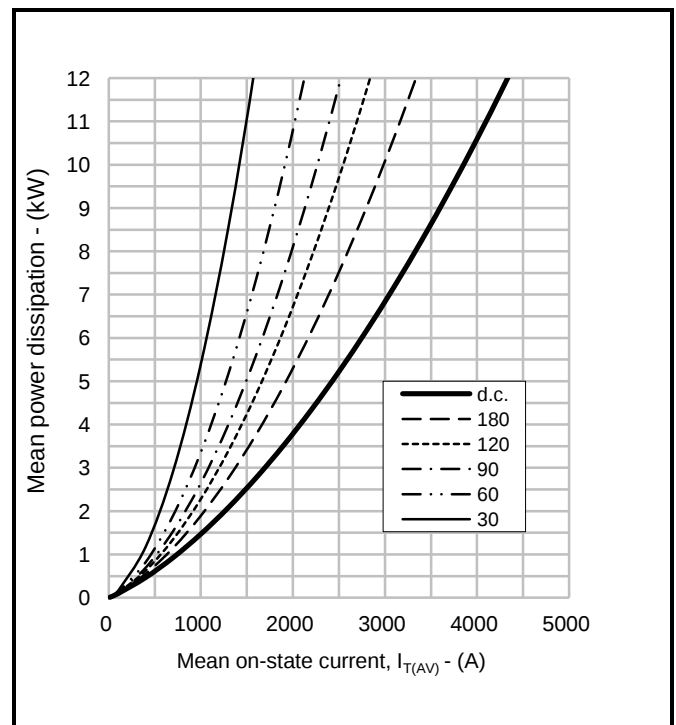


Fig.6 On-state power dissipation – rectangular wave

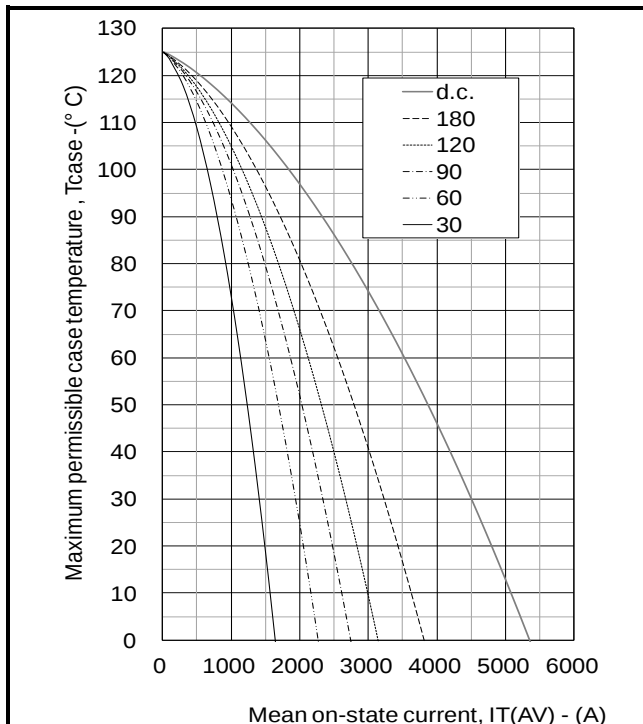


Fig.7 Maximum permissible case temperature, double side cooled – rectangular wave

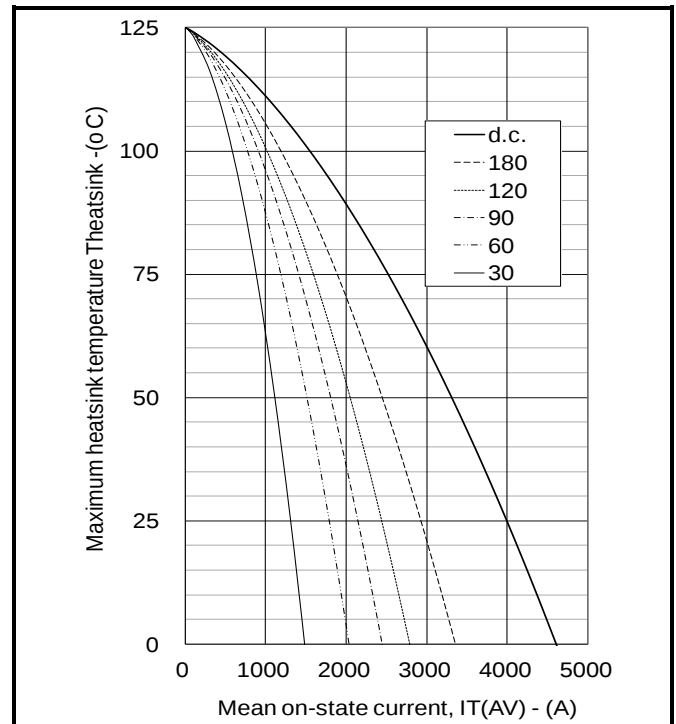


Fig.8 Maximum permissible heatsink temperature, double side cooled – rectangular wave

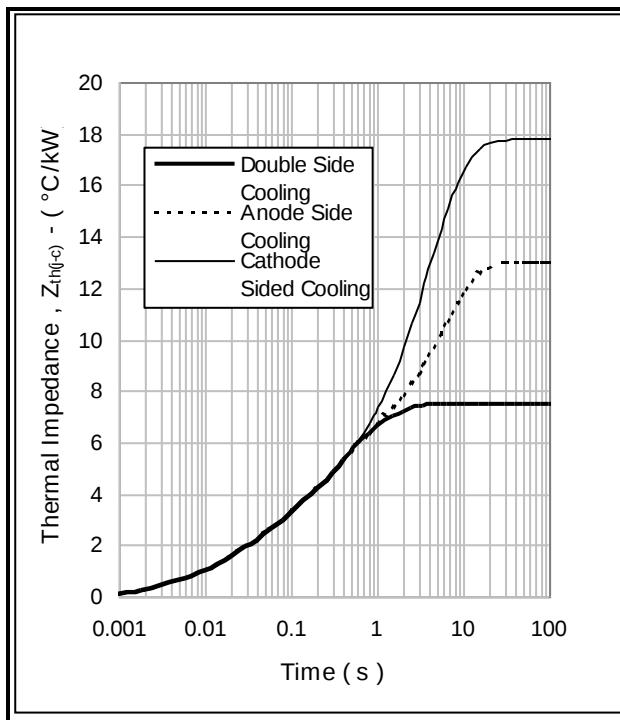


Fig.9 Maximum (limit) transient thermal impedance – junction to case (°C/kW)

		1	2	3	4
Double side cooled	R_i (°C/kW)	0.9206	1.8299	3.4022	1.3044
	T_i (s)	0.0076807	0.0579454	0.4078613	1.2085
Anode side cooled	R_i (°C/kW)	0.9032	1.6719	3.0101	7.4269
	T_i (s)	0.0075871	0.0536531	0.3144537	5.624
Cathode side cooled	R_i (°C/kW)	0.9478	2.0661	1.6884	13.0847
	T_i (s)	0.0078442	0.0645541	0.3894389	4.1447

$$Z_{th} = \sum_{i=1}^{i=4} [R_i \times (1 - \exp(-T/T_i))]$$

$\Delta R_{th(j-c)}$ Conduction

Tables show the increments of thermal resistance $R_{th(j-c)}$ when the device operates at conduction angles other than d.c.

Double side cooling			Anode Side Cooling			Cathode Sided Cooling		
θ°	$\Delta Z_{th} (z)$		θ°	$\Delta Z_{th} (z)$		θ°	$\Delta Z_{th} (z)$	
	sine.	rect.		sine.	rect.		sine.	rect.
180	1.34	0.88	180	1.34	0.88	180	1.33	0.88
120	1.57	1.30	120	1.57	1.30	120	1.57	1.29
90	1.83	1.54	90	1.84	1.54	90	1.83	1.53
60	2.08	1.81	60	2.08	1.81	60	2.07	1.80
30	2.27	2.11	30	2.28	2.11	30	2.26	2.10
15	2.36	2.28	15	2.37	2.28	15	2.35	2.26

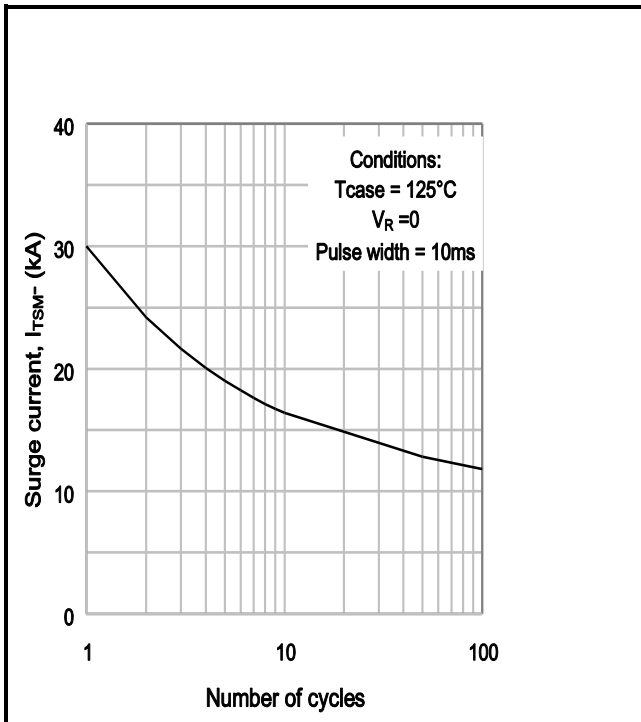


Fig.10 Multi-cycle surge current

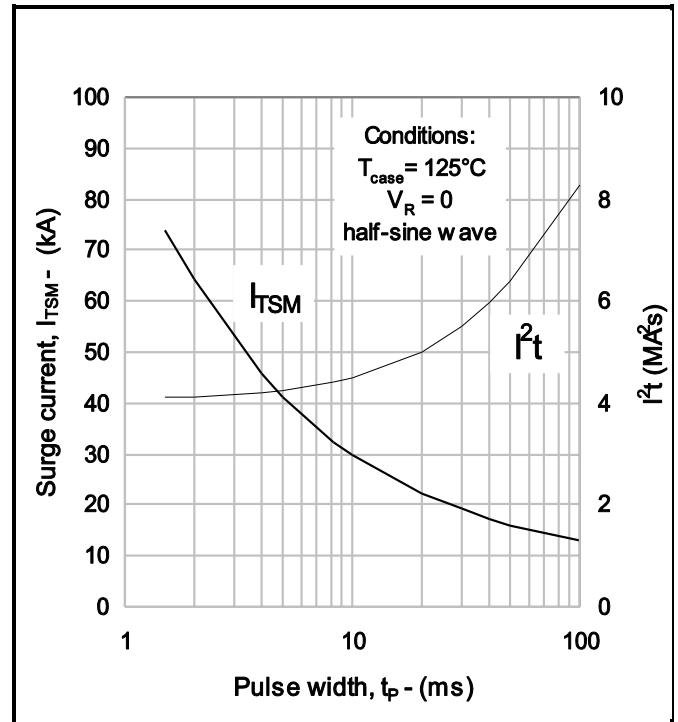


Fig.11 Single-cycle surge current

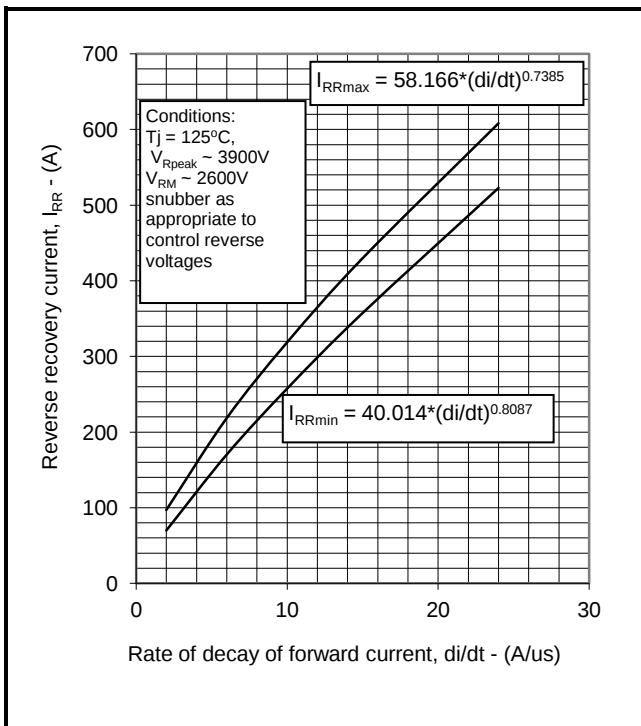


Fig. 12 Stored Charge

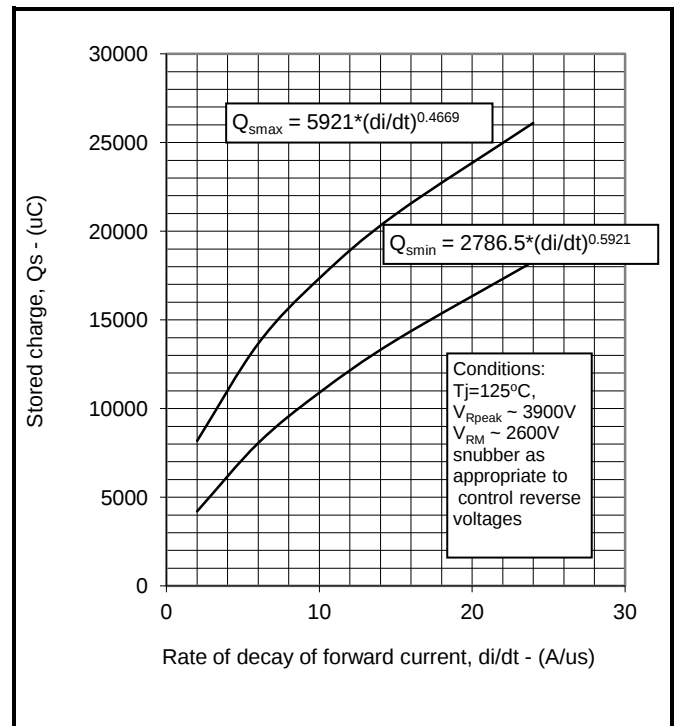


Fig. 13 Reverse Recovery Current

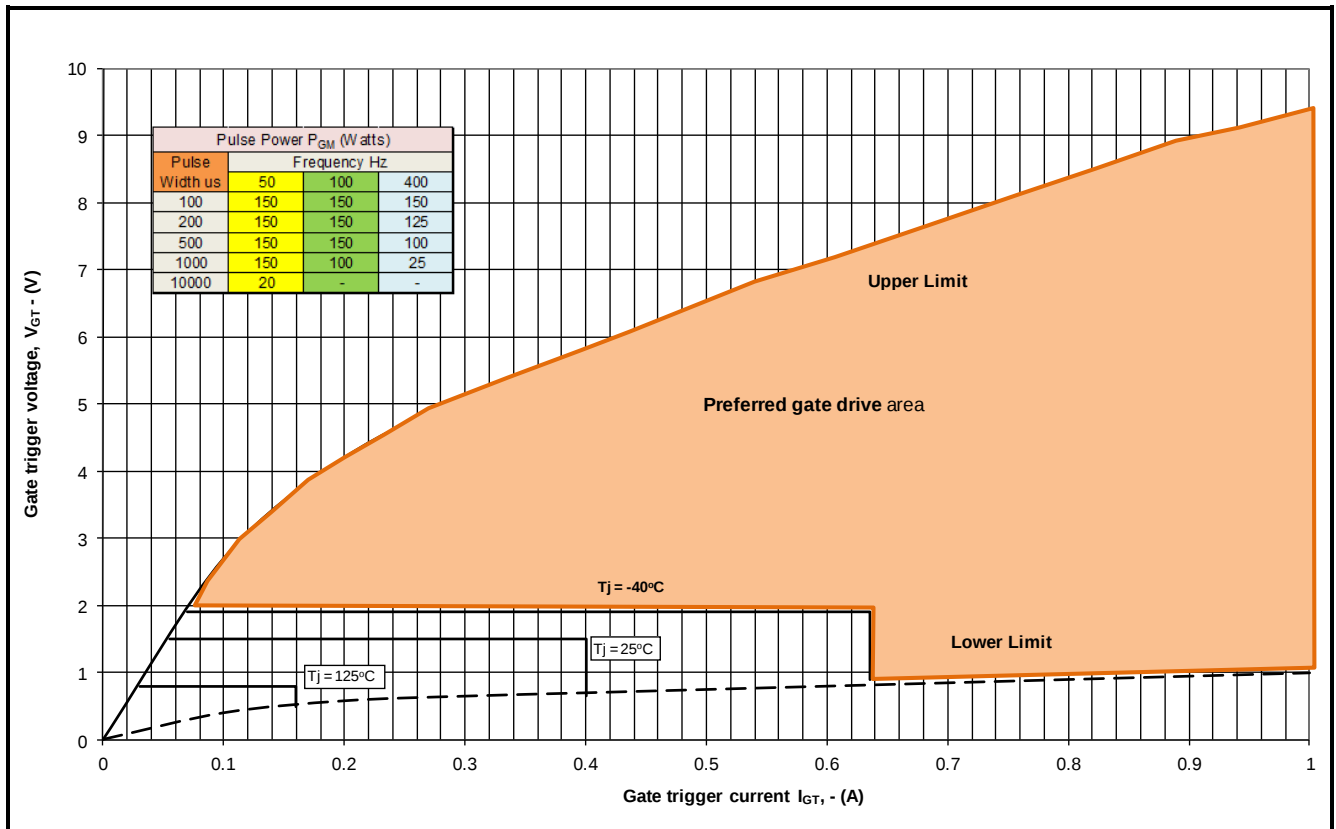


Fig14 Gate Characteristics

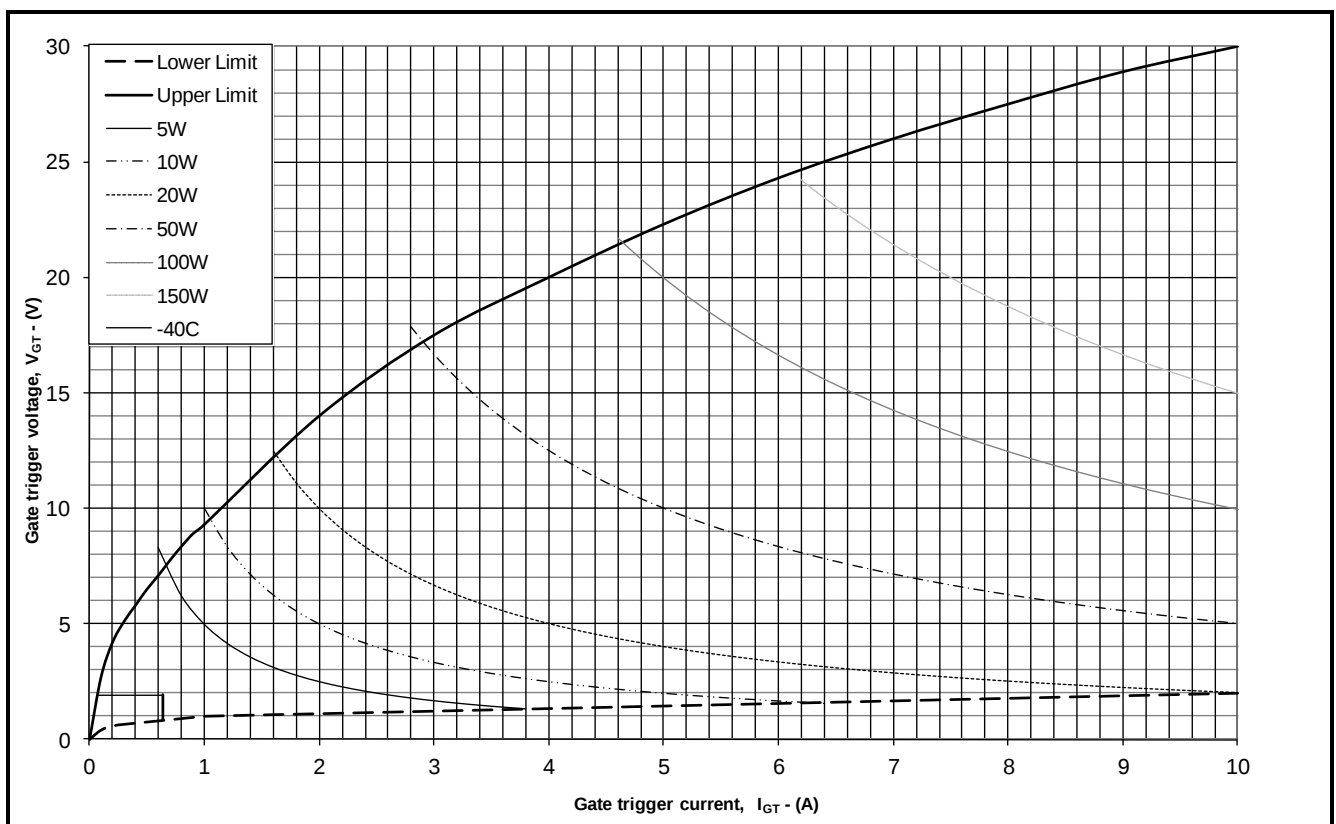
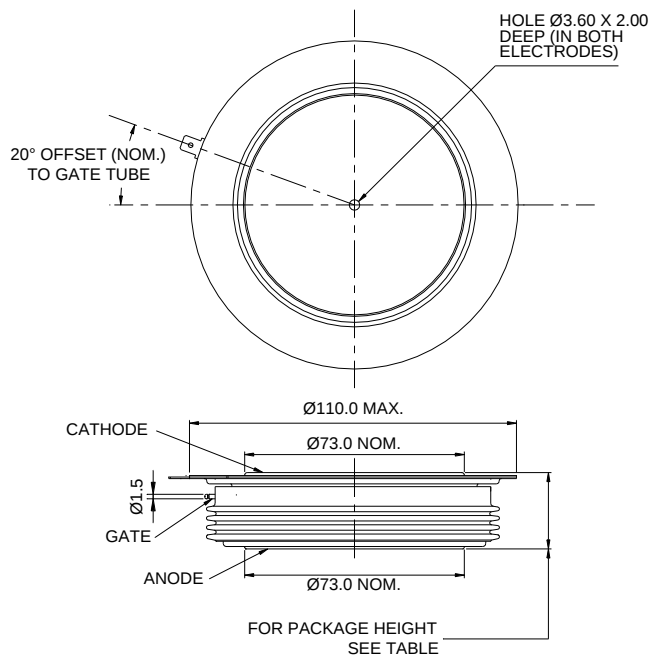


Fig. 15 Gate characteristics

PACKAGE DETAILS

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.

3rd ANGLE PROJECTION  DO NOT SCALE IF IN DOUBT ASK



Device	Maximum Thickness (mm)	Minimum Thickness (mm)
DCR1910V85	28.31	27.56
DCR2290V65	27.95	27.2
DCR2720V52	27.69	26.94
DCR3030V42	27.57	26.82
DCR3780V28	27.34	26.59
DCR4060V22	27.265	26.515
DCR4940V18	27.16	26.43

Lead length: 420mm
Lead terminal connector: M4 ring

Package outline type code: V

Fig.16 Package outline

IMPORTANT INFORMATION:

This publication is provided for information only and not for resale.

The products and information in this publication are intended for use by appropriately trained technical personnel.

Due to the diversity of product applications, the information contained herein is provided as a general guide only and does not constitute any guarantee of suitability for use in a specific application. The user must evaluate the suitability of the product and the completeness of the product data for the application. The user is responsible for product selection and ensuring all safety and any warning requirements are met. Should additional product information be needed please contact Customer Service.

Although we have endeavoured to carefully compile the information in this publication it may contain inaccuracies or typographical errors. The information is provided without any warranty or guarantee of any kind.

This publication is an uncontrolled document and is subject to change without notice. When referring to it please ensure that it is the most up to date version and has not been superseded.

The products are not intended for use in applications where a failure or malfunction may cause loss of life, injury or damage to property. The user must ensure that appropriate safety precautions are taken to prevent or mitigate the consequences of a product failure or malfunction.

The products must not be touched when operating because there is a danger of electrocution or severe burning. Always use protective safety equipment such as appropriate shields for the product and wear safety glasses. Even when disconnected any electric charge remaining in the product must be discharged and allowed to cool before safe handling using protective gloves.

Extended exposure to conditions outside the product ratings may affect reliability leading to premature product failure. Use outside the product ratings is likely to cause permanent damage to the product. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture, a large current to flow or high voltage arcing, resulting in fire or explosion. Appropriate application design and safety precautions should always be followed to protect persons and property.

Product Status & Product Ordering:

We annotate datasheets in the top right hand corner of the front page, to indicate product status if it is not yet fully approved for production. The annotations are as follows:-

Target Information:

This is the most tentative form of information and represents a very preliminary specification. No actual design work on the product has been started.

Preliminary Information:

The product design is complete and final characterisation for volume production is in progress. The datasheet represents the product as it is now understood but details may change.

No Annotation:

The product has been approved for production and unless otherwise notified by Dynex any product ordered will be supplied to the **current version of the data sheet prevailing at the time of our order acknowledgement.**

All products and materials are sold and services provided subject to Dynex's conditions of sale, which are available on request.

Any brand names and product names used in this publication are trademarks, registered trademarks or trade names of their respective owners.

HEADQUARTERS OPERATIONS

DYNEX SEMICONDUCTOR LIMITED
Doddington Road, Lincoln, Lincolnshire, LN6 3LF
United Kingdom.
Phone: +44 (0) 1522 500500
Fax: +44 (0) 1522 500550
Web: <http://www.dynexsemi.com>

CUSTOMER SERVICE

Phone: +44 (0) 1522 502753 / 502901
Fax: +44 (0) 1522 500020
e-mail: power_solutions@dynexsemi.com