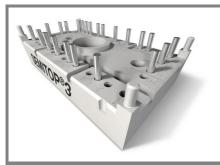
SK 40 DH



SEMITOP® 3

Half Controlled Bridge Rectifier

SK 40 DH

Preliminary Data

Features

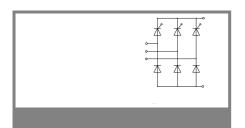
- · Compact design
- · One screw mounting
- Heat transfer and insolation through direct copper bonded aluminium oxide ceramic (DBC)
- Glass passived thyristor chips
- Up to 1600V reverse voltage
- UL recognized, file no. E 63 532

Typical Applications

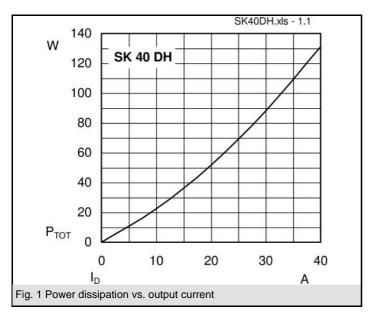
- Soft starters
- Light control
- Temperature control
- Motor control

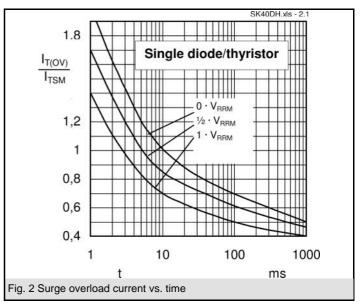
V _{RSM}	V _{RRM} , V _{DRM}	I _D = 42 A (full conduction) (T _s = 80 °C)
900 1300	800 1200	SK 40 DH 08 SK 40 DH 12
1700	1600	SK 40 DH 16

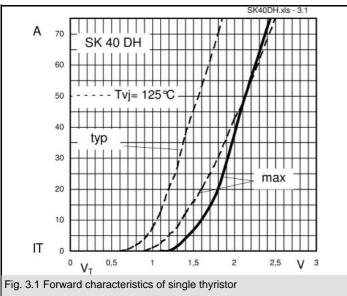
Symbol	Conditions	Values	Units
I _D	T _s = 80 °C	42	Α
I _{FSM} / I _{TSM}	T _{vi} = 25 °C; 10 ms	370	Α
10111	T _{vi} = 125 °C; 10 ms	270	Α
i²t	T _{vi} = 25 °C; 10 ms	685	A²s
	T _{vj} = 125 °C; 10 ms	365	A²s
V _T	T _{vi} = 25 °C; 75A	max. 2,45	V
V _{T(TO)}	T _{vi} = 125 °C;	max. 1,1	V
r _T	T _{vi} = 125 °C	max. 20	mΩ
$I_{DD}; I_{RD}$	T_{vj} = 125 °C; $V_{DD} = V_{DRM}$; $V_{RD} = V_{RRM}$	max. 8	mA
t _{gd}	$T_{vj} = {^{\circ}C}; I_G = A; di_G/dt = A/\mu s$		μs
t_{gr}	$V_D = \cdot V_{DRM}$		μs
(dv/dt) _{cr}	T _{vi} = 125 °C	max. 1000	V/µs
(di/dt) _{cr}	T _{vj} = 125 °C; f = 5060 Hz	max. 50	A/µs
t_q	$T_{vj} = 125 ^{\circ}\text{C}$; typ.	120	μs
I _H	T _{vj} = 25 °C; typ. / max.	80 / 150	mA
I_L	T_{vj} = 25 °C; R_G = 33 Ω	150 / 300	mA
V _{GT}	$T_{vj} = 25 ^{\circ}\text{C}; \text{d.c.}$	min. 2	V
I_{GT}	$T_{vi} = 25 ^{\circ}\text{C}; \text{d.c.}$	min. 100	mA
V_{GD}	$T_{vj} = 125 ^{\circ}\text{C}; \text{d.c.}$	max. 0,25	V
I_{GD}	$T_{vj} = 125 ^{\circ}\text{C}; \text{d.c.}$	max. 3	mA
Rth(j-s)	Per thyristor	1,7	K/W
	Per diode	1,7	K/W
T _{solder}	Terminals, 10s	260	°C
T_{vj}	Diodes	-40+150	°C
T_{vj}			°C
T _{stg}		-40+125	°C
T_{vj}	Thyristors	-40+125	°C
V _{isol}	a. c. 50 Hz; r.m.s.; 1 s / 1 min.	3000 (2500)	V
M_s	Mounting torque to heatsink	2,5	Nm
m	weight	30	g
Case	SEMITOP® 3	T 39	

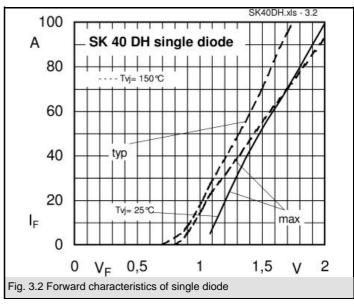


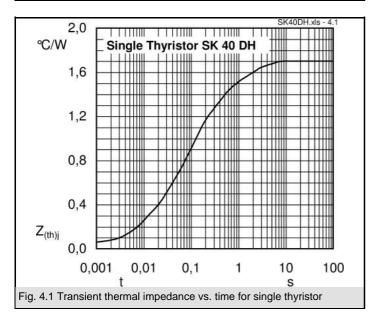
SK 40 DH

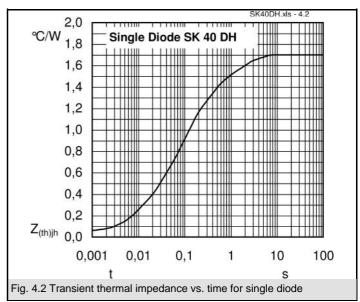


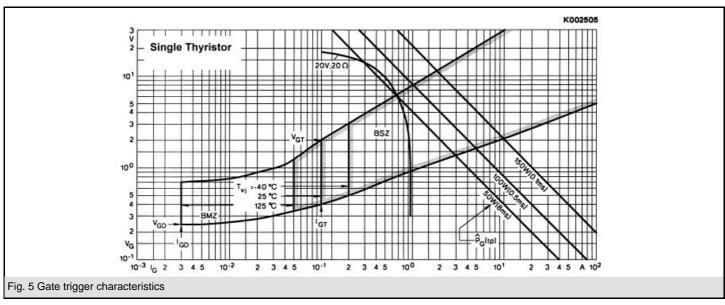


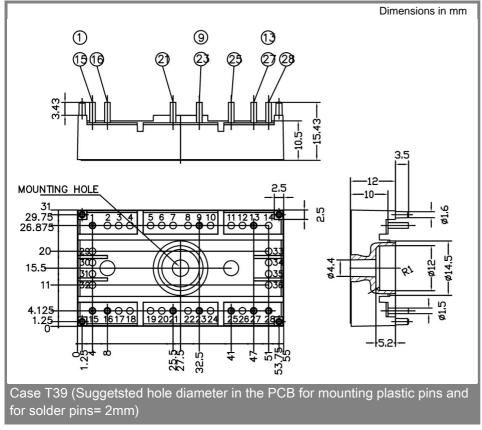


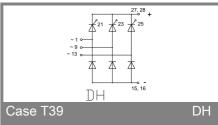












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