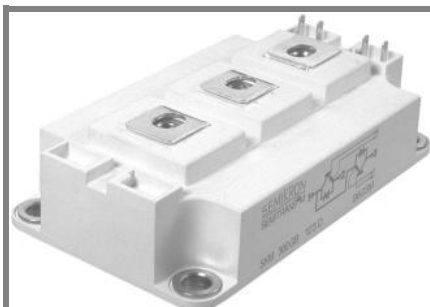


SKM 300GB126D



SEMITRANS® 3

Trench IGBT Module

SKM 300GB126D

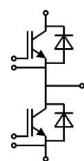
Preliminary Data

Features

- Trench = Trenchgate technology
- V_{CEsat} with positive temperature coefficient
- High short circuit capability, self limiting to $6 \times I_C$

Typical Applications

- Electronic welders
- AC inverter drives
- UPS

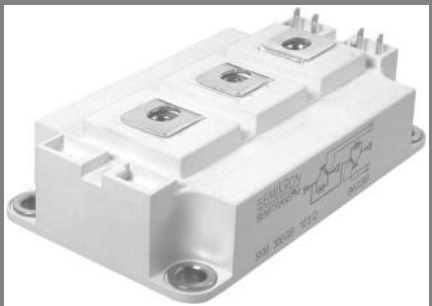


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Absolute Maximum Ratings			T _c = 25 °C, unless otherwise specified	
Symbol	Conditions		Values	Units
IGBT				
V _{CES}	T _j = 25 °C		1200	V
I _C	T _j = 150 °C	T _{case} = 25 °C	310	A
		T _{case} = 80 °C	200	A
I _{CRM}	I _{CRM} =2xI _{Cnom}		400	A
V _{GES}			± 20	V
t _{psc}	V _{CC} = 600 V; V _{GE} ≤ 20 V; T _j = 125 °C V _{CES} < 1200 V		10	µs
Inverse Diode				
I _F	T _j = 150 °C	T _{case} = 25 °C	250	A
		T _{case} = 80 °C	170	A
I _{FRM}	I _{FRM} =2xI _{Fnom}		400	A
Module				
I _{t(RMS)}			500	A
T _{vj}			- 40 ... + 150	°C
T _{stg}			-40....+125	°C
V _{isol}	AC, 1 min.		4000	V

Characteristics			T _c = 25 °C, unless otherwise specified			
Symbol	Conditions		min.	typ.	max.	Units
IGBT						
V _{GE(th)}	V _{GE} = V _{CE} , I _C = 8 mA		5	5,8	6,5	V
I _{CES}	V _{GE} = 0 V, V _{CE} = V _{CES} T _j = 25 °C			0,1	0,3	mA
V _{CE0}	T _j = 25 °C T _j = 125 °C			1	1,2	V
				0,9	1,1	V
r _{CE}	V _{GE} = 15 V T _j = 25°C T _j = 125°C			3,5	4,7	mΩ
				5,5	6,8	mΩ
V _{CE(sat)}	I _{Cnom} = 200 A, V _{GE} = 15 V T _j = 25°C _{chiplev.} T _j = 125°C _{chiplev.}			1,7	2,15	V
				2	2,45	V
C _{ies}	V _{CE} = 25, V _{GE} = 0 V f = 1 MHz			15		nF
C _{oes}				1,2		nF
C _{res}				1,1		nF
Q _G	V _{GE} = -8V - +20V			1800		nC
R _{Gint}	T _j = 25 °C			3,8		Ω
t _{d(on)}	R _{Gon} = 1,5 Ω V _{CC} = 600V I _C = 200A			280		ns
t _r				37		ns
E _{on}	R _{Goff} = 1,5 Ω T _j = 125 °C V _{GE} = ± 15V			21		mJ
t _{d(off)}				560		ns
t _f				100		ns
E _{off}				33		mJ
R _{th(j-c)}	per IGBT				0,12	K/W

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- UPS



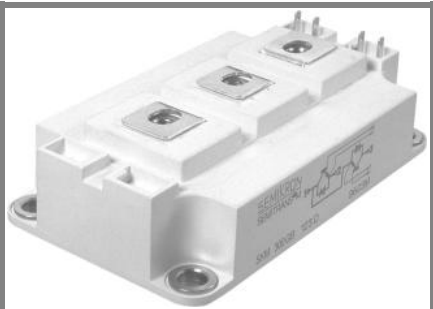
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Characteristics						
Symbol	Conditions		min.	typ.	max.	Units
Inverse diode						
V _F = V _{EC}	I _{Fnom} = 200 A; V _{GE} = 0 V	T _j = 25 °C _{chiplev.}		1,6	1,8	V
		T _j = 125 °C _{chiplev.}		1,6	1,8	V
V _{F0}		T _j = 25 °C		1	1,1	V
		T _j = 125 °C		0,8	0,9	V
r _F		T _j = 25 °C		3	3,5	mΩ
		T _j = 125 °C		4	4,5	mΩ
I _{RRM}	I _F = 200 A	T _j = 125 °C		290		A
Q _{rr}	di/dt = 6200 A/μs			44		μC
E _{rr}	V _{GE} = -15 V; V _{CC} = 600 V			18		mJ
R _{th(j-c)D}	per diode				0,25	K/W
Module						
L _{CE}				15	20	nH
R _{CC'+EE'}	res., terminal-chip	T _{case} = 25 °C		0,35		mΩ
		T _{case} = 125 °C		0,5		mΩ
R _{th(c-s)}	per module				0,038	K/W
M _s	to heat sink M6			3	5	Nm
M _t	to terminals M6			2,5	5	Nm
w					325	g

This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, Chapter IX.

This technical information specifies semiconductor devices but promises no characteristics. No warranty or guarantee expressed or implied is made regarding delivery, performance or suitability.

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Z_{th}			
Symbol	Conditions	Values	Units
$Z_{th(j-c)I}$			
R_i	$i = 1$	80	mk/W
R_i	$i = 2$	30	mk/W
R_i	$i = 3$	8,5	mk/W
R_i	$i = 4$	1,5	mk/W
τ_{u_i}	$i = 1$	0,0576	s
τ_{u_i}	$i = 2$	0,01	s
τ_{u_i}	$i = 3$	0,002	s
τ_{u_i}	$i = 4$	0,0002	s
$Z_{th(j-c)D}$			
R_i	$i = 1$	150	mk/W
R_i	$i = 2$	75	mk/W
R_i	$i = 3$	22	mk/W
R_i	$i = 4$	3	mk/W
τ_{u_i}	$i = 1$	0,0331	s
τ_{u_i}	$i = 2$	0,0113	s
τ_{u_i}	$i = 3$	0,0012	s
τ_{u_i}	$i = 4$	0,001	s



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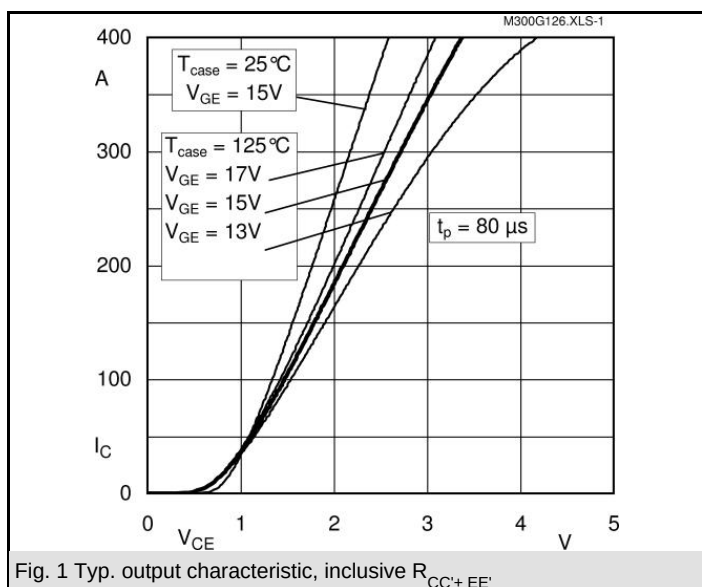


Fig. 1 Typ. output characteristic, inclusive $R_{CC'+EE'}$

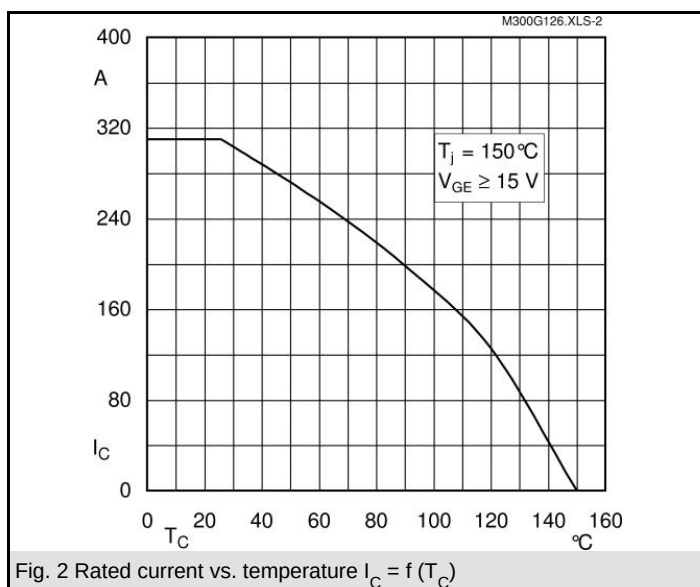


Fig. 2 Rated current vs. temperature $I_C = f(T_C)$

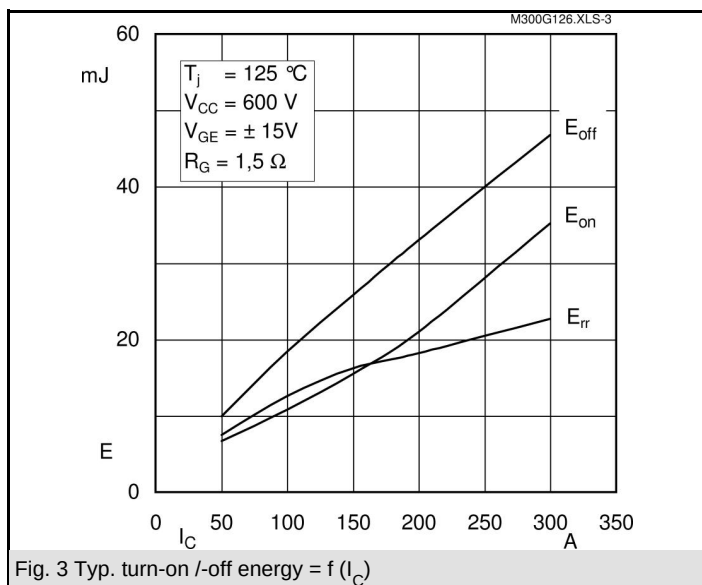


Fig. 3 Typ. turn-on /off energy = $f(I_C)$

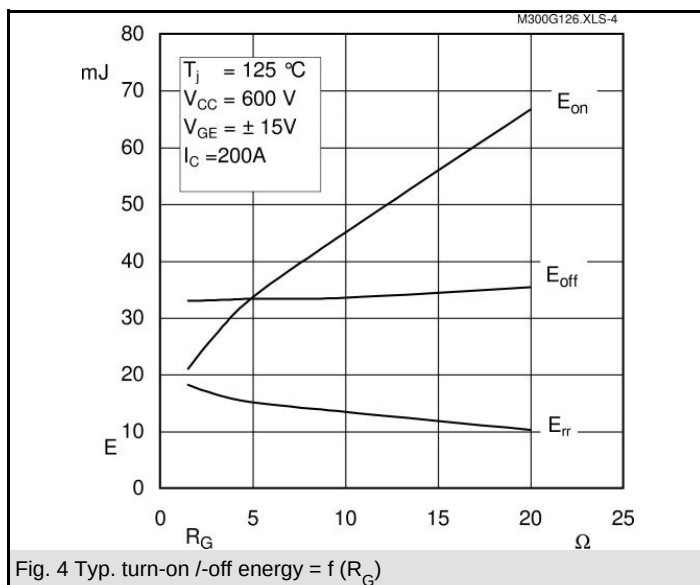


Fig. 4 Typ. turn-on /off energy = $f(R_G)$

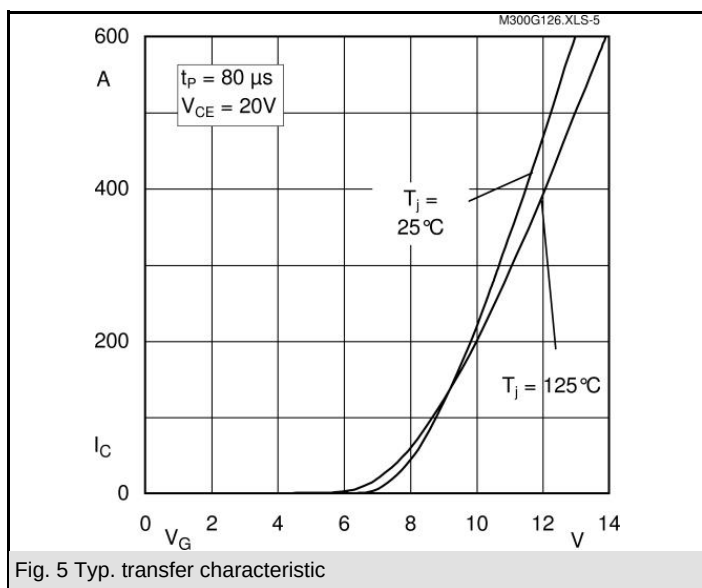


Fig. 5 Typ. transfer characteristic

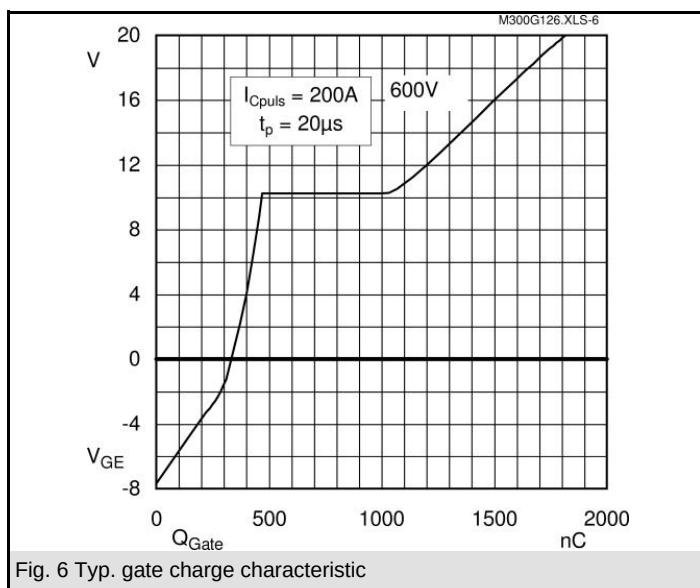


Fig. 6 Typ. gate charge characteristic

