

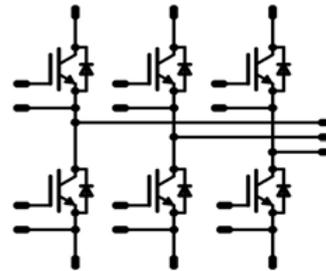
flowPACK 2 3rd gen
1200V/150A
Features

- High power flow2 housing
- Trench Fieldstop Technology IGBT4
- Compact and low inductive design

flow2 housing

Target Applications

- Motor Drive
- Power Generation
- UPS

Schematic

Types

- V23990-P680-F

Maximum Ratings

 $T_j=25^{\circ}\text{C}$, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
Inverter Transistor				
Collector-emitter break down voltage	V_{CE}		1200	V
DC collector current	I_C	$T_j=T_{j,max}$ $T_h=80^{\circ}\text{C}$ $T_c=80^{\circ}\text{C}$	134 150	A
Repetitive peak collector current	$I_{C,pulse}$	t_p limited by $T_{j,max}$	450	A
Power dissipation per IGBT	P_{tot}	$T_j=T_{j,max}$ $T_h=80^{\circ}\text{C}$ $T_c=80^{\circ}\text{C}$	313 475	W
Gate-emitter peak voltage	V_{GE}		± 20	V
Short circuit ratings	t_{SC} V_{CC}	$T_j \leq 150^{\circ}\text{C}$ $V_{GE} = 15\text{V}$	10 800	μs V
Maximum Junction Temperature	$T_{j,max}$		175	$^{\circ}\text{C}$
Inverter Diode				
Peak Repetitive Reverse Voltage	V_{RRM}	$T_j=25^{\circ}\text{C}$	1200	V
DC forward current	I_F	$T_j=T_{j,max}$ $T_h=80^{\circ}\text{C}$ $T_c=80^{\circ}\text{C}$	110 145	A
Repetitive peak forward current	I_{FRM}	t_p limited by $T_{j,max}$	300	A
Power dissipation per Diode	P_{tot}	$T_j=T_{j,max}$ $T_h=80^{\circ}\text{C}$ $T_c=80^{\circ}\text{C}$	189 287	W
Maximum Junction Temperature	$T_{j,max}$		175	$^{\circ}\text{C}$
Thermal Properties				
Storage temperature	T_{stg}		-40...+125	$^{\circ}\text{C}$
Operation temperature under switching condition	T_{op}		-40...+($T_{j,max} - 25$)	$^{\circ}\text{C}$

Maximum Ratings

T_j=25°C, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
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Insulation Properties

Insulation voltage	V _{is}	t=2s DC voltage	4000	V
Creepage distance			min 12,7	mm
Clearance			min 12,7	mm

Characteristic Values

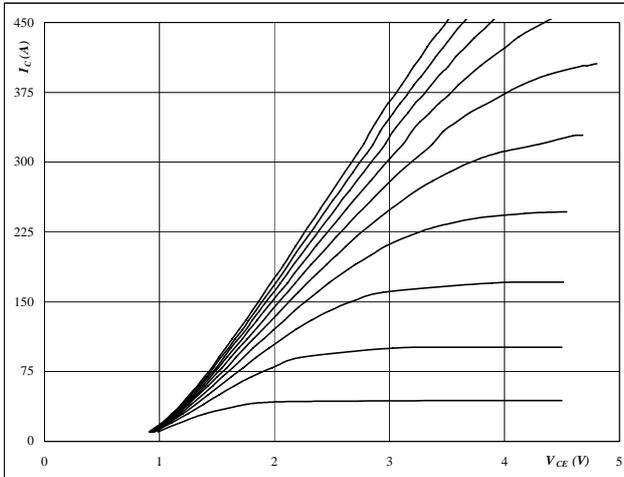
Parameter	Symbol	Conditions					Value			Unit
		V_{GE} [V] or V_{GS} [V]	V_r [V] or V_{CE} [V] or V_{DS} [V]	I_c [A] or I_F [A] or I_b [A]	T_j	Min	Typ	Max		
Inverter Transistor										
Gate emitter threshold voltage	$V_{GE(th)}$	$V_{CE}=V_{GE}$			0.006	$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	5	5.8	6.5	V
Collector-emitter saturation voltage	$V_{CE(sat)}$		15		150	$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	1.5	1.85	2.5	V
Collector-emitter cut-off current incl. Diode	I_{CES}		0	1200		$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$			0.04	mA
Gate-emitter leakage current	I_{GES}		20	0		$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$			700	nA
Integrated Gate resistor	R_{gint}							5		Ω
Turn-on delay time	$t_{d(on)}$	$R_{goff}=4\ \Omega$ $R_{gon}=4\ \Omega$	± 15	600	150	$T_j=25^\circ\text{C}$		213		ns
Rise time	t_r					$T_j=150^\circ\text{C}$		229		
Turn-off delay time	$t_{d(off)}$					$T_j=25^\circ\text{C}$		35		
Fall time	t_f					$T_j=150^\circ\text{C}$		44		
Turn-on energy loss per pulse	E_{on}					$T_j=25^\circ\text{C}$		326		
Turn-off energy loss per pulse	E_{off}					$T_j=150^\circ\text{C}$		410		
Input capacitance	C_{ies}							8800		pF
Output capacitance	C_{oss}	$f=1\text{MHz}$	0	25		$T_j=25^\circ\text{C}$		580		
Reverse transfer capacitance	C_{riss}							470		
Gate charge	Q_{Gate}	$V_{CC}=960\text{V}$	± 15		150	$T_j=25^\circ\text{C}$		750		nC
Thermal resistance chip to heatsink per chip	R_{thJH}	Thermal grease thickness $\leq 50\mu\text{m}$						0.30		KW
Thermal resistance chip to case per chip	R_{thJC}	$\lambda = 1\ \text{W/mK}$						0.20		
Inverter Diode										
Diode forward voltage	V_F				150	$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	1.3	1.94 1.98	2.5	V
Peak reverse recovery current	I_{RRM}	$R_{gon}=4\ \Omega$	± 15	600	150	$T_j=25^\circ\text{C}$		143		A
Reverse recovery time	t_{rr}					$T_j=150^\circ\text{C}$		168		
Reverse recovered charge	Q_{rr}					$T_j=25^\circ\text{C}$		287		
Peak rate of fall of recovery current	$di(rec)_{max}/dt$					$T_j=150^\circ\text{C}$		465		
Reverse recovered energy	E_{rec}					$T_j=25^\circ\text{C}$		15.56		
						$T_j=150^\circ\text{C}$		29.16		
Thermal resistance chip to heatsink per chip	R_{thJH}	Thermal grease thickness $\leq 50\mu\text{m}$						0.50		KW
Thermal resistance chip to case per chip	R_{thJC}	$\lambda = 1\ \text{W/mK}$						0.33		

Output Inverter

Figure 1 Output inverter IGBT

Typical output characteristics

$$I_C = f(V_{CE})$$

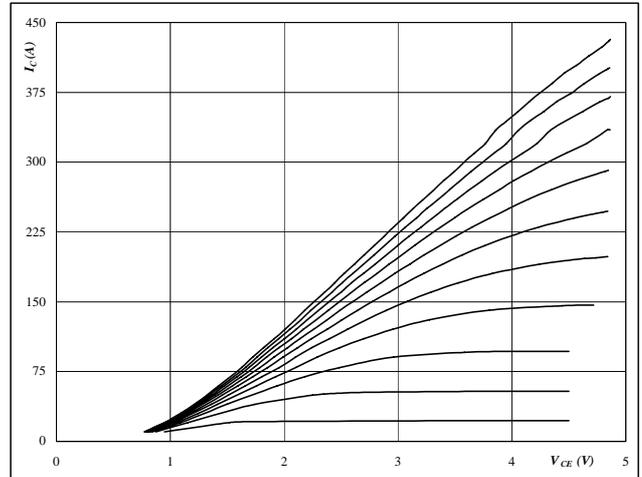


At
 $t_p = 250 \mu s$
 $T_j = 25 \text{ } ^\circ C$
 V_{GE} from 7 V to 17 V in steps of 1 V

Figure 2 Output inverter IGBT

Typical output characteristics

$$I_C = f(V_{CE})$$

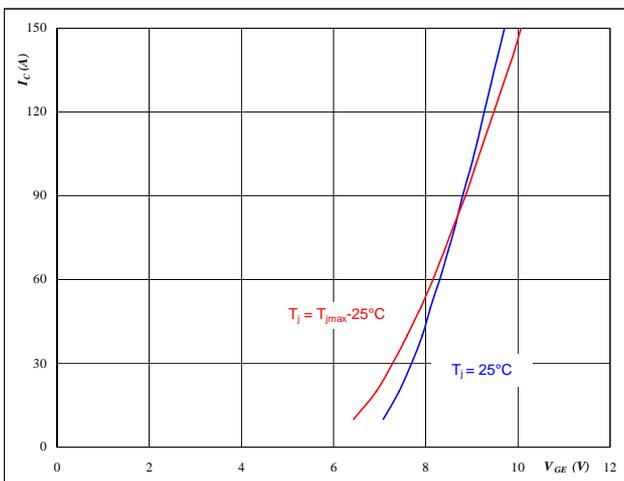


At
 $t_p = 250 \mu s$
 $T_j = 150 \text{ } ^\circ C$
 V_{GE} from 7 V to 17 V in steps of 1 V

Figure 3 Output inverter IGBT

Typical transfer characteristics

$$I_C = f(V_{GE})$$

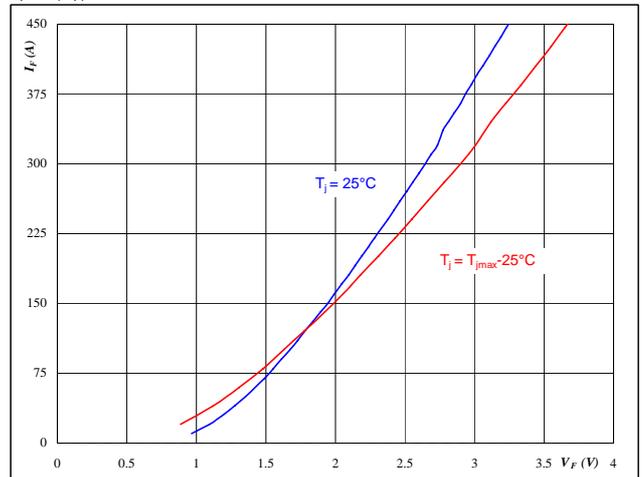


At
 $t_p = 250 \mu s$
 $V_{CE} = 0 V$

Figure 4 Output inverter FRED

Typical diode forward current as a function of forward voltage

$$I_F = f(V_F)$$



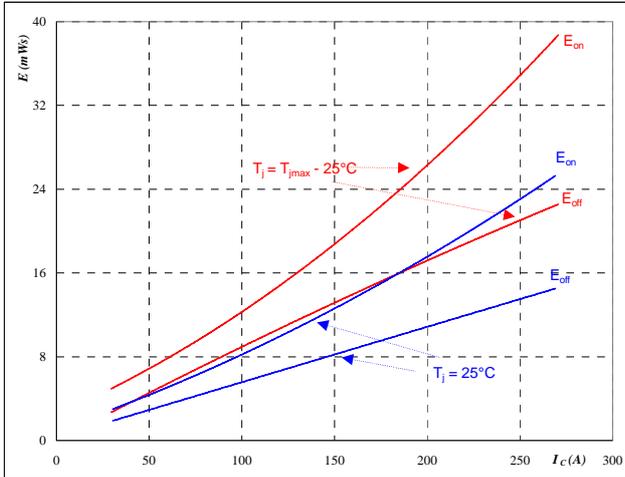
At
 $t_p = 250 \mu s$

Output Inverter

Figure 5 Output inverter IGBT

Typical switching energy losses
as a function of collector current

$$E = f(I_C)$$



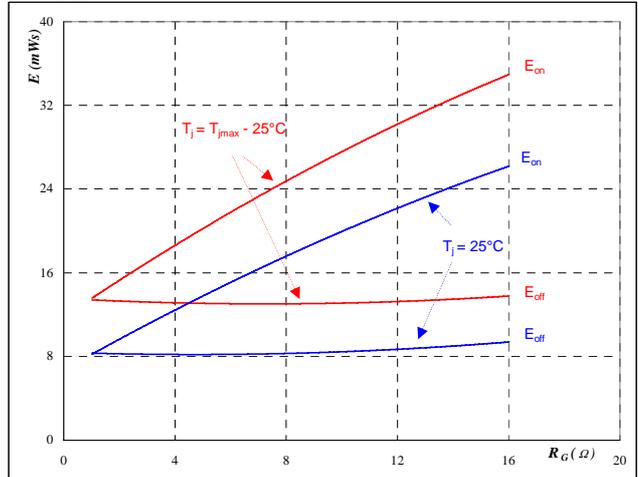
With an inductive load at

$T_j =$	25/150	°C
$V_{CE} =$	600	V
$V_{GE} =$	±15	V
$R_{gon} =$	4	Ω
$R_{goff} =$	4	Ω

Figure 6 Output inverter IGBT

Typical switching energy losses
as a function of gate resistor

$$E = f(R_G)$$



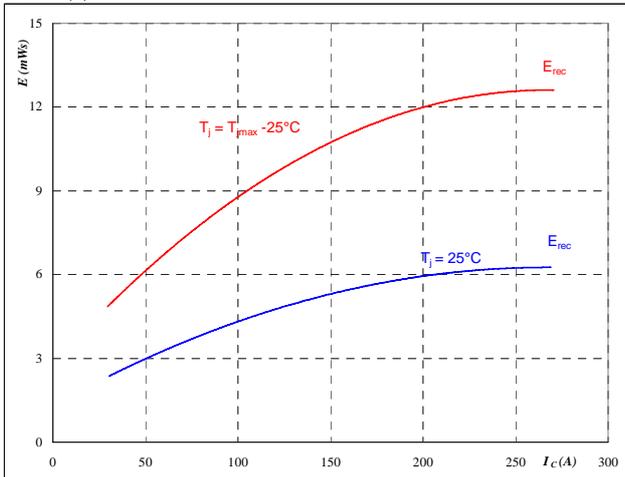
With an inductive load at

$T_j =$	25/150	°C
$V_{CE} =$	600	V
$V_{GE} =$	±15	V
$I_C =$	150	A

Figure 7 Output inverter IGBT

Typical reverse recovery energy loss
as a function of collector current

$$E_{rec} = f(I_C)$$



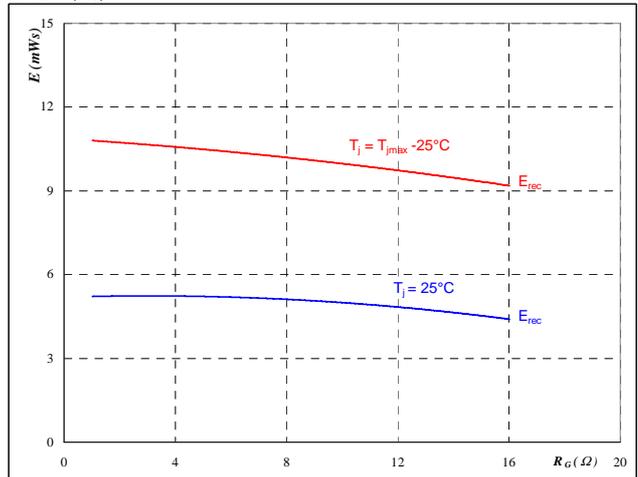
With an inductive load at

$T_j =$	25/150	°C
$V_{CE} =$	600	V
$V_{GE} =$	±15	V
$R_{gon} =$	4	Ω

Figure 8 Output inverter IGBT

Typical reverse recovery energy loss
as a function of gate resistor

$$E_{rec} = f(R_G)$$



With an inductive load at

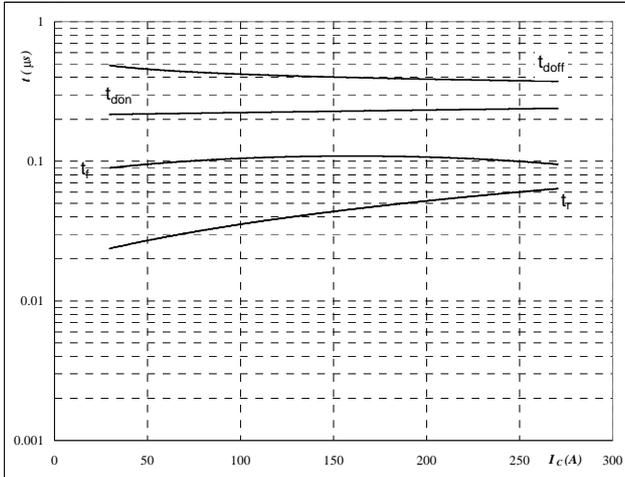
$T_j =$	25/150	°C
$V_{CE} =$	600	V
$V_{GE} =$	±15	V
$I_C =$	150	A

Output Inverter

Figure 9 Output inverter IGBT

Typical switching times as a function of collector current

$$t = f(I_C)$$



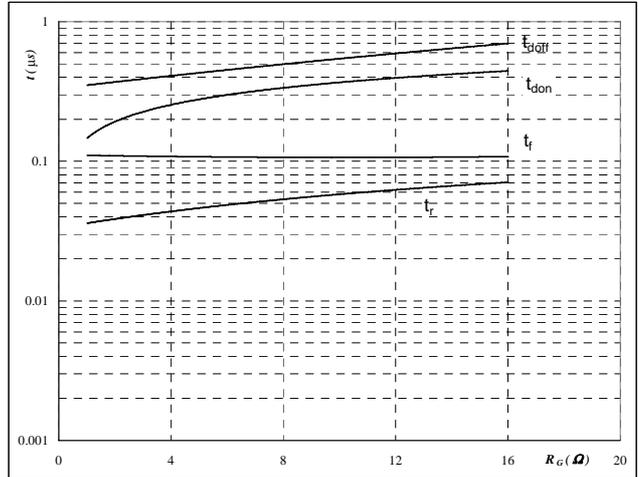
With an inductive load at

$T_J =$	150	°C
$V_{CE} =$	600	V
$V_{GE} =$	±15	V
$R_{gon} =$	4	Ω
$R_{goff} =$	4	Ω

Figure 10 Output inverter IGBT

Typical switching times as a function of gate resistor

$$t = f(R_G)$$



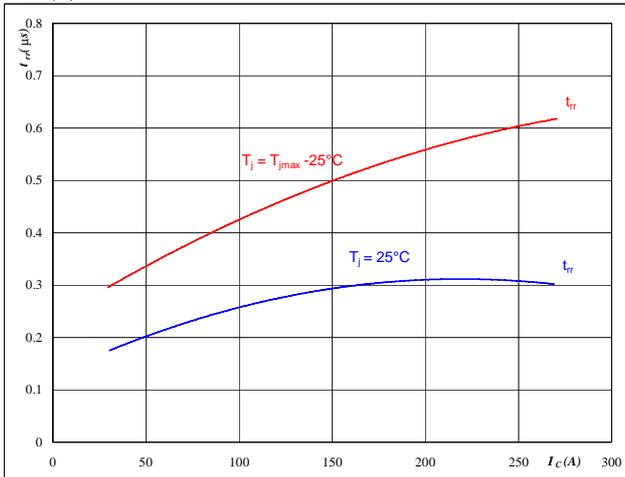
With an inductive load at

$T_J =$	150	°C
$V_{CE} =$	600	V
$V_{GE} =$	±15	V
$I_C =$	150	A

Figure 11 Output inverter FRED

Typical reverse recovery time as a function of collector current

$$t_{rr} = f(I_C)$$



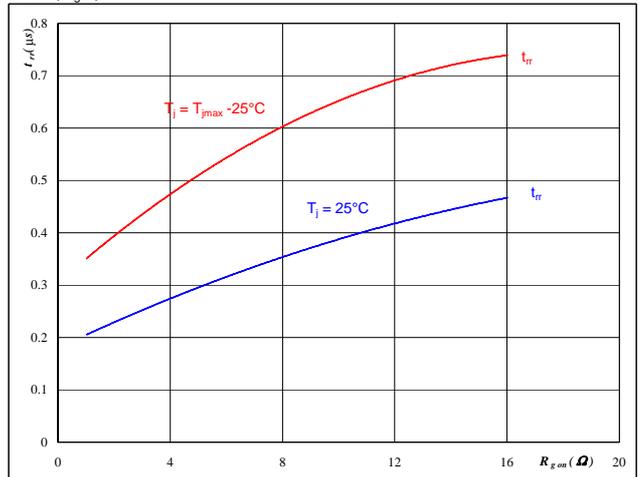
At

$T_J =$	25/150	°C
$V_{CE} =$	600	V
$V_{GE} =$	±15	V
$R_{gon} =$	4	Ω

Figure 12 Output inverter FRED

Typical reverse recovery time as a function of IGBT turn on gate resistor

$$t_{rr} = f(R_{gon})$$



At

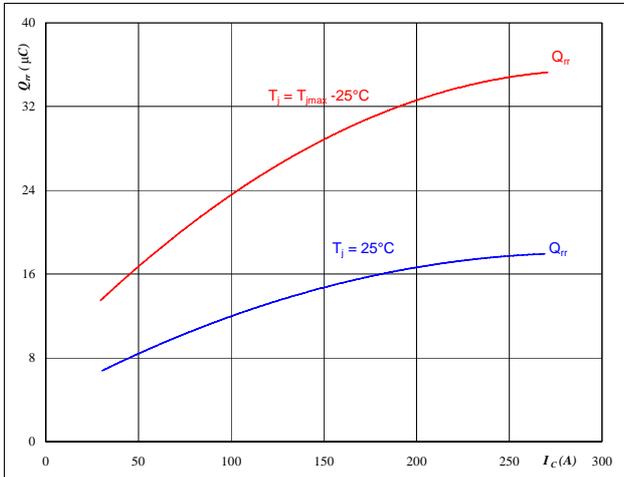
$T_J =$	25/150	°C
$V_R =$	600	V
$I_F =$	150	A
$V_{GE} =$	±15	V

Output Inverter

Figure 13 Output inverter FRED

Typical reverse recovery charge as a function of collector current

$$Q_{rr} = f(I_C)$$

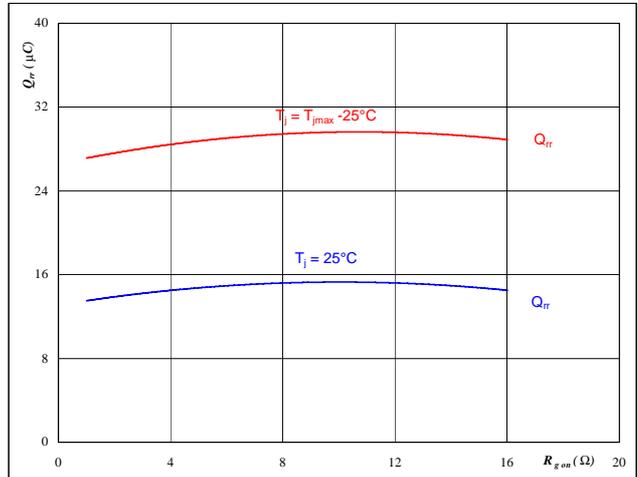


At
 $T_j = 25/150$ °C
 $V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 4$ Ω

Figure 14 Output inverter FRED

Typical reverse recovery charge as a function of IGBT turn on gate resistor

$$Q_{rr} = f(R_{gon})$$

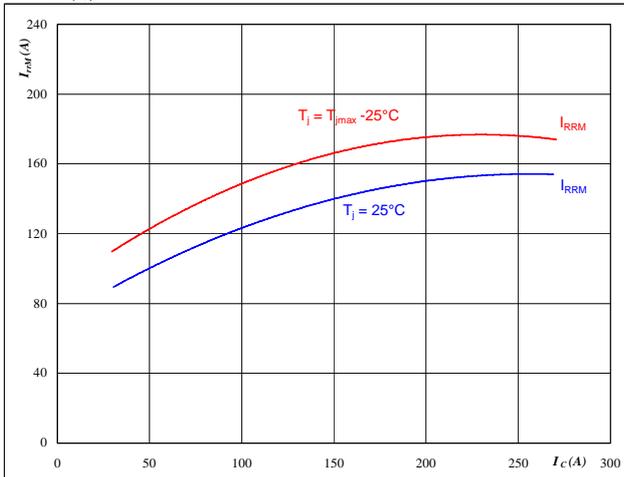


At
 $T_j = 25/150$ °C
 $V_R = 600$ V
 $I_F = 150$ A
 $V_{GE} = \pm 15$ V

Figure 15 Output inverter FRED

Typical reverse recovery current as a function of collector current

$$I_{RRM} = f(I_C)$$

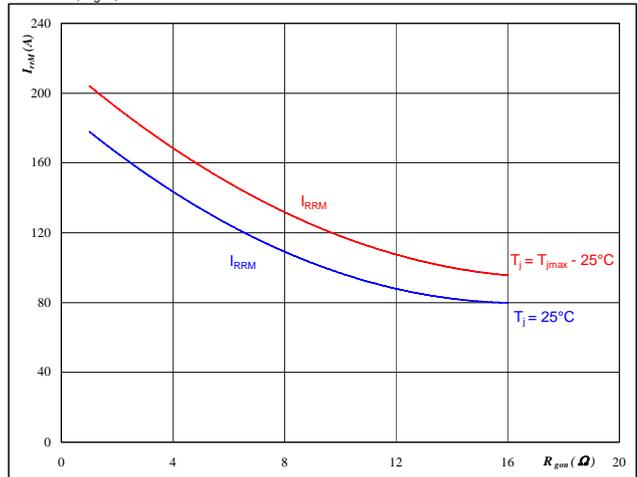


At
 $T_j = 25/150$ °C
 $V_{CE} = 600$ V
 $V_{GE} = \pm 15$ V
 $R_{gon} = 4$ Ω

Figure 16 Output inverter FRED

Typical reverse recovery current as a function of IGBT turn on gate resistor

$$I_{RRM} = f(R_{gon})$$



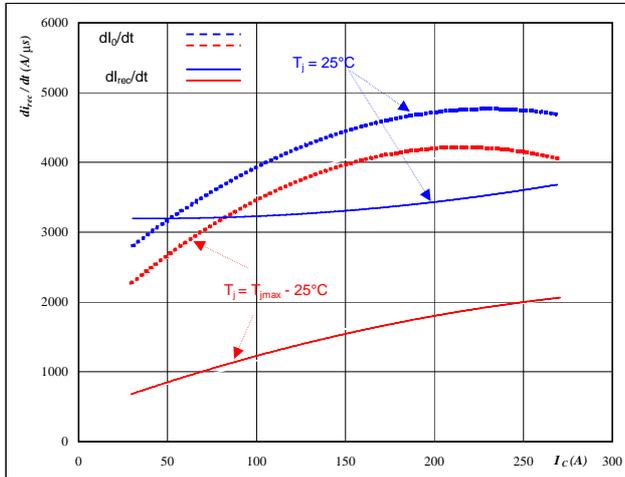
At
 $T_j = 25/150$ °C
 $V_R = 600$ V
 $I_F = 150$ A
 $V_{GE} = \pm 15$ V

Output Inverter

Figure 17 Output inverter FRED

Typical rate of fall of forward and reverse recovery current as a function of collector current

$$di_o/dt, di_{rec}/dt = f(I_c)$$



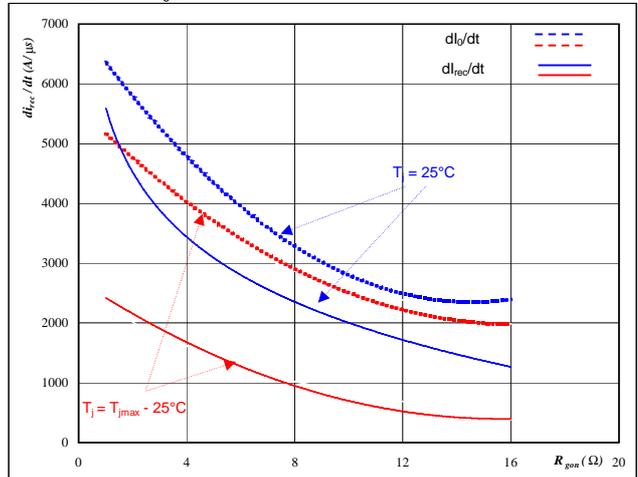
At

$T_j =$	25/150	°C
$V_{CE} =$	600	V
$V_{GE} =$	±15	V
$R_{gon} =$	4	Ω

Figure 18 Output inverter FRED

Typical rate of fall of forward and reverse recovery current as a function of IGBT turn on gate resistor

$$di_o/dt, di_{rec}/dt = f(R_{gon})$$



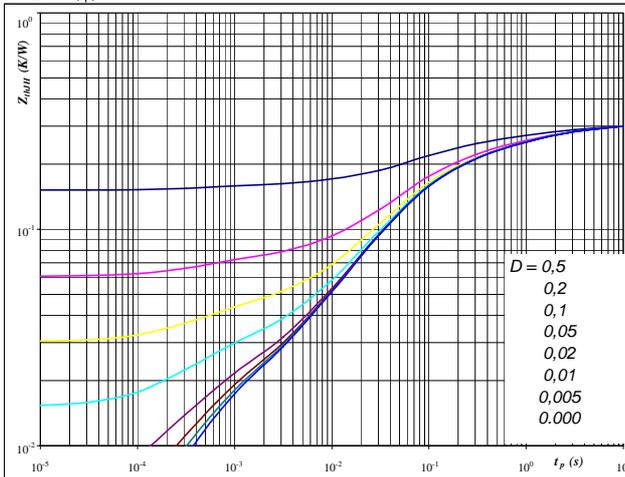
At

$T_j =$	25/150	°C
$V_R =$	600	V
$I_F =$	150	A
$V_{GE} =$	±15	V

Figure 19 Output inverter IGBT

IGBT transient thermal impedance as a function of pulse width

$$Z_{thJH} = f(t_p)$$



At

$D =$	t_p / T	
$R_{thJH} =$	0.30	K/W

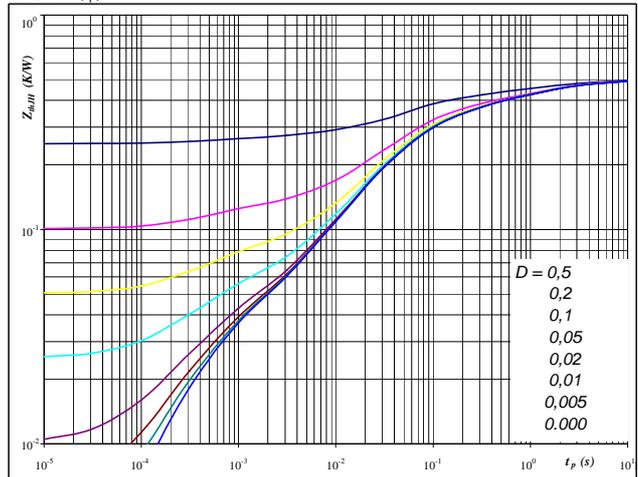
IGBT thermal model values

R (C/W)	Tau (s)
0.03	4.8E+00
0.06	1.1E+00
0.10	1.8E-01
0.09	3.7E-02
0.01	3.8E-03
0.01	3.9E-04

Figure 20 Output inverter FRED

FRED transient thermal impedance as a function of pulse width

$$Z_{thJH} = f(t_p)$$



At

$D =$	t_p / T	
$R_{thJH} =$	0.50	K/W

FRED thermal model values

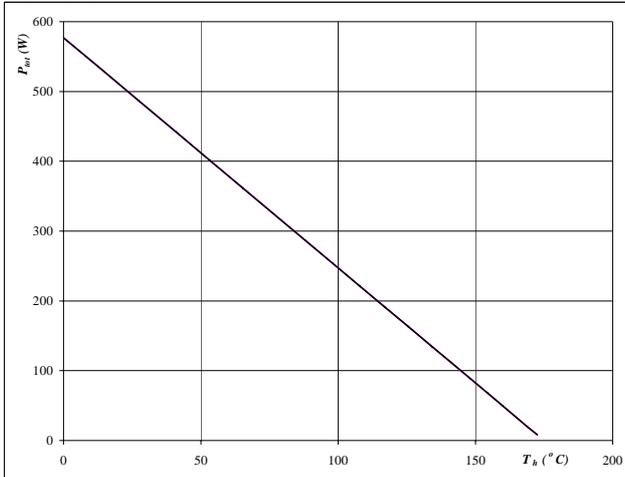
R (C/W)	Tau (s)
0.03	1.0E+01
0.10	1.4E+00
0.12	1.8E-01
0.19	3.3E-02
0.03	4.7E-03
0.03	4.2E-04

Output Inverter

Figure 21 Output inverter IGBT

Power dissipation as a function of heatsink temperature

$$P_{tot} = f(T_h)$$

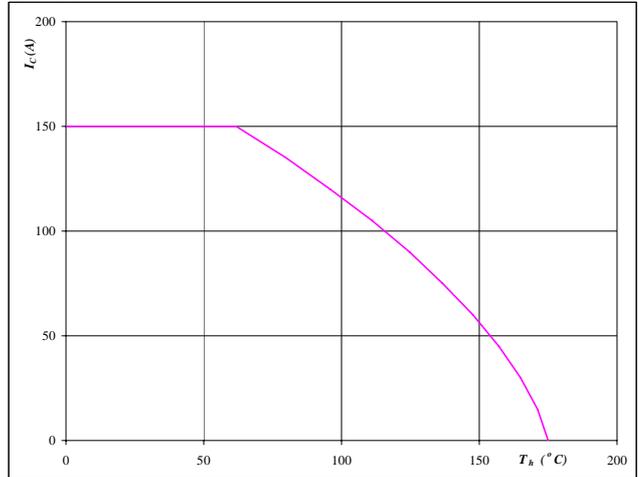


At
 $T_j = 175$ °C
— single heating
— overall heating

Figure 22 Output inverter IGBT

Collector current as a function of heatsink temperature

$$I_C = f(T_h)$$

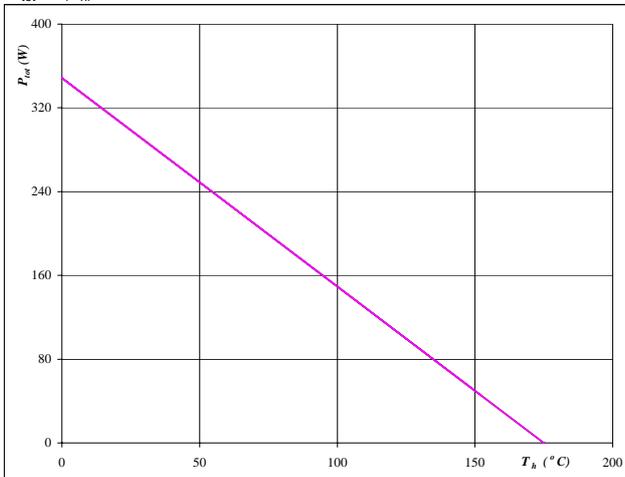


At
 $T_j = 175$ °C
 $V_{GE} = 15$ V

Figure 23 Output inverter FRED

Power dissipation as a function of heatsink temperature

$$P_{tot} = f(T_h)$$

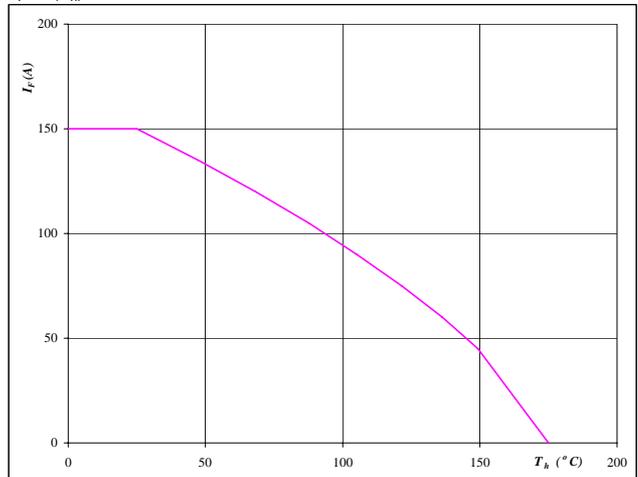


At
 $T_j = 175$ °C
— single heating
— overall heating

Figure 24 Output inverter FRED

Forward current as a function of heatsink temperature

$$I_F = f(T_h)$$

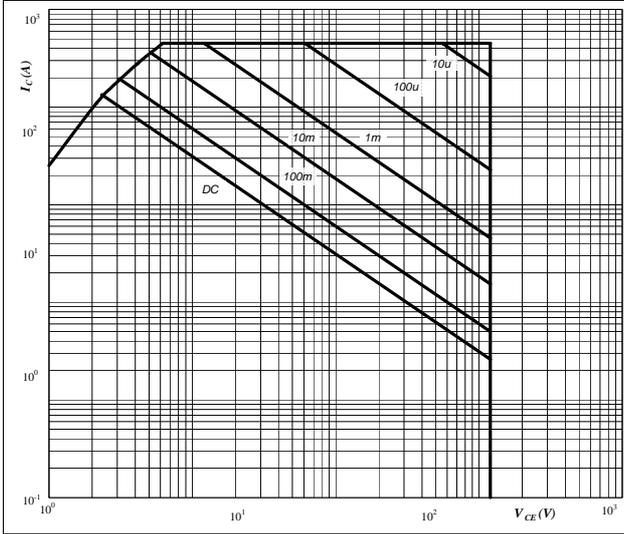


At
 $T_j = 175$ °C

Output Inverter

Figure 25 Output inverter IGBT

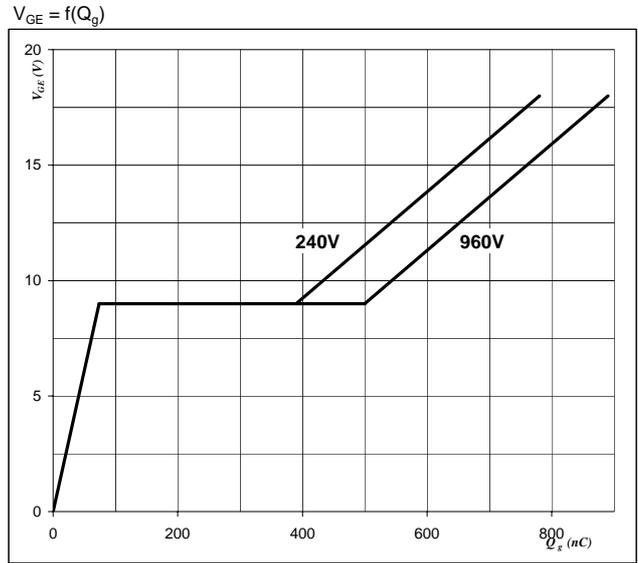
Safe operating area as a function of collector-emitter voltage
 $I_C = f(V_{CE})$



At
 D = single pulse
 Th = 80 °C
 V_{GE} = ±15 V
 T_j = T_{jmax} °C

Figure 26 Output inverter IGBT

Gate voltage vs Gate charge



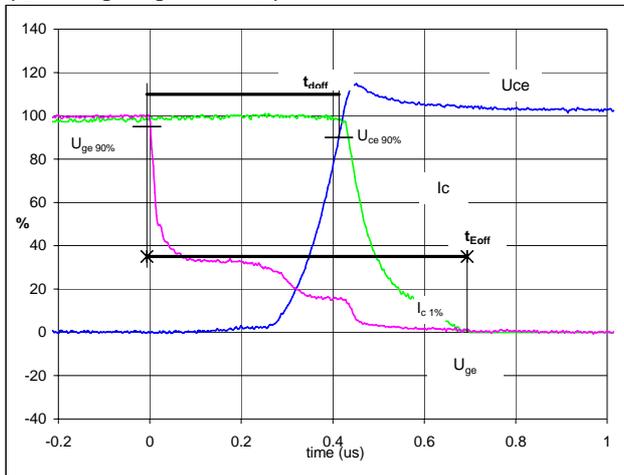
At
 I_C = 150 A

Switching Definitions Output Inverter

General conditions	
T_j	= 150 °C
R_{gon}	= 4 Ω
R_{goff}	= 4 Ω

Figure 1 Output inverter IGBT

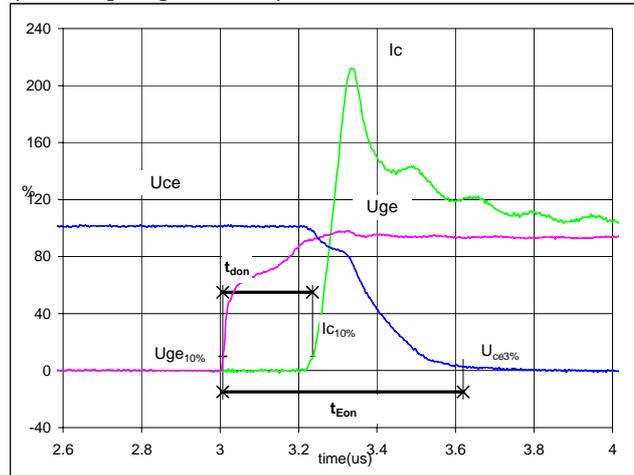
Turn-off Switching Waveforms & definition of t_{doff} , t_{Eoff}
(t_{Eoff} = integrating time for E_{off})



$V_{GE}(0\%) =$	-15	V
$V_{GE}(100\%) =$	15	V
$V_C(100\%) =$	600	V
$I_C(100\%) =$	149	A
$t_{doff} =$	0.41	μs
$t_{Eoff} =$	0.70	μs

Figure 2 Output inverter IGBT

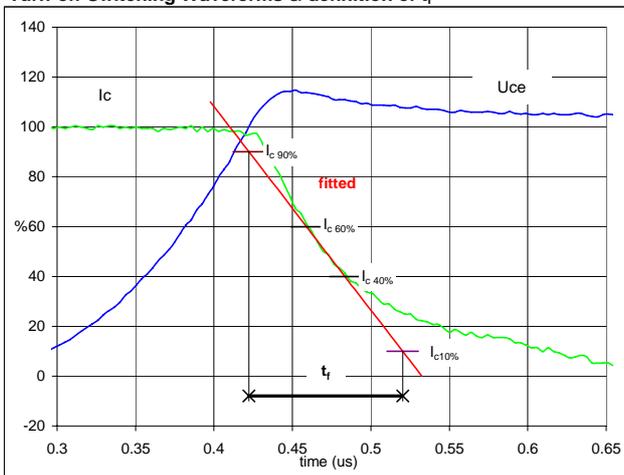
Turn-on Switching Waveforms & definition of t_{don} , t_{Eon}
(t_{Eon} = integrating time for E_{on})



$V_{GE}(0\%) =$	-15	V
$V_{GE}(100\%) =$	15	V
$V_C(100\%) =$	600	V
$I_C(100\%) =$	149	A
$t_{don} =$	0.23	μs
$t_{Eon} =$	0.61	μs

Figure 3 Output inverter IGBT

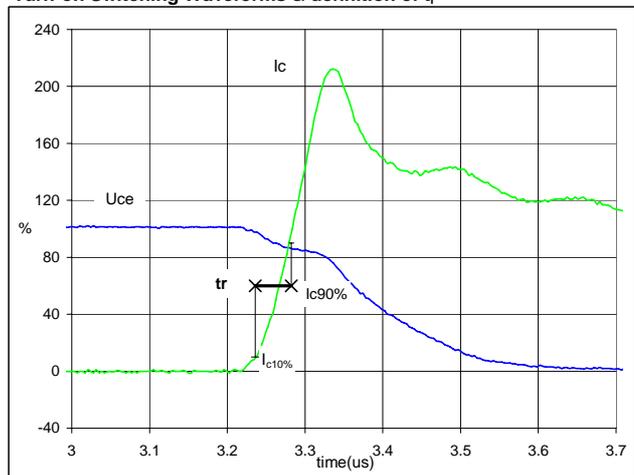
Turn-off Switching Waveforms & definition of t_f



$V_C(100\%) =$	600	V
$I_C(100\%) =$	149	A
$t_f =$	0.10	μs

Figure 4 Output inverter IGBT

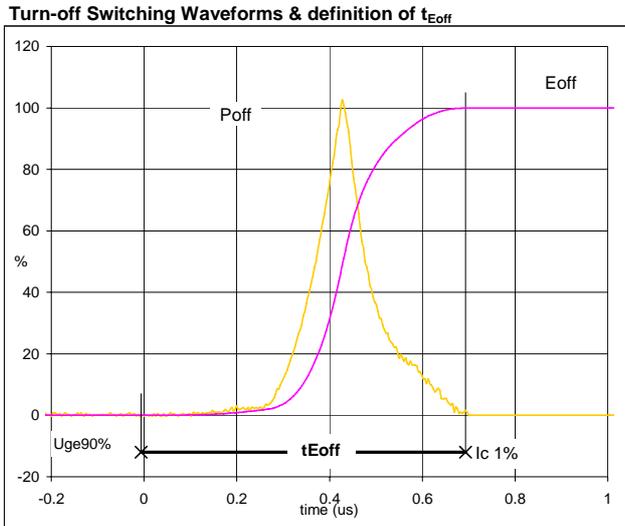
Turn-on Switching Waveforms & definition of t_r



$V_C(100\%) =$	600	V
$I_C(100\%) =$	149	A
$t_r =$	0.04	μs

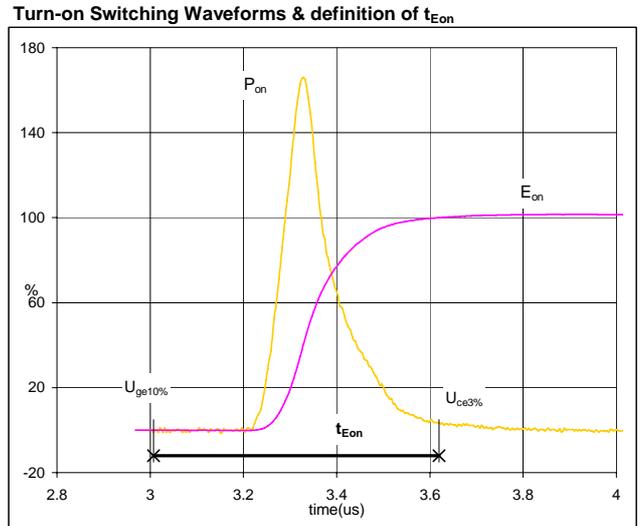
Switching Definitions Output Inverter

Figure 5 Output inverter IGBT



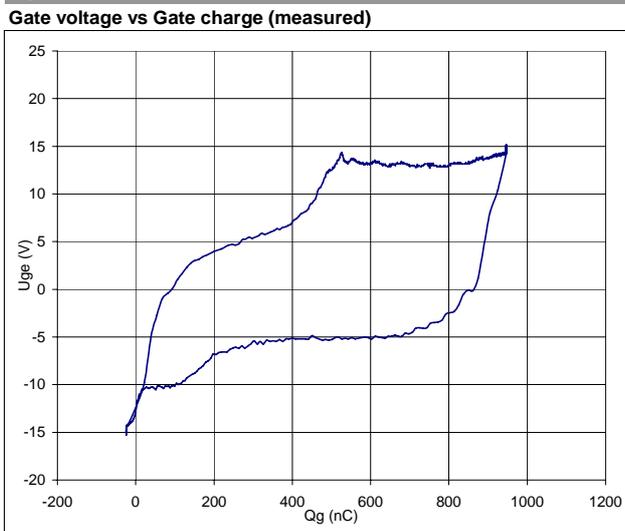
$P_{off} (100\%) =$	89.53	kW
$E_{off} (100\%) =$	12.85	mJ
$t_{Eoff} =$	0.70	μ s

Figure 6 Output inverter IGBT



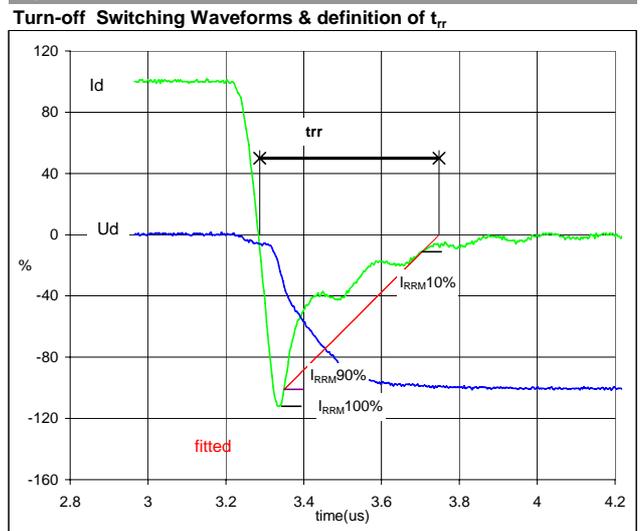
$P_{on} (100\%) =$	89.53	kW
$E_{on} (100\%) =$	18.80	mJ
$t_{Eon} =$	0.61	μ s

Figure 7 Output inverter FRED



$V_{GEoff} =$	-15	V
$V_{GEon} =$	15	V
$V_C (100\%) =$	600	V
$I_C (100\%) =$	149	A
$Q_g =$	4677.76	nC

Figure 8 Output inverter IGBT

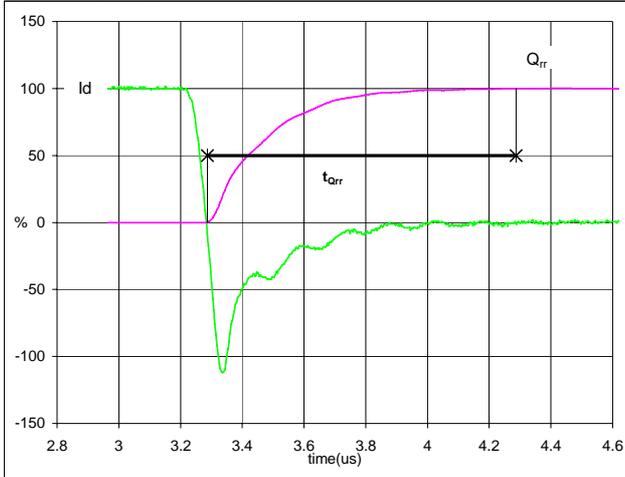


$V_d (100\%) =$	600	V
$I_d (100\%) =$	149	A
$I_{RRM} (100\%) =$	-168	A
$t_{rr} =$	0.47	μ s

Switching Definitions Output Inverter

Figure 9 Output inverter FRED

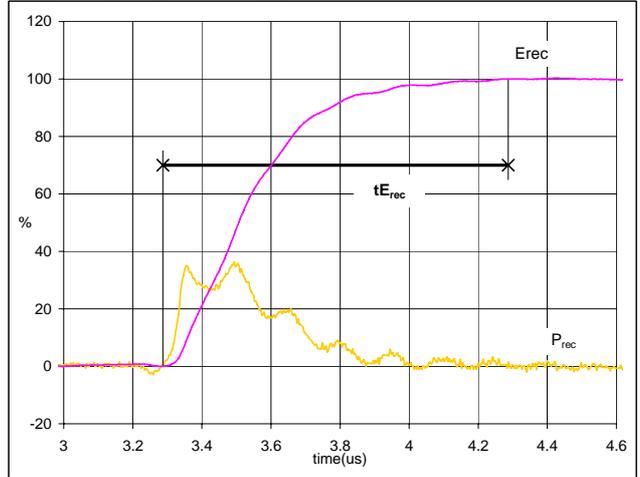
Turn-on Switching Waveforms & definition of t_{Qrr}
(t_{Qrr} = integrating time for Q_{rr})



I_d (100%) =	149	A
Q_{rr} (100%) =	29.16	μC
t_{Qrr} =	1.00	μs

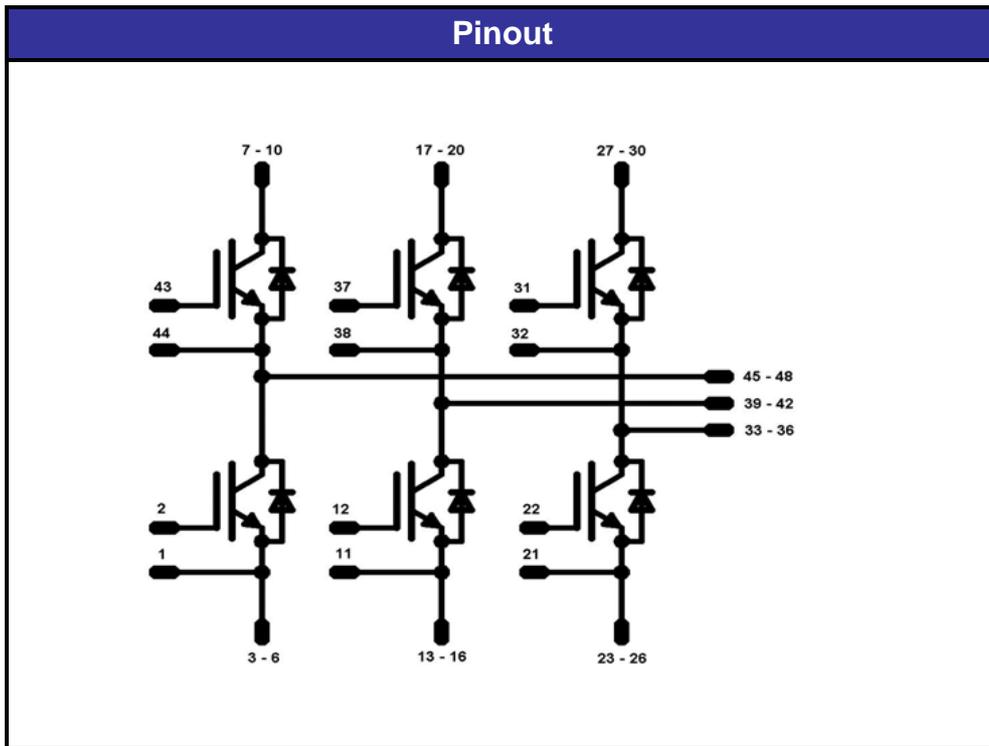
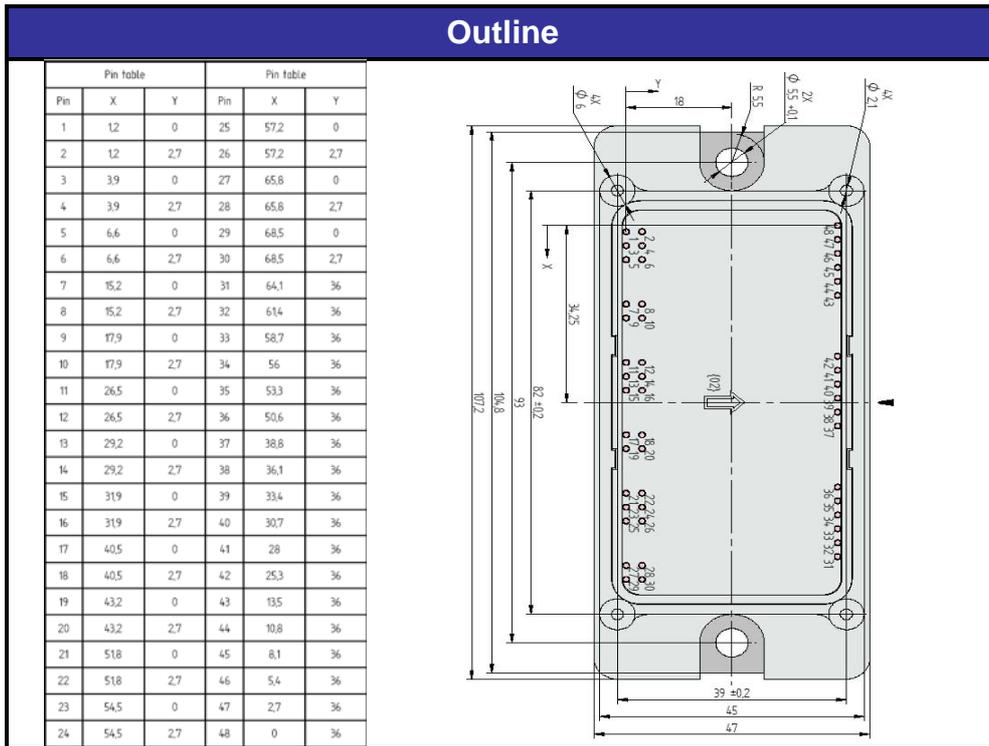
Figure 10 Output inverter FRED

Turn-on Switching Waveforms & definition of $t_{E_{rec}}$
($t_{E_{rec}}$ = integrating time for E_{rec})



P_{rec} (100%) =	89.53	kW
E_{rec} (100%) =	10.81	mJ
$t_{E_{rec}}$ =	1.00	μs

Package Outline and Pinout



PRODUCT STATUS DEFINITIONS

Datasheet Status	Product Status	Definition
Target	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice. The data contained is exclusively intended for technically trained staff.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data may be published at a later date. Vincotech reserves the right to make changes at any time without notice in order to improve design. The data contained is exclusively intended for technically trained staff.
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