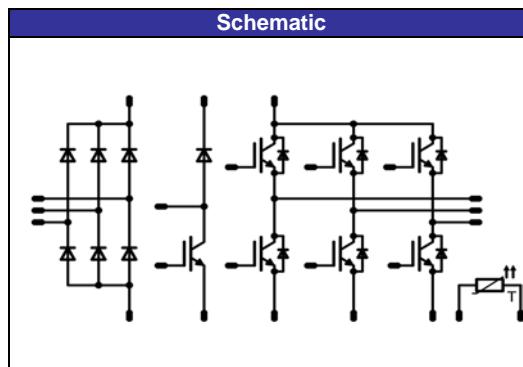


**MiniSKiiP® 1 PIM**
**1200V / 8A**

Features
<ul style="list-style-type: none"> <li>• Solderless interconnection</li> <li>• Trench Fieldstop IGBT4 technology</li> </ul>



Target Applications
<ul style="list-style-type: none"> <li>• Industrial Motor Drives</li> </ul>



Types
<ul style="list-style-type: none"> <li>• V23990-K209-A40-PM</li> </ul>

## Maximum Ratings

 $T_j=25^\circ\text{C}$ , unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
-----------	--------	-----------	-------	------

**Input Rectifier Diode**

Repetitive peak reverse voltage	$V_{RRM}$		1600	V
DC forward current	$I_{FAV}$	$T_j=T_{j\max}$	27	A
Surge forward current	$I_{FSM}$		220	A
$I^2t$ -value	$I^2t$	$t_p=10\text{ms}$ $T_j=25^\circ\text{C}$	240	$\text{A}^2\text{s}$
Power dissipation per Diode	$P_{\text{tot}}$	$T_j=T_{j\max}$	41	W
Maximum Junction Temperature	$T_{j\max}$		150	$^\circ\text{C}$

**Inverter Transistor**

Collector-emitter break down voltage	$V_{CE}$		1200	V
DC collector current	$I_C$	$T_j=T_{j\max}$	13	A
Repetitive peak collector current	$I_{Cpulse}$	$t_p$ limited by $T_{j\max}$	24	A
Power dissipation per IGBT	$P_{\text{tot}}$	$T_j=T_{j\max}$	47	W
Gate-emitter peak voltage	$V_{GE}$		$\pm 20$	V
Short circuit ratings	$t_{sc}$ $V_{CC}$	$T_j \leq 150^\circ\text{C}$ $V_{GE}=15\text{V}$	10 800	$\mu\text{s}$ V
Maximum Junction Temperature	$T_{j\max}$		175	$^\circ\text{C}$

## Maximum Ratings

$T_j=25^\circ\text{C}$ , unless otherwise specified

Parameter	Symbol	Condition	Value	Unit	
<b>Inverter Diode</b>					
Repetitive peak reverse voltage	$V_{RRM}$		1200	V	
DC forward current	$I_F$	$T_j=T_j\max$	11	A	
Repetitive peak forward current	$I_{FRM}$	$t_p$ limited by $T_j\max$	24	A	
Power dissipation per Diode	$P_{tot}$	$T_j=T_j\max$	31	W	
Maximum Junction Temperature	$T_j\max$		175	$^\circ\text{C}$	
<b>Brake Transistor</b>					
Collector-emitter break down voltage	$V_{CE}$		1200	V	
DC collector current	$I_C$	$T_j=T_j\max$	13	A	
Repetitive peak collector current	$I_{Cpuls}$	$t_p$ limited by $T_j\max$	24	A	
Power dissipation per IGBT	$P_{tot}$	$T_j=T_j\max$	44	W	
Gate-emitter peak voltage	$V_{GE}$		$\pm 20$	V	
Short circuit ratings	$t_{sc}$ $V_{CC}$	$T_j \leq 150^\circ\text{C}$ $V_{GE}=15\text{V}$	10 800	$\mu\text{s}$ V	
Maximum Junction Temperature	$T_j\max$		175	$^\circ\text{C}$	
<b>Brake Diode</b>					
Repetitive peak reverse voltage	$V_{RRM}$		1200	V	
DC forward current	$I_F$	$T_j=T_j\max$	12	A	
Repetitive peak forward current	$I_{FRM}$	$t_p$ limited by $T_j\max$	24	A	
Power dissipation per Diode	$P_{tot}$	$T_j=T_j\max$	33	W	
Maximum Junction Temperature	$T_j\max$		175	$^\circ\text{C}$	
<b>Thermal Properties</b>					
Storage temperature	$T_{stg}$		-40...+125	$^\circ\text{C}$	
Operation temperature under switching condition	$T_{op}$		-40...+( $T_j\max - 25$ )	$^\circ\text{C}$	
<b>Insulation Properties</b>					
Insulation voltage	$V_{is}$	$t=2\text{s}$	DC voltage	4000	V
Creepage distance				min 12.7	mm
Clearance				min 12.7	mm

**Characteristic Values**

Parameter	Symbol	Conditions					Value			Unit
			$V_{GE}$ [V] or $V_{GS}$ [V]	$V_r$ [V] or $V_{CE}$ [V] or $V_{DS}$ [V]	$I_c$ [A] or $I_F$ [A] or $I_D$ [A]	$T_j$	Min	Typ	Max	
<b>Input Rectifier Diode</b>										
Forward voltage	$V_F$				15	$T_j=25^\circ C$ $T_j=125^\circ C$	1	1.07 0.98	1.75	V
Threshold voltage (for power loss calc. only)	$V_{to}$					$T_j=25^\circ C$ $T_j=125^\circ C$		0.85 0.73		V
Slope resistance (for power loss calc. only)	$r_t$					$T_j=25^\circ C$ $T_j=125^\circ C$		0.027 0.031		$\Omega$
Reverse current	$I_r$			1500		$T_j=25^\circ C$ $T_j=125^\circ C$			0.1 1.1	mA
Thermal resistance chip to heatsink per chip	$R_{thJH}$	Thermal grease thickness≤50μm $\lambda=0.61\text{W/mK}$						1.71		K/W
Thermal resistance chip to case per chip	$R_{thJC}$								N/A	
<b>Inverter Transistor</b>										
Gate emitter threshold voltage	$V_{GE(th)}$	$V_{CE}=V_{GE}$			0.0003	$T_j=25^\circ C$ $T_j=150^\circ C$	5	5.8	6.5	V
Collector-emitter saturation voltage	$V_{CE(sat)}$		15		8	$T_j=25^\circ C$ $T_j=150^\circ C$	1.6	2.01 2.38	2.5	V
Collector-emitter cut-off current incl. Diode	$I_{CES}$		0	1200		$T_j=25^\circ C$ $T_j=150^\circ C$			0.06	mA
Gate-emitter leakage current	$I_{GES}$		20	0		$T_j=25^\circ C$ $T_j=150^\circ C$			180	nA
Integrated Gate resistor	$R_{gint}$							-		$\Omega$
Turn-on delay time	$t_{d(on)}$	$R_{goff}=64\Omega$ $R_{gon}=64\Omega$	$\pm 15$	600	8	$T_j=25^\circ C$ $T_j=150^\circ C$		115 126		ns
Rise time	$t_r$					$T_j=25^\circ C$ $T_j=150^\circ C$		33 39		
Turn-off delay time	$t_{d(off)}$					$T_j=25^\circ C$ $T_j=150^\circ C$		225 290		
Fall time	$t_f$					$T_j=25^\circ C$ $T_j=150^\circ C$		89 130		
Turn-on energy loss per pulse	$E_{on}$					$T_j=25^\circ C$ $T_j=150^\circ C$		0.56 0.88		mWs
Turn-off energy loss per pulse	$E_{off}$					$T_j=25^\circ C$ $T_j=150^\circ C$		0.48 0.77		
Input capacitance	$C_{ies}$	$f=1\text{MHz}$	0	25		$T_j=25^\circ C$		490		pF
Output capacitance	$C_{oss}$							50		
Reverse transfer capacitance	$C_{rss}$							30		
Gate charge	$Q_{Gate}$	$V_{cc}=960\text{V}$	15		8	$T_j=25^\circ C$		53		nC
Thermal resistance chip to heatsink per chip	$R_{thJH}$	Thermal grease thickness≤50μm $\lambda=0.61\text{W/mK}$						2.01		K/W
Thermal resistance chip to case per chip	$R_{thJC}$								N/A	
<b>Inverter Diode</b>										
Diode forward voltage	$V_F$				8	$T_j=25^\circ C$ $T_j=150^\circ C$	1.5	2.37 2.28	2.9	V
Peak reverse recovery current	$I_{RRM}$	$R_{goff}=64\Omega$	$\pm 15$	600	8	$T_j=25^\circ C$ $T_j=150^\circ C$		4.49 6.2		A
Reverse recovery time	$t_{rr}$					$T_j=25^\circ C$ $T_j=150^\circ C$		362 574		ns
Reverse recovered charge	$Q_{rr}$					$T_j=25^\circ C$ $T_j=150^\circ C$		0.61 1.47		$\mu C$
Peak rate of fall of recovery current	$d(i_{rec})/\text{dt}$					$T_j=25^\circ C$ $T_j=150^\circ C$		31 22		$A/\mu s$
Reverse recovered energy	$E_{rec}$					$T_j=25^\circ C$ $T_j=150^\circ C$		0.24 0.62		mWs
Thermal resistance chip to heatsink per chip	$R_{thJH}$	Thermal grease thickness≤50μm $\lambda=0.61\text{W/mK}$						3.1		K/W
Thermal resistance chip to case per chip	$R_{thJC}$								N/A	

**Characteristic Values**

Parameter	Symbol	Conditions				Value			Unit	
			$V_{GE}$ [V] or $V_{GS}$ [V]	$V_r$ [V] or $V_{CE}$ [V] or $V_{DS}$ [V]	$I_c$ [A] or $I_F$ [A] or $I_D$ [A]	$T_j$	Min	Typ	Max	

**Brake Transistor**

Gate emitter threshold voltage	$V_{GE(th)}$	$V_{CE}=V_{GE}$			0.0003	$T_j=25^\circ C$ $T_j=150^\circ C$	5	5.8	6.5	V
Collector-emitter saturation voltage	$V_{CE(sat)}$		15		8	$T_j=25^\circ C$ $T_j=150^\circ C$	1.6	2.01 2.38	2.5	V
Collector-emitter cut-off incl diode	$I_{CES}$		0	1200		$T_j=25^\circ C$ $T_j=150^\circ C$			0.06	mA
Gate-emitter leakage current	$I_{GES}$		20	0		$T_j=25^\circ C$ $T_j=150^\circ C$			180	nA
Integrated Gate resistor	$R_{gint}$							-		$\Omega$
Turn-on delay time	$t_{d(on)}$	$R_{gon}=64\Omega$ $R_{goff}=64\Omega$	$\pm 15$	600	8	$T_j=25^\circ C$ $T_j=150^\circ C$	114 116			ns
Rise time	$t_r$					$T_j=25^\circ C$ $T_j=150^\circ C$	32 38			
Turn-off delay time	$t_{d(off)}$					$T_j=25^\circ C$ $T_j=150^\circ C$	221 279			
Fall time	$t_f$					$T_j=25^\circ C$ $T_j=150^\circ C$	93 134			
Turn-on energy loss per pulse	$E_{on}$					$T_j=25^\circ C$ $T_j=150^\circ C$	0.52 0.84			mWs
Turn-off energy loss per pulse	$E_{off}$					$T_j=25^\circ C$ $T_j=150^\circ C$	0.49 0.78			
Input capacitance	$C_{ies}$						490			
Output capacitance	$C_{oss}$	$f=1MHz$	0	25		$T_j=25^\circ C$	50			pF
Reverse transfer capacitance	$C_{rss}$						30			
Gate charge	$Q_{Gate}$					$T_j=25^\circ C$	53			nC
Thermal resistance chip to heatsink per chip	$R_{thJH}$	Thermal grease thickness≤50μm $\lambda=0.61W/mK$						2.14		K/W
Thermal resistance chip to case per chip	$R_{thJC}$						N/A			

**Brake Diode**

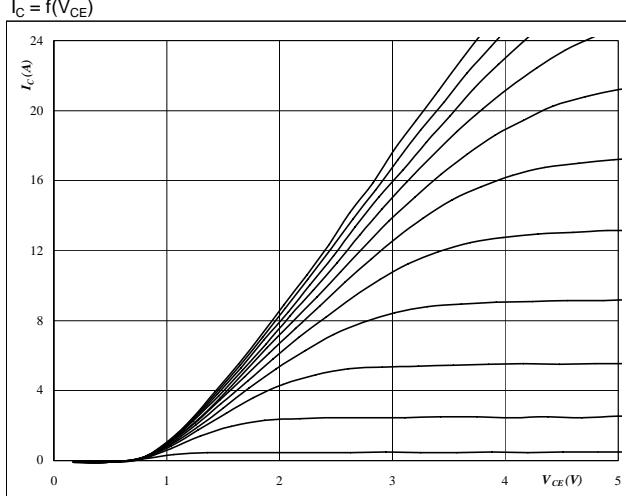
Diode forward voltage	$V_F$				8	$T_j=25^\circ C$ $T_j=150^\circ C$	1.5	2.47 2.3	2.9	V
Reverse leakage current	$I_r$		±15	600	8	$T_j=25^\circ C$ $T_j=150^\circ C$			60 700	$\mu A$
Peak reverse recovery current	$I_{RRM}$	$R_{gon}=64\Omega$	$\pm 15$	600	8	$T_j=25^\circ C$ $T_j=150^\circ C$	4.46 6.38			A
Reverse recovery time	$t_{rr}$					$T_j=25^\circ C$ $T_j=150^\circ C$	315 570			ns
Reverse recovered charge	$Q_{rr}$					$T_j=25^\circ C$ $T_j=150^\circ C$	0.56 1.44			$\mu C$
Peak rate of fall of recovery current	$dI_{(rec)max}/dt$					$T_j=25^\circ C$ $T_j=150^\circ C$	31 25			A/ $\mu s$
Reverse recovery energy	$E_{rec}$					$T_j=25^\circ C$ $T_j=150^\circ C$	0.23 0.61			mWs
Thermal resistance chip to heatsink per chip	$R_{thJH}$	Thermal grease thickness≤50μm $\lambda=0.61W/mK$						2.86		K/W
Thermal resistance chip to case per chip	$R_{thJC}$						N/A			

**Thermistor**

Rated resistance	$R$					$T_j=25^\circ C$ $T_j=150^\circ C$	0.97	1 2.23	1.03	kΩ
Temperature coefficient	$a$					$T_j=25^\circ C$		0.76		%/K
Recommended measuring current	$I$					$T_j=25^\circ C$		1	3	mA

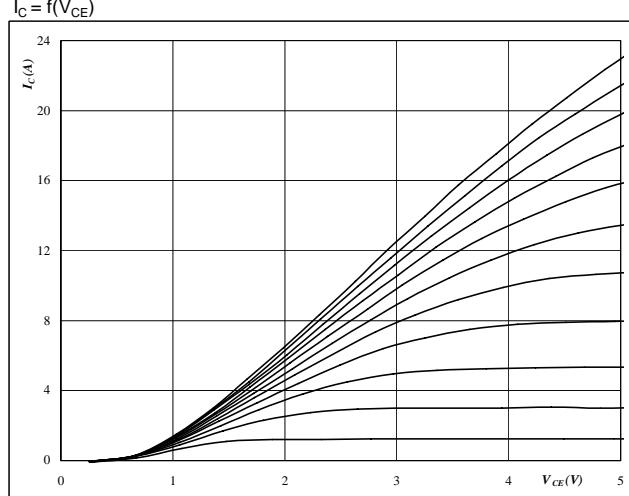
## Output Inverter

**Figure 1**  
Typical output characteristics  
 $I_C = f(V_{CE})$



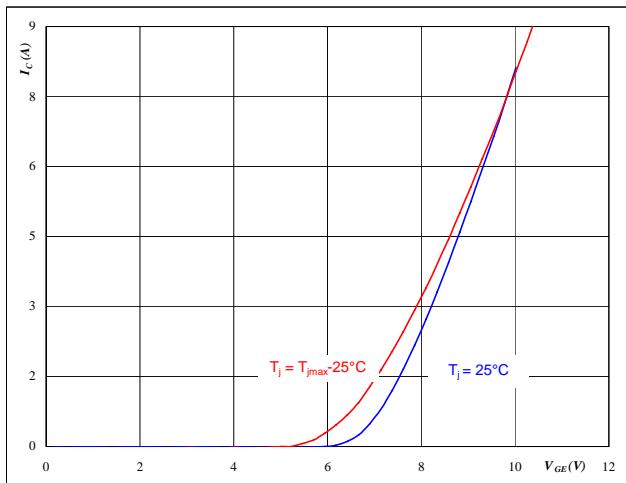
At  
 $t_p = 250 \mu s$   
 $T_j = 25 {}^\circ C$   
 $V_{GE}$  from 7 V to 17 V in steps of 1 V

**Figure 2**  
Typical output characteristics  
 $I_C = f(V_{CE})$



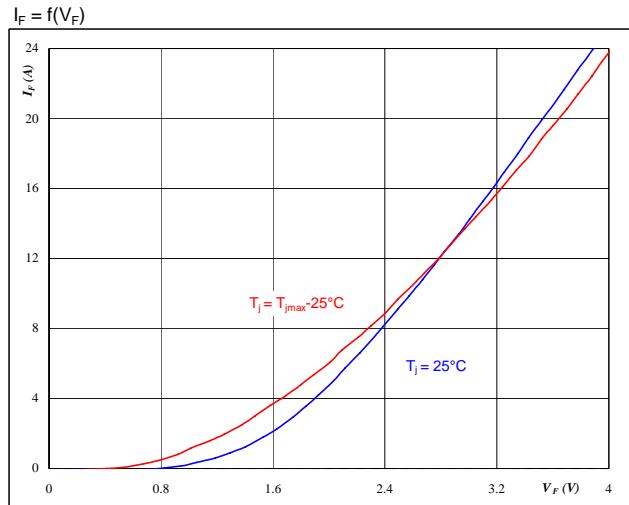
At  
 $t_p = 250 \mu s$   
 $T_j = 150 {}^\circ C$   
 $V_{GE}$  from 7 V to 17 V in steps of 1 V

**Figure 3**  
Typical transfer characteristics  
 $I_C = f(V_{GE})$



At  
 $t_p = 250 \mu s$   
 $V_{CE} = 10 V$

**Figure 4**  
Typical diode forward current as a function of forward voltage  
 $I_F = f(V_F)$



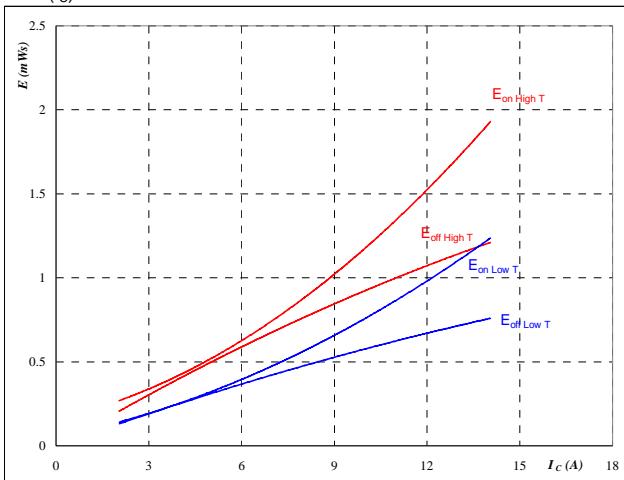
At  
 $t_p = 250 \mu s$

## Output Inverter

**Figure 5**

**Typical switching energy losses  
as a function of collector current**

$$E = f(I_C)$$



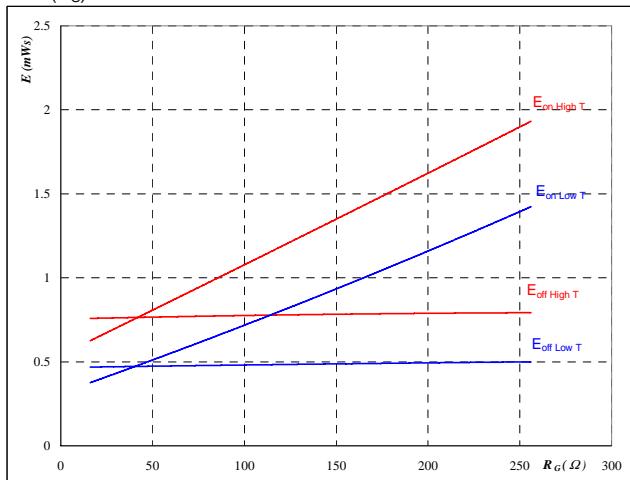
With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 64 \quad \Omega \\ R_{goff} &= 64 \quad \Omega \end{aligned}$$

**Output inverter IGBT**
**Figure 6**

**Typical switching energy losses  
as a function of gate resistor**

$$E = f(R_G)$$



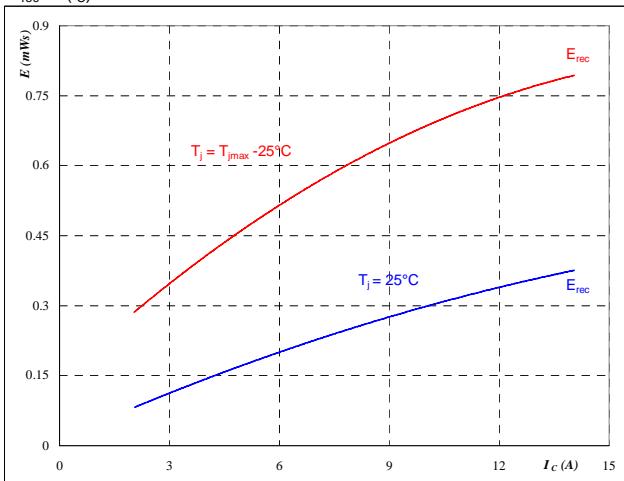
With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ I_C &= 8 \quad \text{A} \end{aligned}$$

**Figure 7**
**Output inverter IGBT**

**Typical reverse recovery energy loss  
as a function of collector current**

$$E_{rec} = f(I_C)$$



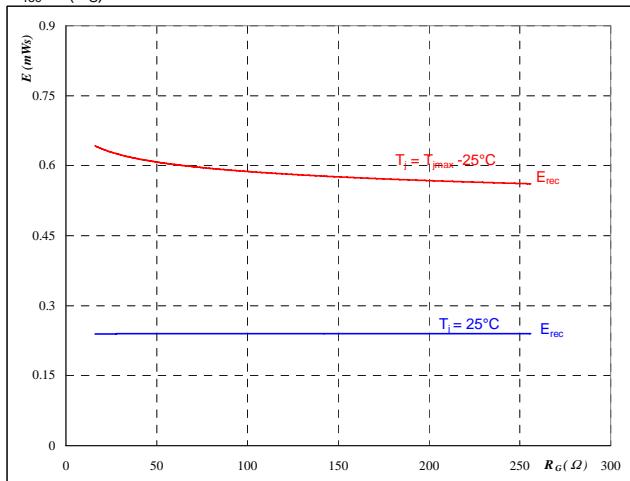
With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 64 \quad \Omega \end{aligned}$$

**Figure 8**
**Output inverter IGBT**

**Typical reverse recovery energy loss  
as a function of gate resistor**

$$E_{rec} = f(R_G)$$



With an inductive load at

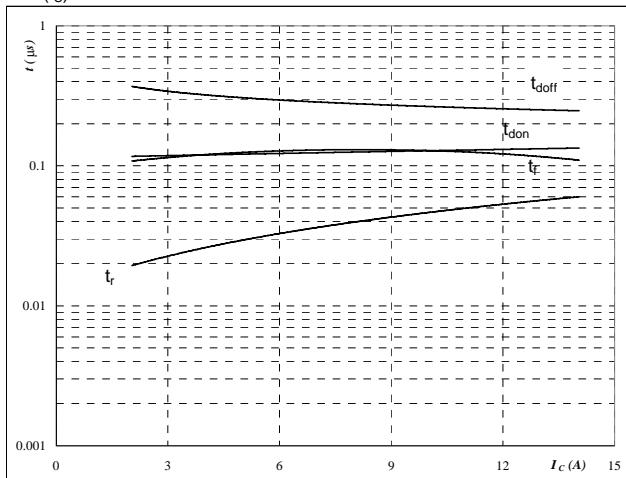
$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ I_C &= 8 \quad \text{A} \end{aligned}$$

## Output Inverter

**Figure 9**

Typical switching times as a function of collector current

$$t = f(I_C)$$



With an inductive load at

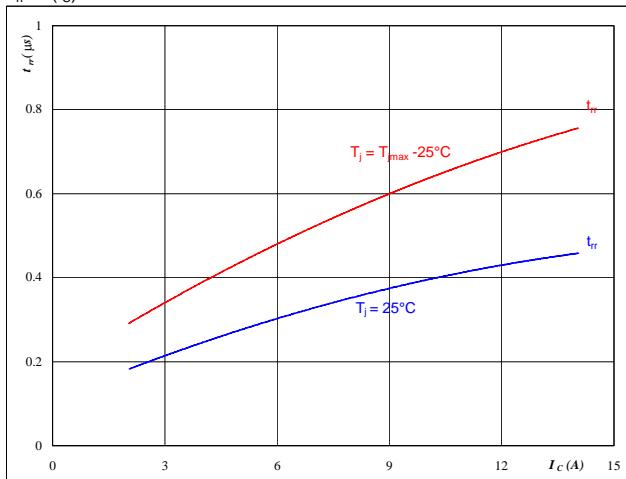
$T_j =$	150	°C
$V_{CE} =$	600	V
$V_{GE} =$	±15	V
$R_{gon} =$	64	Ω
$R_{goff} =$	64	Ω

**Figure 11**

Output inverter FRED

Typical reverse recovery time as a function of collector current

$$t_{rr} = f(I_C)$$



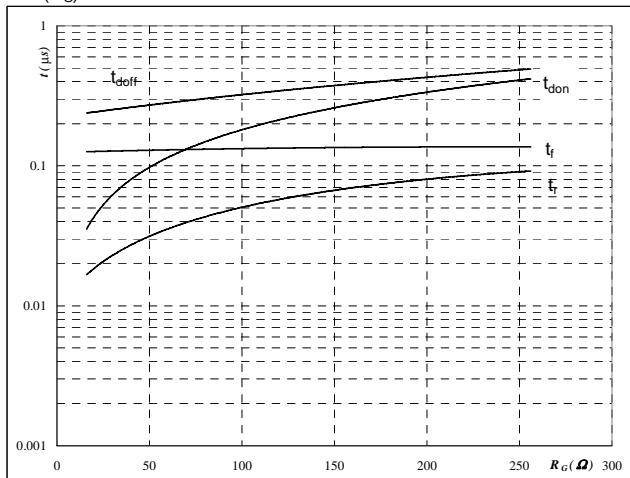
At

$T_j =$	25/150	°C
$V_{CE} =$	600	V
$V_{GE} =$	±15	V
$R_{gon} =$	64	Ω

**Figure 10**

Typical switching times as a function of gate resistor

$$t = f(R_G)$$



With an inductive load at

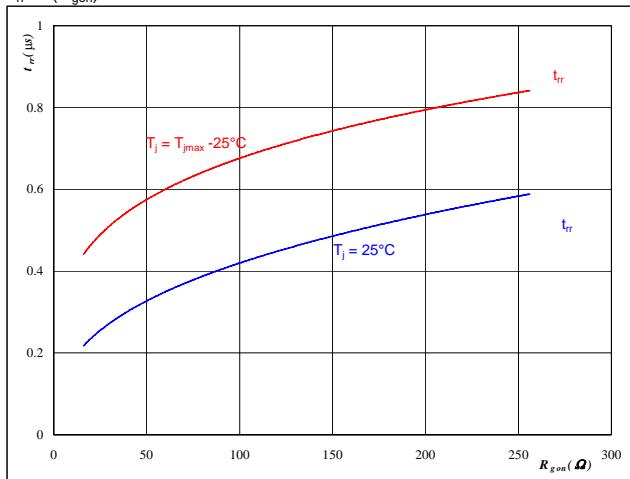
$T_j =$	150	°C
$V_{CE} =$	600	V
$V_{GE} =$	±15	V
$I_C =$	8	A

**Figure 12**

Output inverter FRED

Typical reverse recovery time as a function of IGBT turn on gate resistor

$$t_{rr} = f(R_{gon})$$



At

$T_j =$	25/150	°C
$V_R =$	600	V
$I_F =$	8	A
$V_{GE} =$	±15	V

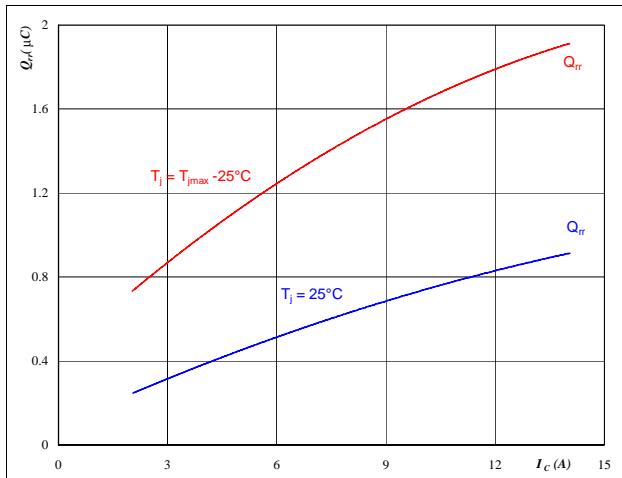
## Output Inverter

**Figure 13**

Output inverter FRED

Typical reverse recovery charge as a function of collector current

$$Q_{rr} = f(I_C)$$


**At**

$$T_j = 25/150 \quad ^\circ C$$

$$V_{CE} = 600 \quad V$$

$$V_{GE} = \pm 15 \quad V$$

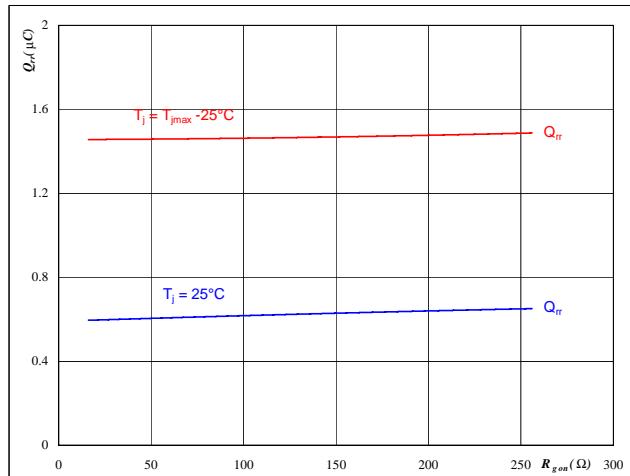
$$R_{gon} = 64 \quad \Omega$$

**Figure 14**

Output inverter FRED

Typical reverse recovery charge as a function of IGBT turn on gate resistor

$$Q_{rr} = f(R_{gon})$$


**At**

$$T_j = 25/150 \quad ^\circ C$$

$$V_R = 600 \quad V$$

$$I_F = 8 \quad A$$

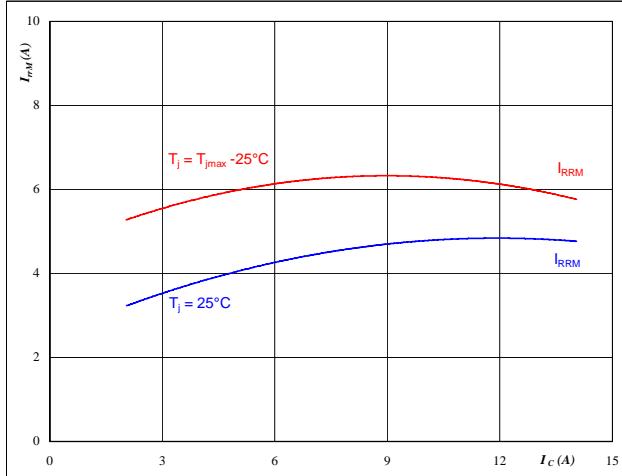
$$V_{GE} = \pm 15 \quad V$$

**Figure 15**

Output inverter FRED

Typical reverse recovery current as a function of collector current

$$I_{RRM} = f(I_C)$$


**At**

$$T_j = 25/150 \quad ^\circ C$$

$$V_{CE} = 600 \quad V$$

$$V_{GE} = \pm 15 \quad V$$

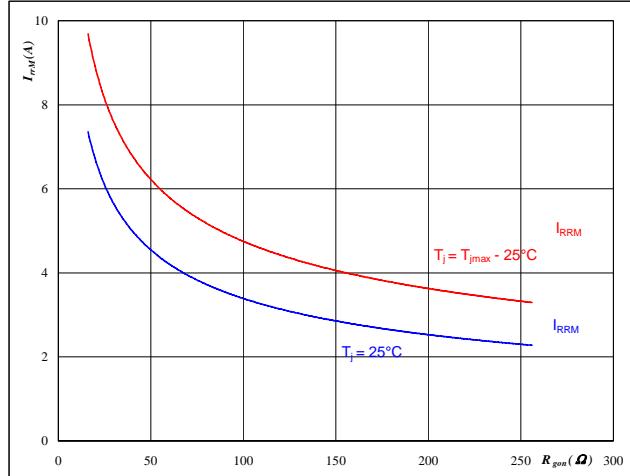
$$R_{gon} = 64 \quad \Omega$$

**Figure 16**

Output inverter FRED

Typical reverse recovery current as a function of IGBT turn on gate resistor

$$I_{RRM} = f(R_{gon})$$


**At**

$$T_j = 25/150 \quad ^\circ C$$

$$V_R = 600 \quad V$$

$$I_F = 8 \quad A$$

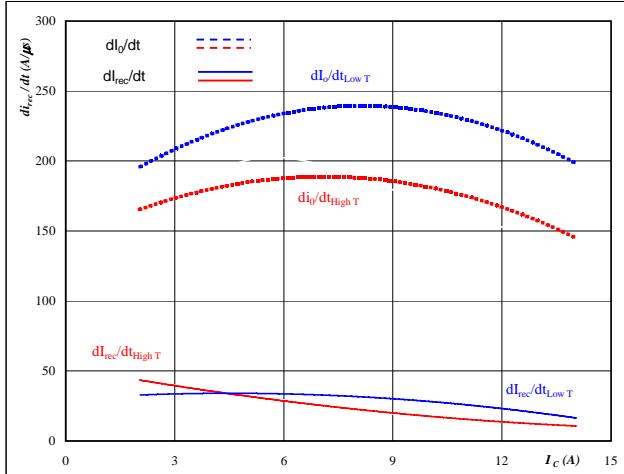
$$V_{GE} = \pm 15 \quad V$$

## Output Inverter

**Figure 17**

Output inverter FRED

**Typical rate of fall of forward and reverse recovery current as a function of collector current**  
 $dI_0/dt, dI_{rec}/dt = f(I_C)$

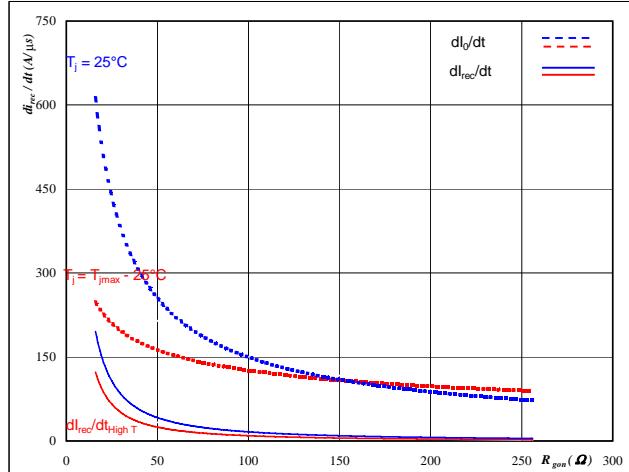

**At**

T<sub>j</sub> = 25/150 °C  
V<sub>CE</sub> = 600 V  
V<sub>GE</sub> = ±15 V  
R<sub>gon</sub> = 64 Ω

**Figure 18**

Output inverter FRED

**Typical rate of fall of forward and reverse recovery current as a function of IGBT turn on gate resistor**  
 $dI_0/dt, dI_{rec}/dt = f(R_{gon})$

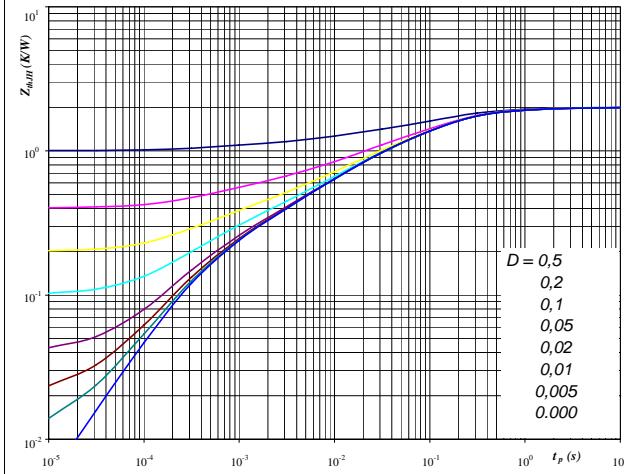

**At**

T<sub>j</sub> = 25/150 °C  
V<sub>R</sub> = 600 V  
I<sub>F</sub> = 8 A  
V<sub>GE</sub> = ±15 V

**Figure 19**

Output inverter IGBT

**IGBT transient thermal impedance as a function of pulse width**  
 $Z_{thJH} = f(t_p)$


**At**

D = t<sub>p</sub> / T  
R<sub>thJH</sub> = 2.01 K/W

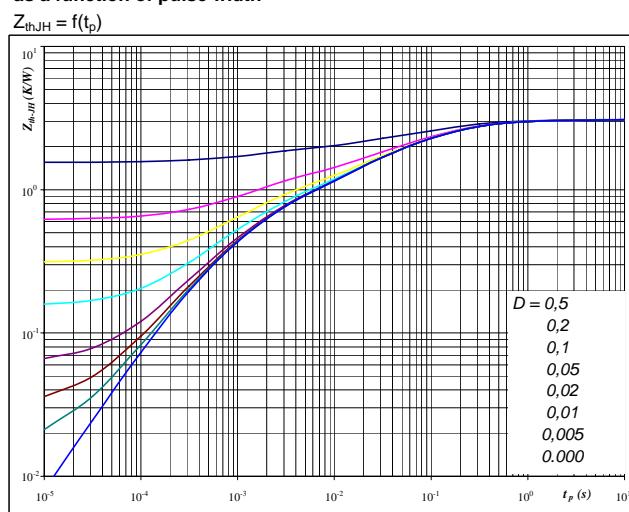
IGBT thermal model values

R (C/W)	Tau (s)
0.05	7.7E+00
0.29	5.4E-01
0.84	1.1E-01
0.47	1.7E-02
0.21	2.6E-03
0.15	3.6E-04

**Figure 20**

Output inverter FRED

**FRED transient thermal impedance as a function of pulse width**  
 $Z_{thJH} = f(t_p)$


**At**

D = t<sub>p</sub> / T  
R<sub>thJH</sub> = 3.10 K/W

FRED thermal model values

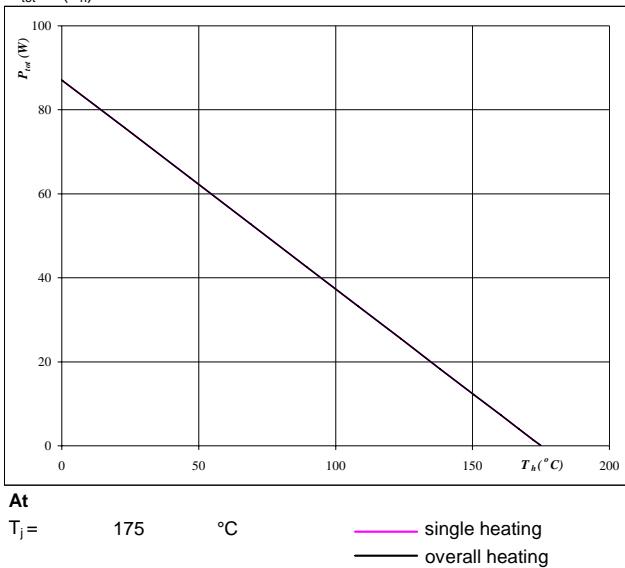
R (C/W)	Tau (s)
0.08	9.0E+00
0.49	4.4E-01
1.25	7.9E-02
0.67	1.2E-02
0.50	1.4E-03
0.11	2.9E-04

## Output Inverter

**Figure 21**

**Power dissipation as a function of heatsink temperature**

$$P_{\text{tot}} = f(T_h)$$

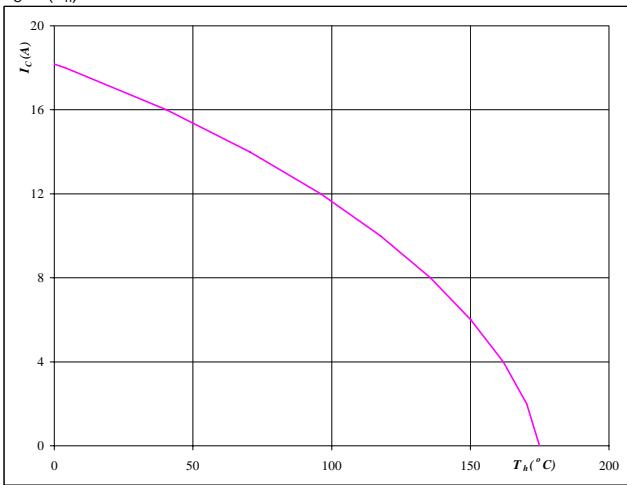

**At**

$$T_j = 175 \quad ^\circ\text{C}$$

**Output inverter IGBT**
**Figure 22**

**Collector current as a function of heatsink temperature**

$$I_C = f(T_h)$$


**At**

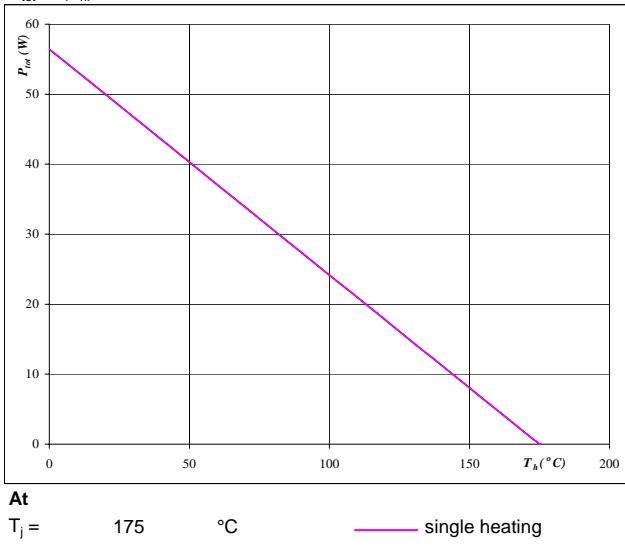
$$T_j = 175 \quad ^\circ\text{C}$$

$$V_{GE} = 15 \quad \text{V}$$

**Figure 23**

**Power dissipation as a function of heatsink temperature**

$$P_{\text{tot}} = f(T_h)$$

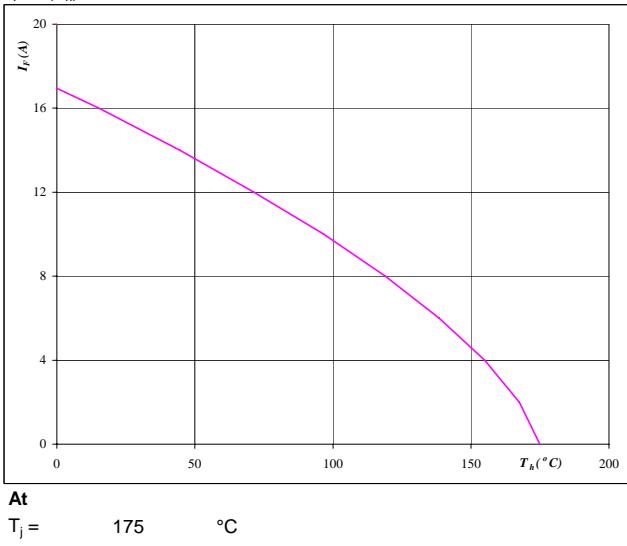

**At**

$$T_j = 175 \quad ^\circ\text{C}$$

**Output inverter IGBT**
**Figure 24**

**Forward current as a function of heatsink temperature**

$$I_F = f(T_h)$$


**At**

$$T_j = 175 \quad ^\circ\text{C}$$

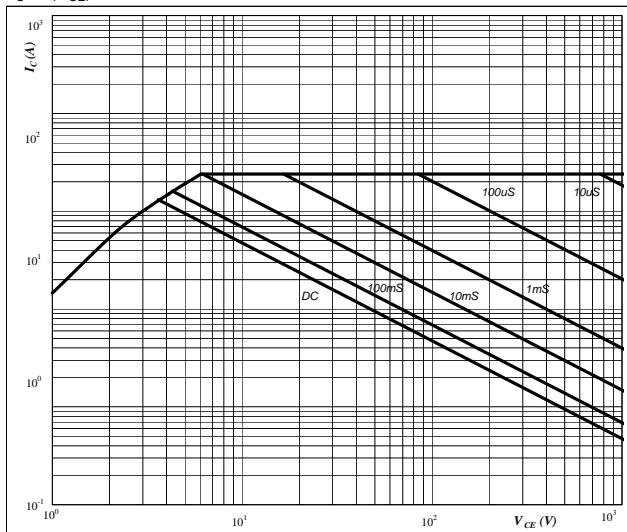
**Output inverter FRED**

## Output Inverter

**Figure 25**

**Safe operating area as a function  
of collector-emitter voltage**

$$I_C = f(V_{CE})$$


**At**

D = single pulse

T<sub>h</sub> = 80 °C

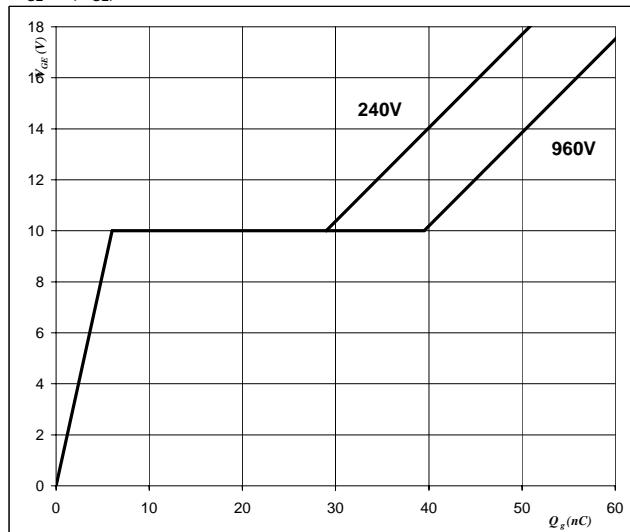
V<sub>GE</sub> = ±15 V

T<sub>j</sub> = T<sub>jmax</sub> °C

**Figure 26**

**Gate voltage vs Gate charge**

$$V_{GE} = f(Q_{GE})$$

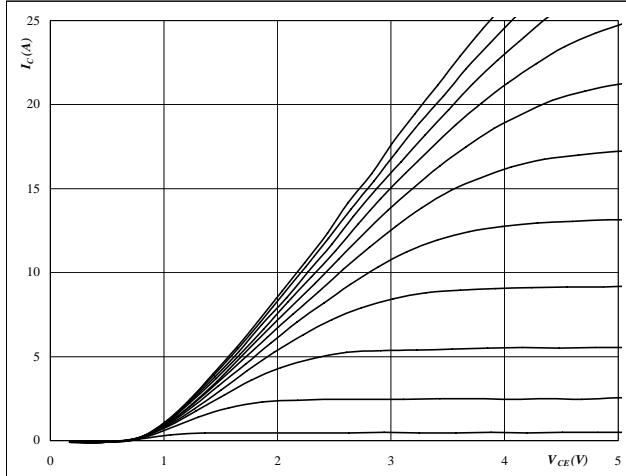

**At**

I<sub>C</sub> = 8 A

## Brake

**Figure 1**
**Typical output characteristics**

$$I_C = f(V_{CE})$$


**At**

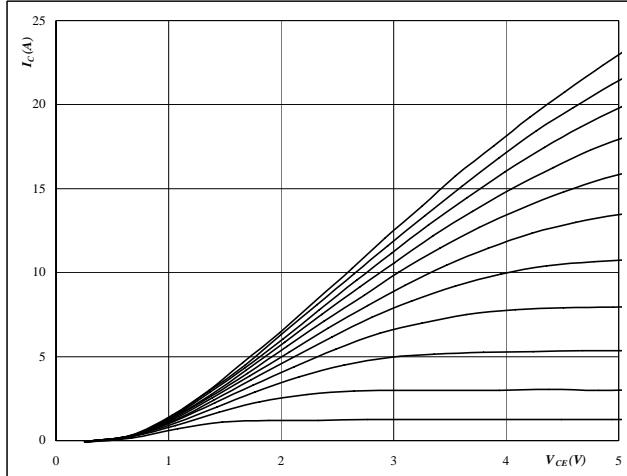
$$t_p = 250 \mu\text{s}$$

$$T_j = 25^\circ\text{C}$$

 $V_{GE}$  from 7 V to 17 V in steps of 1 V

**Brake IGBT**
**Figure 2**
**Typical output characteristics**

$$I_C = f(V_{CE})$$


**At**

$$t_p = 250 \mu\text{s}$$

$$T_j = 150^\circ\text{C}$$

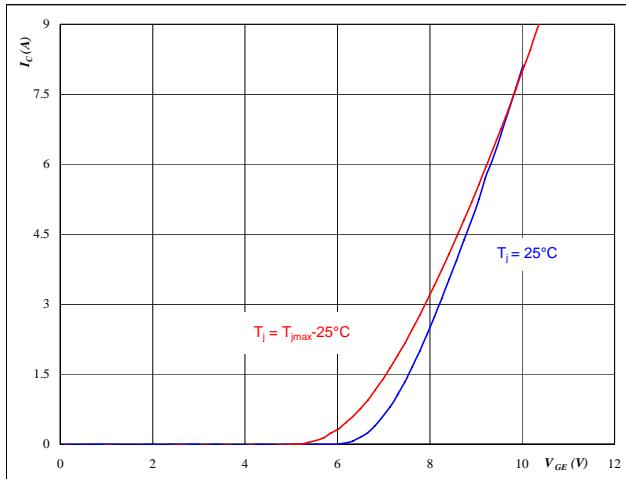
 $V_{GE}$  from 7 V to 17 V in steps of 1 V

**Figure 3**
**Typical transfer characteristics**

$$I_C = f(V_{GE})$$

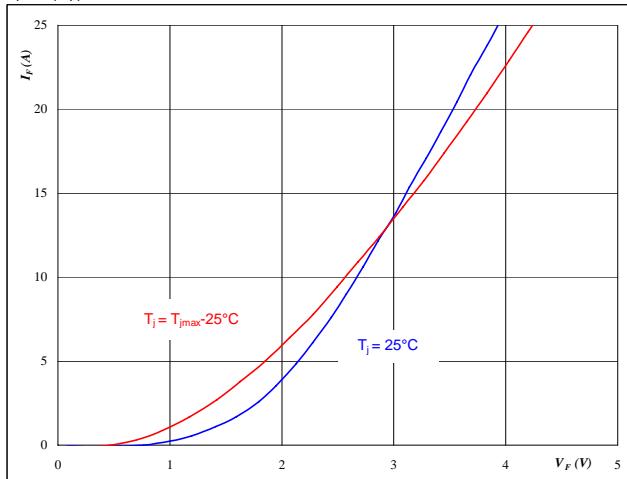
**Brake IGBT**
**Figure 4**
**Typical diode forward current as**
**a function of forward voltage**

$$I_F = f(V_F)$$

**Brake FRED**

**At**

$$t_p = 250 \mu\text{s}$$

$$V_{CE} = 10 \text{ V}$$


**At**

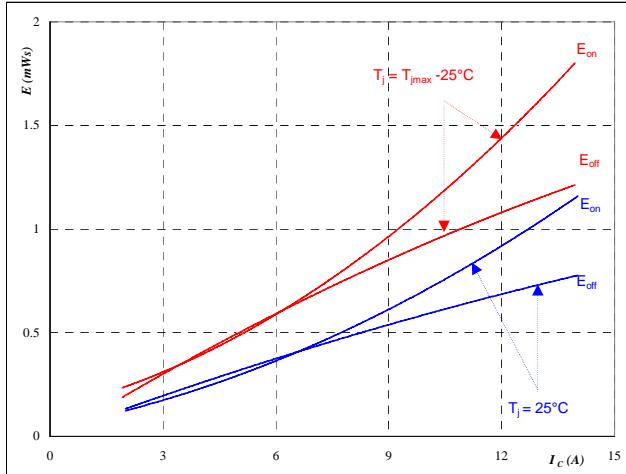
$$t_p = 250 \mu\text{s}$$

## Brake

**Figure 5**

**Typical switching energy losses  
as a function of collector current**

$$E = f(I_C)$$



With an inductive load at

$$T_j = 25/150 \quad ^\circ\text{C}$$

$$V_{CE} = 600 \quad \text{V}$$

$$V_{GE} = \pm 15 \quad \text{V}$$

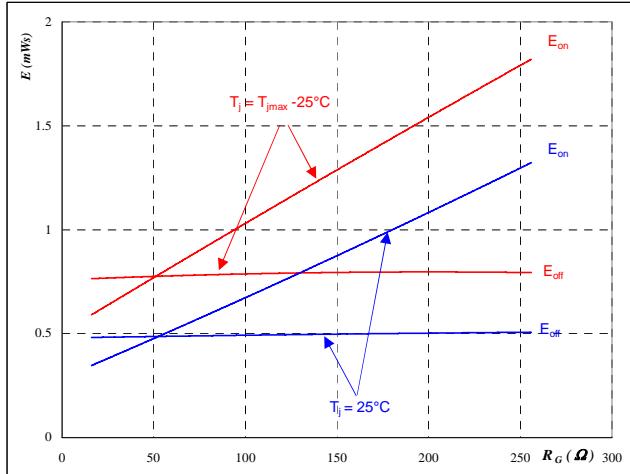
$$R_{gon} = 64 \quad \Omega$$

$$R_{goff} = 64 \quad \Omega$$

**Brake IGBT**
**Figure 6**

**Typical switching energy losses  
as a function of gate resistor**

$$E = f(R_G)$$



With an inductive load at

$$T_j = 25/150 \quad ^\circ\text{C}$$

$$V_{CE} = 600 \quad \text{V}$$

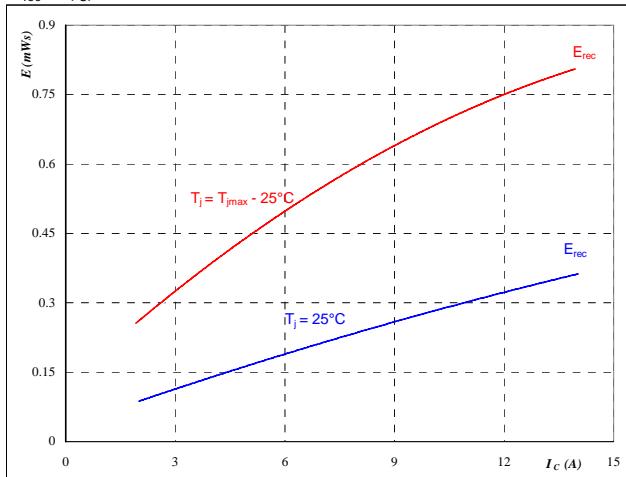
$$V_{GE} = \pm 15 \quad \text{V}$$

$$I_C = 8 \quad \text{A}$$

**Figure 7**

**Typical reverse recovery energy loss  
as a function of collector current**

$$E_{rec} = f(I_C)$$



With an inductive load at

$$T_j = 25/150 \quad ^\circ\text{C}$$

$$V_{CE} = 600 \quad \text{V}$$

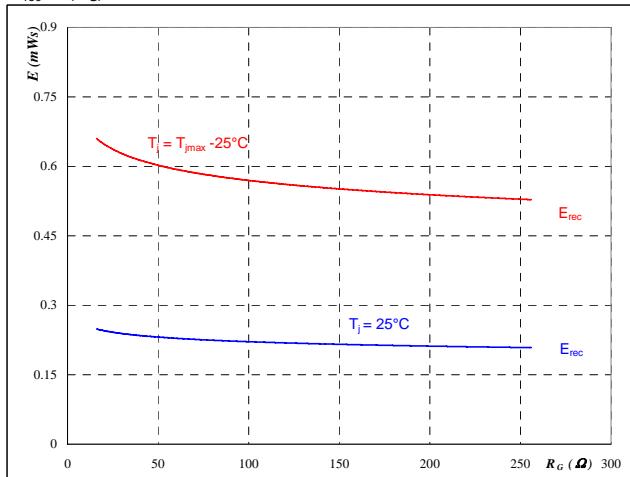
$$V_{GE} = \pm 15 \quad \text{V}$$

$$R_{gon} = 64 \quad \Omega$$

**Brake IGBT**
**Figure 8**

**Typical reverse recovery energy loss  
as a function of gate resistor**

$$E_{rec} = f(R_G)$$



With an inductive load at

$$T_j = 25/150 \quad ^\circ\text{C}$$

$$V_{CE} = 600 \quad \text{V}$$

$$V_{GE} = \pm 15 \quad \text{V}$$

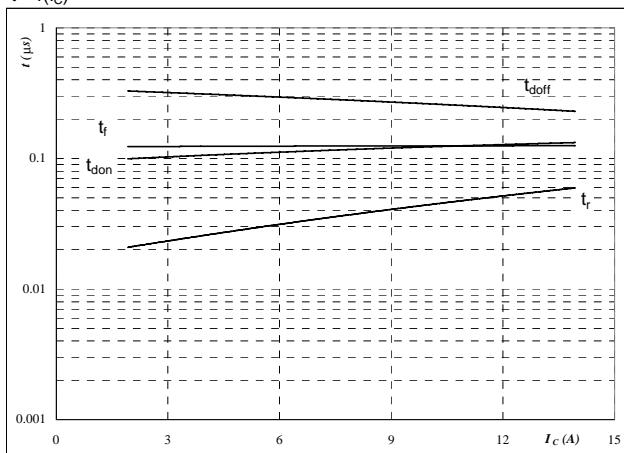
$$I_C = 8 \quad \text{A}$$

## Brake

**Figure 9**

Typical switching times as a function of collector current

$$t = f(I_C)$$



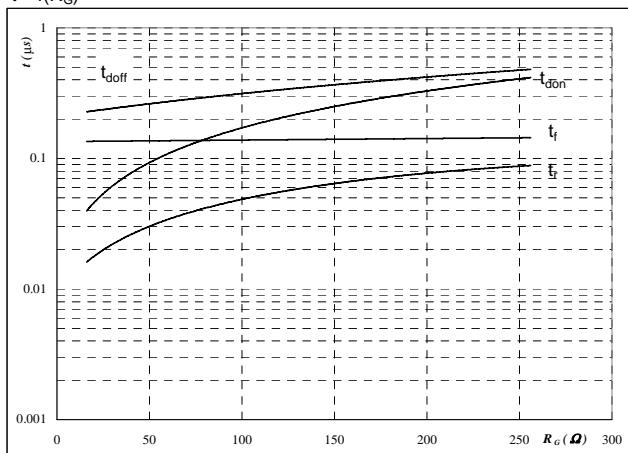
With an inductive load at

$T_j =$	25/150	°C
$V_{CE} =$	600	V
$V_{GE} =$	±15	V
$R_{gon} =$	64	Ω
$R_{goff} =$	64	Ω

**Brake IGBT**
**Figure 10**

Typical switching times as a function of gate resistor

$$t = f(R_G)$$



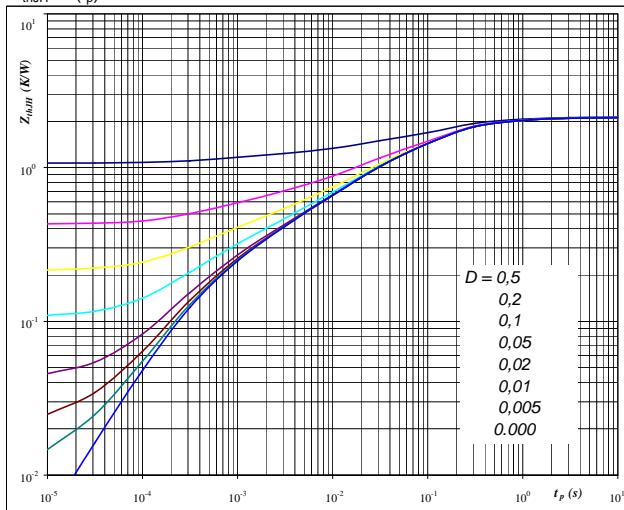
With an inductive load at

$T_j =$	25/150	°C
$V_{CE} =$	600	V
$V_{GE} =$	±15	V
$I_C =$	8	A

**Figure 11**

IGBT transient thermal impedance as a function of pulse width

$$Z_{thJH} = f(t_p)$$



At

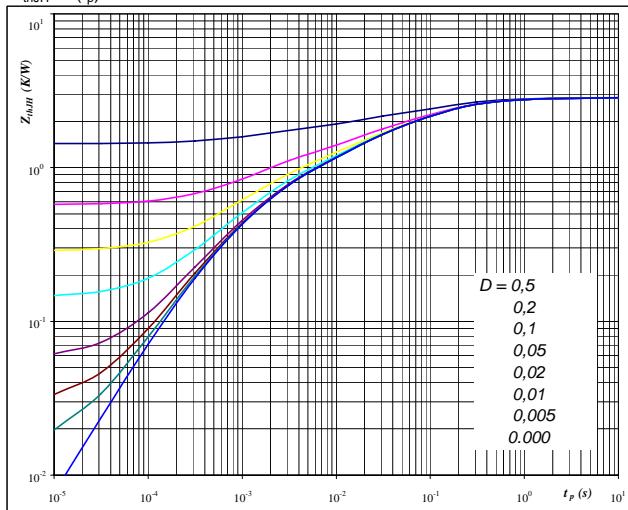
$$D = \frac{t_p}{T}$$

$$R_{thJH} = 2.14 \text{ K/W}$$

**Brake IGBT**
**Figure 12**

FRED transient thermal impedance as a function of pulse width

$$Z_{thJH} = f(t_p)$$



At

$$D = \frac{t_p}{T}$$

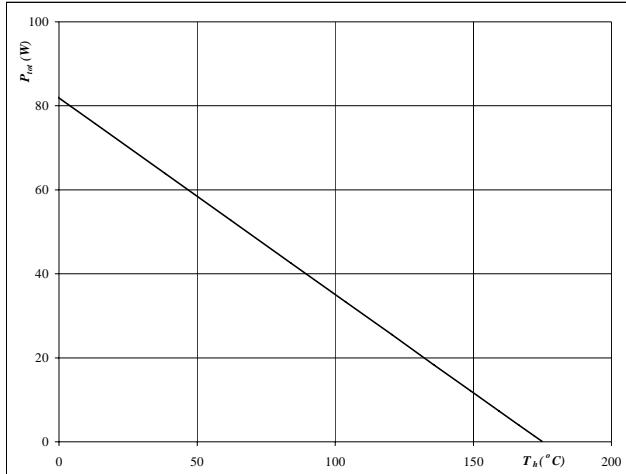
$$R_{thJH} = 2.86 \text{ K/W}$$

## Brake

**Figure 13**

**Power dissipation as a function of heatsink temperature**

$$P_{\text{tot}} = f(T_h)$$


**At**

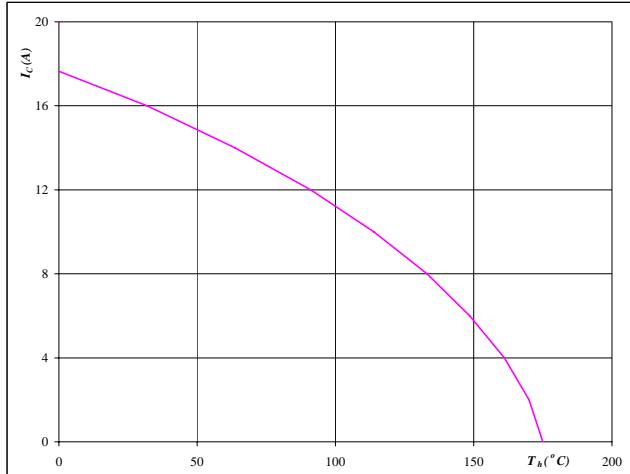
T<sub>j</sub> = 175 °C

Brake IGBT

**Figure 14**

**Collector current as a function of heatsink temperature**

$$I_C = f(T_h)$$


**At**

T<sub>j</sub> = 175 °C

V<sub>GE</sub> = 15 V

**Figure 15**

**Power dissipation as a function of heatsink temperature**

$$P_{\text{tot}} = f(T_h)$$


**At**

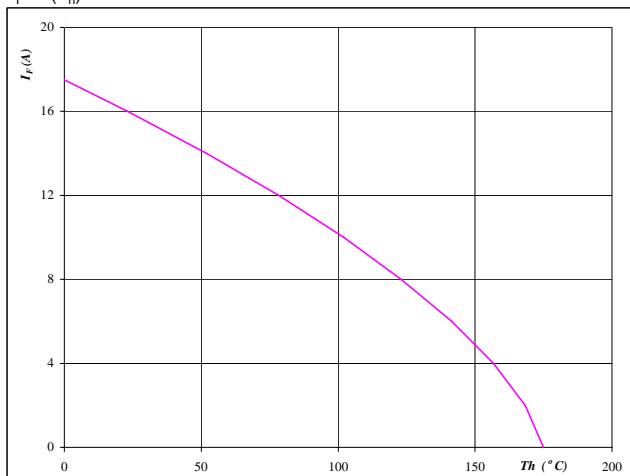
T<sub>j</sub> = 175 °C

Brake FRED

**Figure 16**

**Forward current as a function of heatsink temperature**

$$I_F = f(T_h)$$


**At**

T<sub>j</sub> = 175 °C

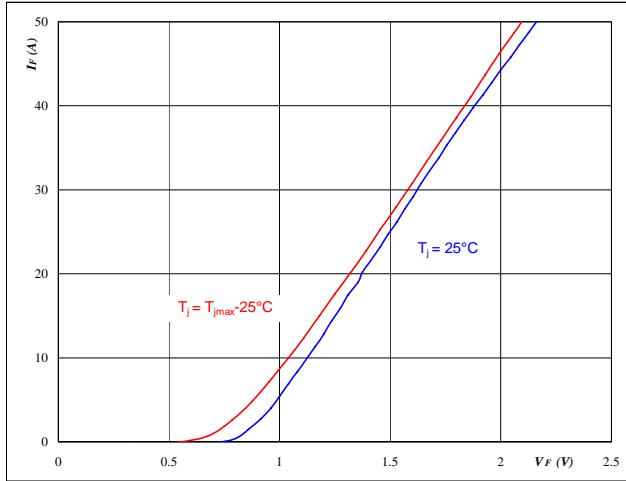
## Input Rectifier Bridge

**Figure 1**

Rectifier diode

Typical diode forward current as  
a function of forward voltage

$$I_F = f(V_F)$$



At

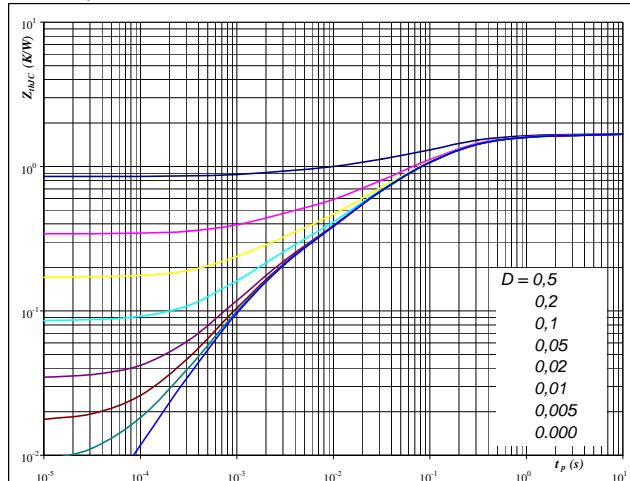
$$t_p = 250 \mu\text{s}$$

**Figure 2**

Rectifier diode

Diode transient thermal impedance  
as a function of pulse width

$$Z_{thJH} = f(t_p)$$



At

$$D = t_p / T$$

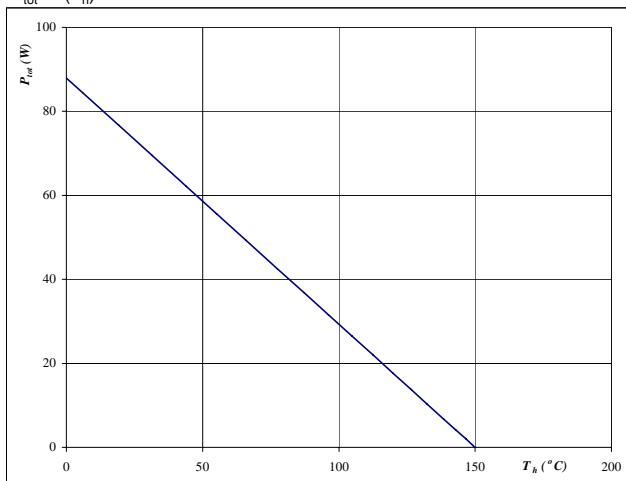
$$R_{thJH} = 1.706 \text{ K/W}$$

**Figure 3**

Rectifier diode

Power dissipation as a  
function of heatsink temperature

$$P_{tot} = f(T_h)$$



At

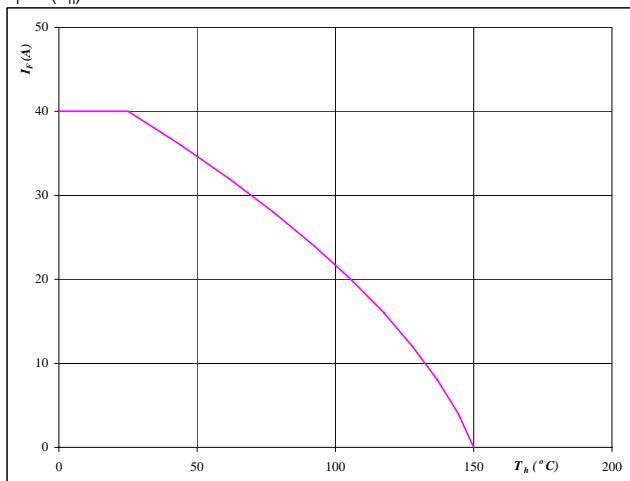
$$T_j = 150 ^\circ\text{C}$$

**Figure 4**

Rectifier diode

Forward current as a  
function of heatsink temperature

$$I_F = f(T_h)$$



At

$$T_j = 150 ^\circ\text{C}$$

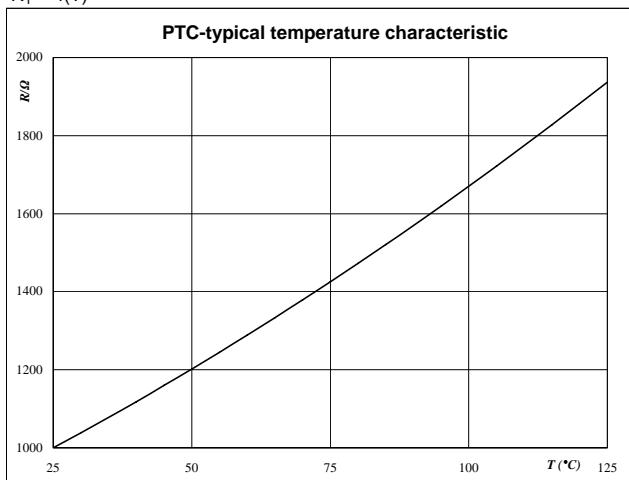
## Thermistor

**Figure 1**

Thermistor

Typical PTC characteristic  
as a function of temperature

$$R_T = f(T)$$



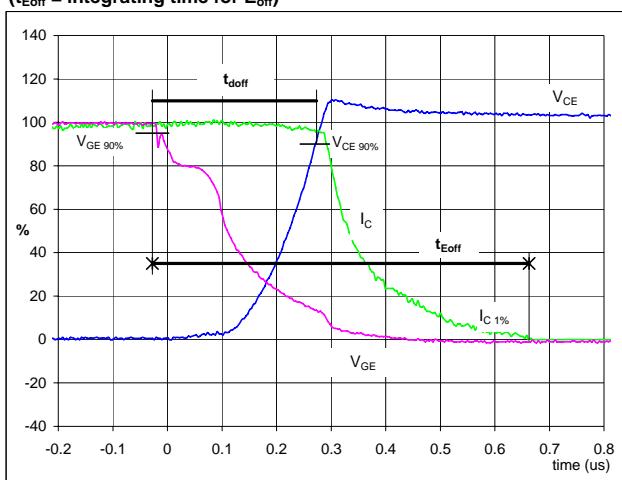
## Switching Definitions Output Inverter

**General conditions**

$T_j$	=	150 °C
$R_{gon}$	=	64 Ω
$R_{goff}$	=	64 Ω

**Figure 1**

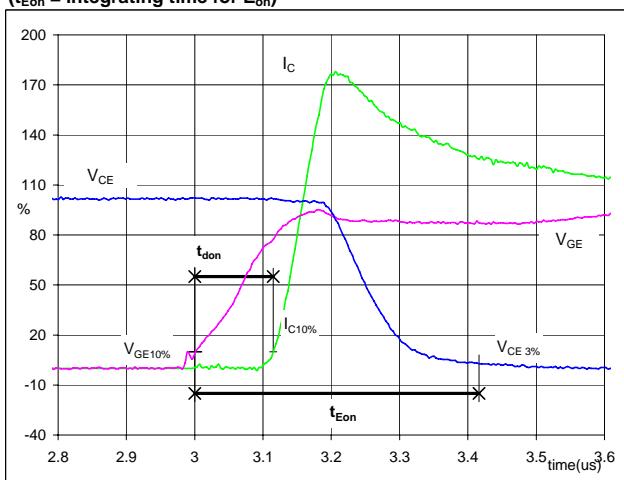
Output inverter IGBT

Turn-off Switching Waveforms & definition of  $t_{doff}$ ,  $t_{Eoff}$   
( $t_{Eoff}$  = integrating time for  $E_{off}$ )


$V_{GE}(0\%) =$	-15	V
$V_{GE}(100\%) =$	15	V
$V_C(100\%) =$	600	V
$I_C(100\%) =$	8	A
$t_{doff} =$	0.29	μs
$t_{Eoff} =$	0.69	μs

**Figure 2**

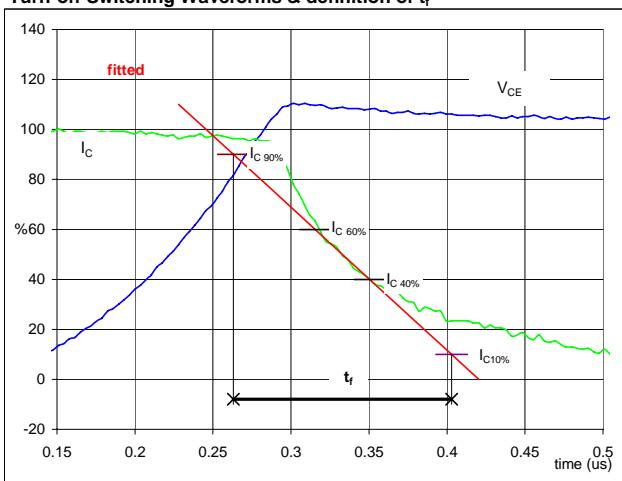
Output inverter IGBT

Turn-on Switching Waveforms & definition of  $t_{don}$ ,  $t_{Eon}$   
( $t_{Eon}$  = integrating time for  $E_{on}$ )


$V_{GE}(0\%) =$	-15	V
$V_{GE}(100\%) =$	15	V
$V_C(100\%) =$	600	V
$I_C(100\%) =$	8	A
$t_{don} =$	0.13	μs
$t_{Eon} =$	0.42	μs

**Figure 3**

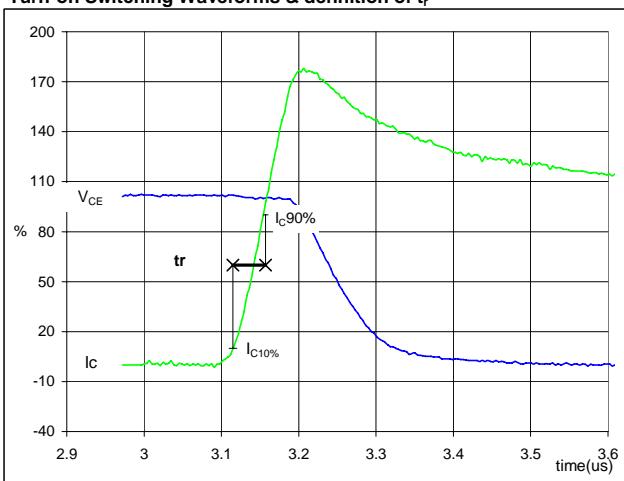
Output inverter IGBT

Turn-off Switching Waveforms & definition of  $t_f$ 


$V_C(100\%) =$	600	V
$I_C(100\%) =$	8	A
$t_f =$	0.13	μs

**Figure 4**

Output inverter IGBT

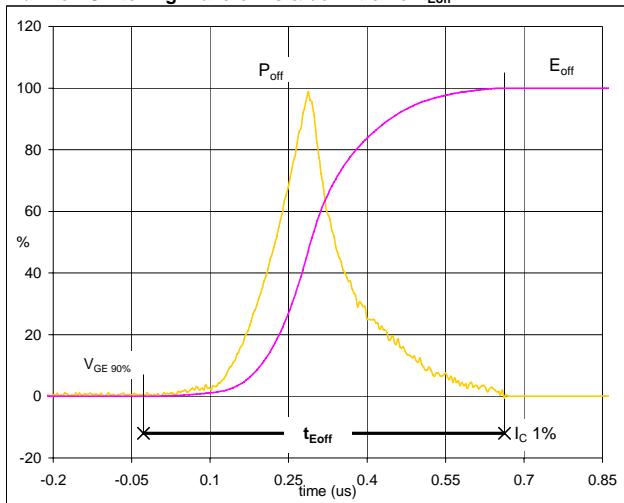
Turn-on Switching Waveforms & definition of  $t_r$ 


$V_C(100\%) =$	600	V
$I_C(100\%) =$	8	A
$t_r =$	0.04	μs

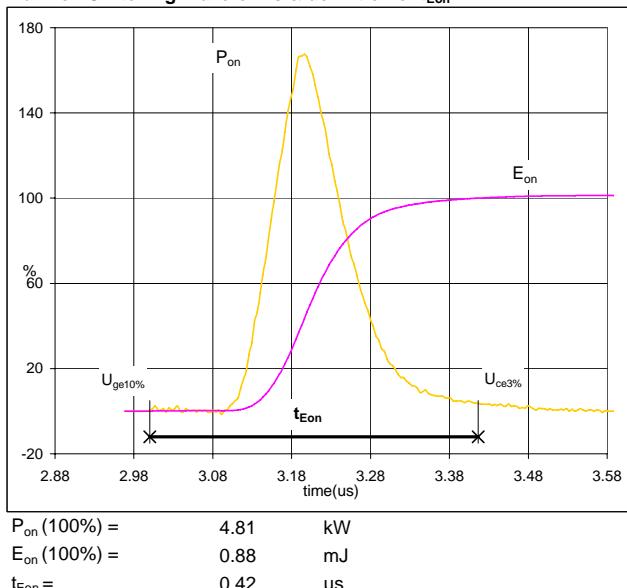
## Switching Definitions Output Inverter

**Figure 5**

Output inverter IGBT

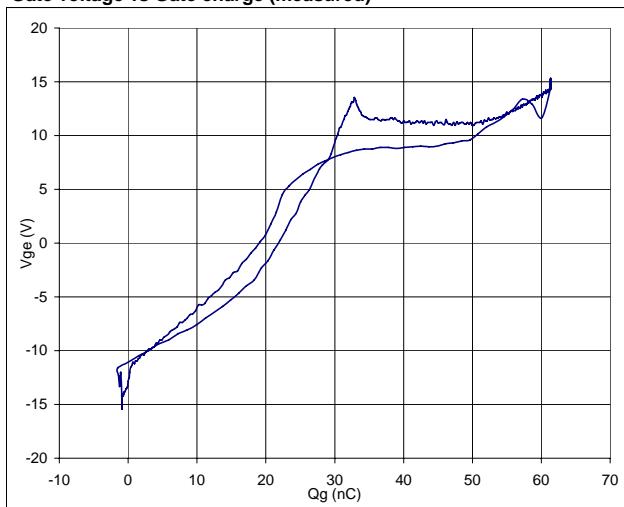
Turn-off Switching Waveforms & definition of  $t_{Eoff}$ 

**Figure 6**

Output inverter IGBT

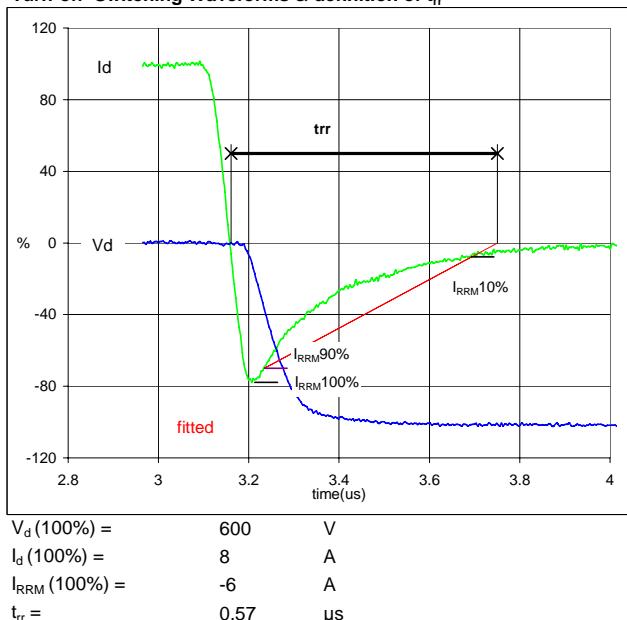
Turn-on Switching Waveforms & definition of  $t_{Eon}$ 

**Figure 7**

Output inverter FRED

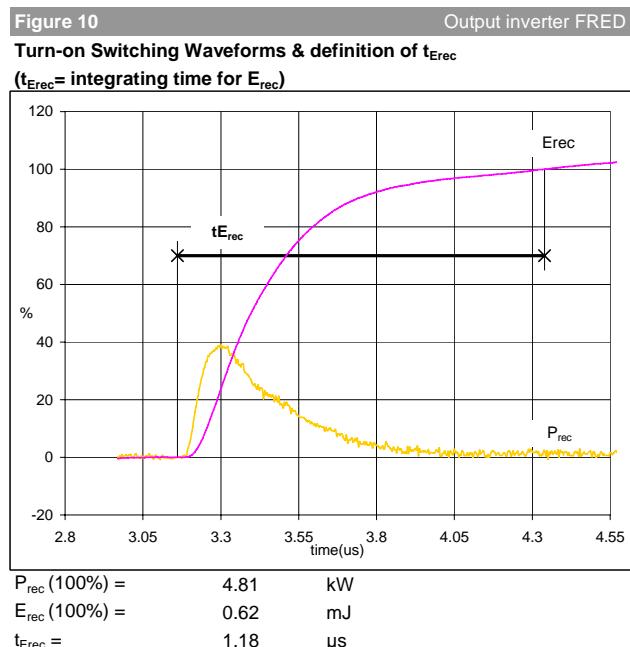
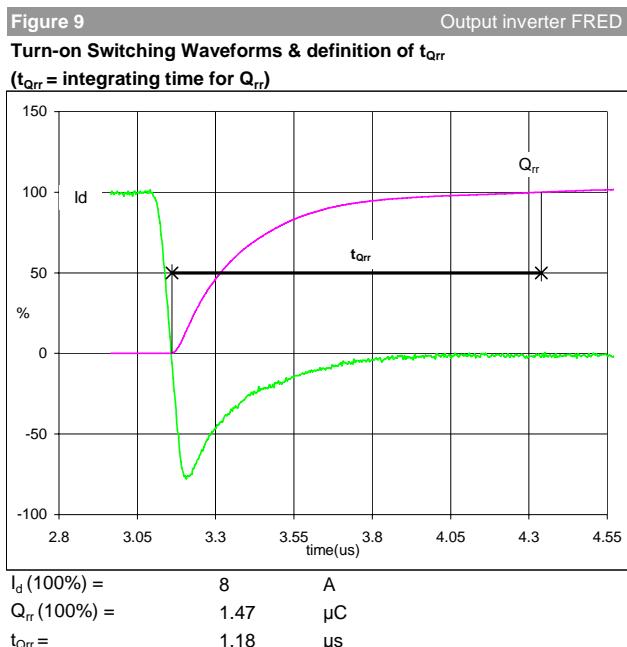
Gate voltage vs Gate charge (measured)


**Figure 8**

Output inverter IGBT

Turn-off Switching Waveforms & definition of  $t_{trr}$ 


## Switching Definitions Output Inverter

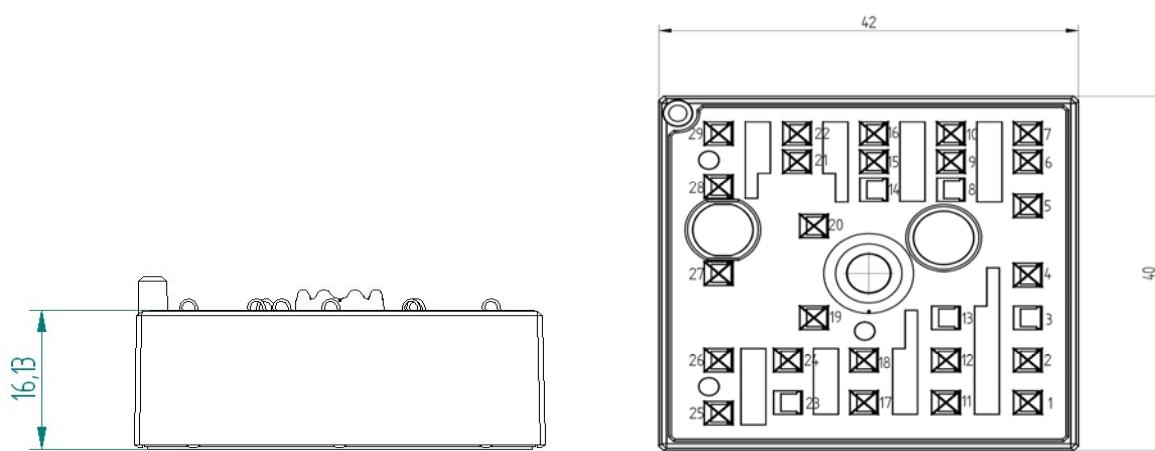


## Ordering Code and Marking - Outline - Pinout

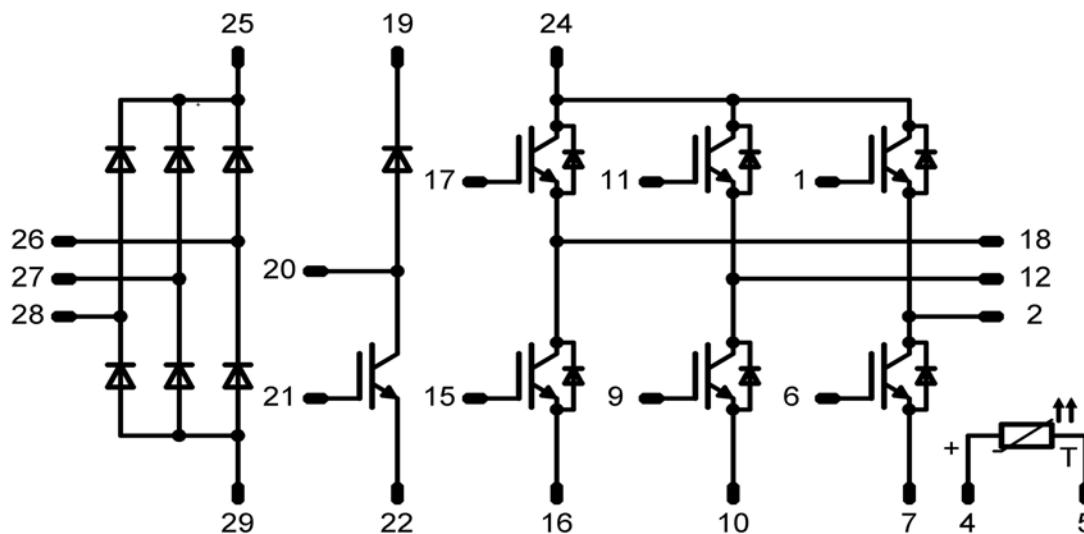
### Ordering Code & Marking

Version	Ordering Code	in DataMatrix as	in packaging barcode as
with std lid (black V23990-K12-T-PM)	V23990-K209-A40-/0A-/PM	K209A40	K209A40-/0A/
with std lid (black V23990-K12-T-PM) and P12	V23990-K209-A40-/1A-/PM	K209A40	K209A40-/1A/
with thin lid (white V23990-K13-T-PM)	V23990-K209-A40-/0B-/PM	K209A40	K209A40-/0B/
with thin lid (white V23990-K13-T-PM) and P12	V23990-K209-A40-/1B-/PM	K209A40	K209A40-/1B/

### Outline



### Pinout



**PRODUCT STATUS DEFINITIONS**

Datasheet Status	Product Status	Definition
Target	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice. The data contained is exclusively intended for technically trained staff.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data may be published at a later date. Vincotech reserves the right to make changes at any time without notice in order to improve design. The data contained is exclusively intended for technically trained staff.
Final	Full Production	This datasheet contains final specifications. Vincotech reserves the right to make changes at any time without notice in order to improve design. The data contained is exclusively intended for technically trained staff.

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2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.