

flowPIM 0		600V/30A
<p>Features</p> <ul style="list-style-type: none"> • Vincotech clip-in housing • Trench Fieldstop IGBT's for low saturation losses • Optional w/o BRC 		<p>flowPIM 0 housing</p> <p>12mm housing 17mm housing</p>
<p>Target Applications</p> <ul style="list-style-type: none"> • Industrial drives • Embedded drives 		
<p>Types</p> <ul style="list-style-type: none"> • V23990-P546-A38-PM • V23990-P546-A39-PM • V23990-P546-C38-PMw/o BRC • V23990-P546-C39-PMw/o BRC 		<p>Schematic</p>

Maximum Ratings

$T_j=25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
Input Rectifier Diode				
Repetitive peak reverse voltage	V_{RRM}		1600	V
DC forward current	I_{FAV}	$T_j=T_{j,\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	33 46	A
Surge forward current	I_{FSM}	$t_p=10\text{ms}$ $50\text{Hz half sine wave}$ $T_j=25^\circ\text{C}$	250	A
I^2t -value	I^2t		310	A^2s
Power dissipation per Diode	P_{tot}	$T_j=T_{j,\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	37 59	W
Maximum Junction Temperature	$T_{j,\max}$		150	$^\circ\text{C}$
Inverter IGBT				
Collector-emitter break down voltage	V_{CE}		600	V
DC collector current	I_C	$T_j=T_{j,\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	27 36	A
Repetitive peak collector current	I_{Cpulse}	t_p limited by $T_{j,\max}$	90	A
Turn off safe operating area		$V_{CE} \leq 600\text{V}$, $T_j \leq T_{j,\max}$	90	A
Power dissipation per IGBT	P_{tot}	$T_j=T_{j,\max}$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	54 82	W
Gate-emitter peak voltage	V_{GE}		± 20	V
Short circuit ratings	t_{sc} V_{CC}	$T_j \leq 150^\circ\text{C}$ $V_{GE}=15\text{V}$	6 360	μs V
Maximum Junction Temperature	$T_{j,\max}$		175	$^\circ\text{C}$

Maximum Ratings

$T_j=25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit	
Inverter FWD					
Peak Repetitive Reverse Voltage	V_{RRM}		600	V	
DC forward current	I_F	$T_j=T_j\max$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	25 32	A	
Repetitive peak forward current	I_{FRM}	t_p limited by $T_j\max$	60	A	
Power dissipation per Diode	P_{tot}	$T_j=T_j\max$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	40 60	W	
Maximum Junction Temperature	$T_j\max$		175	$^\circ\text{C}$	
Brake IGBT					
Collector-emitter break down voltage	V_{CE}		600	V	
DC collector current	I_C	$T_j=T_j\max$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	21 27	A	
Repetitive peak collector current	I_{Cpuls}	t_p limited by $T_j\max$	60	A	
Turn off safe operating area		$V_{CE} \leq 600\text{V}$, $T_j \leq T_{j\max}$	60	A	
Power dissipation per IGBT	P_{tot}	$T_j=T_j\max$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	41 63	W	
Gate-emitter peak voltage	V_{GE}		± 20	V	
Short circuit ratings	t_{sc} V_{CC}	$T_j \leq 150^\circ\text{C}$ $V_{GE}=15\text{V}$	6 360	μs V	
Maximum Junction Temperature	$T_j\max$		175	$^\circ\text{C}$	
Brake FWD					
Peak Repetitive Reverse Voltage	V_{RRM}		600	V	
DC forward current	I_F	$T_j=T_j\max$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	18 25	A	
Repetitive peak forward current	I_{FRM}	t_p limited by $T_j\max$	40	A	
Power dissipation per Diode	P_{tot}	$T_j=T_j\max$ $T_h=80^\circ\text{C}$ $T_c=80^\circ\text{C}$	31 47	W	
Maximum Junction Temperature	$T_j\max$		175	$^\circ\text{C}$	
Thermal Properties					
Storage temperature	T_{stg}		-40...+125	$^\circ\text{C}$	
Operation temperature under switching condition	T_{op}		-40...+($T_{j\max} - 25$)	$^\circ\text{C}$	
Insulation Properties					
Insulation voltage	V_{is}	$t=2\text{s}$	DC voltage	4000	V
Creepage distance				min 12,7	mm
Clearance				min 12,7	mm
Comparative tracking index	CTI			>200	

Characteristic Values

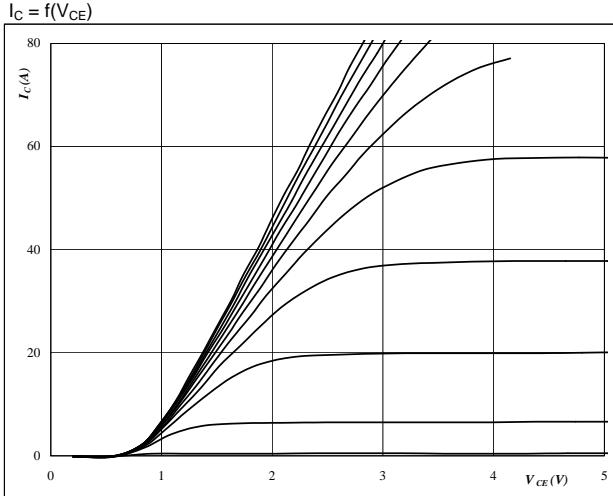
Parameter	Symbol	Conditions					Value			Unit
			V _{GE} [V] or V _{Gs} [V]	V _r [V] or V _{CE} [V] or V _{DS} [V]	I _C [A] or I _F [A] or I _D [A]	T _j	Min	Typ	Max	
Input Rectifier Diode										
Forward voltage	V _F				30	T _j =25°C T _j =125°C	0,8	1,16 1,13	1,6	V
Threshold voltage (for power loss calc. only)	V _{to}				30	T _j =25°C T _j =125°C		0,90 0,78		V
Slope resistance (for power loss calc. only)	R _t				30	T _j =25°C T _j =125°C		8 11		mΩ
Reverse current	I _r			1500		T _j =25°C T _j =150°C			2	mA
Thermal resistance chip to heatsink per chip	R _{thJH}	Thermal grease thickness≤50um λ = 1 W/mK						1,89		K/W
Inverter IGBT										
Gate emitter threshold voltage	V _{GE(th)}	V _{CE} =V _{GE}			0,00043	T _j =25°C T _j =150°C	5	5,8	6,5	V
Collector-emitter saturation voltage	V _{CE(sat)}		15		50	T _j =25°C T _j =150°C	1,1	1,67 1,90	1,9	V
Collector-emitter cut-off current incl. Diode	I _{CES}		0	600		T _j =25°C T _j =150°C			0,0016	mA
Gate-emitter leakage current	I _{GES}		20	0		T _j =25°C T _j =150°C			300	nA
Integrated Gate resistor	R _{gint}							none		Ω
Turn-on delay time	t _{d(on)}	R _{goff} =4 Ω R _{gon} =8 Ω	±15	300	30	T _j =25°C T _j =150°C		17		ns
Rise time	t _r					T _j =25°C T _j =150°C		18		
Turn-off delay time	t _{d(off)}					T _j =25°C T _j =150°C		16		
Fall time	t _f					T _j =25°C T _j =150°C		18		
Turn-on energy loss per pulse	E _{on}					T _j =25°C T _j =150°C		156		mWs
Turn-off energy loss per pulse	E _{off}					T _j =25°C T _j =150°C		172		
Input capacitance	C _{res}					T _j =25°C T _j =150°C		88		
Output capacitance	C _{oss}	f=1MHz	0	25	T _j =25°C	T _j =25°C T _j =150°C		101		pF
Reverse transfer capacitance	C _{rss}					T _j =25°C T _j =150°C		0,52		
Gate charge	Q _{Gate}					T _j =25°C T _j =150°C		0,71		
Thermal resistance chip to heatsink per chip	R _{thJH}	Thermal grease thickness≤50um λ = 1 W/mK				T _j =25°C T _j =150°C		0,72		
						T _j =25°C T _j =150°C		0,90		
Inverter FWD										
Diode forward voltage	V _F				30	T _j =25°C T _j =150°C	1,25	1,64 1,66	1,95	V
Peak reverse recovery current	I _{RRM}	R _{gon} =8 Ω	±15	300	30	T _j =25°C T _j =150°C		25		A
Reverse recovery time	t _{rr}					T _j =25°C T _j =150°C		28		
Reverse recovered charge	Q _{rr}					T _j =25°C T _j =150°C		176		
Peak rate of fall of recovery current	di(rec)max/dt					T _j =25°C T _j =150°C		256		ns
Reverse recovered energy	E _{rec}					T _j =25°C T _j =150°C		1,36 2,45		μC
						T _j =25°C T _j =150°C		1521 932		A/μs
Thermal resistance chip to heatsink per chip	R _{thJH}	Thermal grease thickness≤50um λ = 1 W/mK				T _j =25°C T _j =150°C		0,27 0,51		mWs
						T _j =25°C T _j =150°C		2,40		K/W

Characteristic Values

Parameter	Symbol	Conditions					Value			Unit	
			V _{GE} [V] or V _{GS} [V]	V _r [V] or V _{CE} [V] or V _{DS} [V]	I _C [A] or I _F [A] or I _D [A]	T _j	Min	Typ	Max		
Brake IGBT											
Gate emitter threshold voltage	V _{GE(th)}	V _{CE} =V _{GE}			0,00029	T _j =25°C T _j =150°C	5	5,8	6,5	V	
Collector-emitter saturation voltage	V _{CE(sat)}		15		20	T _j =25°C T _j =150°C	1	1,58 1,76	2,2	V	
Collector-emitter cut-off incl diode	I _{CES}		0	600		T _j =25°C T _j =150°C			0,0011	mA	
Gate-emitter leakage current	I _{GES}		20	0		T _j =25°C T _j =150°C			300	nA	
Integrated Gate resistor	R _{gint}							none		Ω	
Turn-on delay time	t _{d(on)}	R _{goff} =8 Ω R _{gon} =16 Ω	±15	300	20	T _j =25°C T _j =150°C	15 14			ns	
Rise time	t _r					T _j =25°C T _j =150°C	12 15				
Turn-off delay time	t _{d(off)}					T _j =25°C T _j =150°C	197 220				
Fall time	t _f					T _j =25°C T _j =150°C	100 119				
Turn-on energy loss per pulse	E _{on}					T _j =25°C T _j =150°C	0,31 0,43			mWs	
Turn-off energy loss per pulse	E _{off}					T _j =25°C T _j =150°C	0,53 0,67				
Input capacitance	C _{res}							1100		pF	
Output capacitance	C _{oss}	f=1MHz	0	25		T _j =25°C		71			
Reverse transfer capacitance	C _{rss}							32			
Gate charge	Q _{Gate}					T _j =25°C		120		nC	
Thermal resistance chip to heatsink per chip	R _{thJH}	Thermal grease thickness≤50um λ = 1 W/mK						2,30		K/W	
Brake FWD											
Diode forward voltage	V _F				20	T _j =25°C T _j =150°C	1,25 1,76	1,83 1,76	1,95	V	
Reverse leakage current	I _r	R _{gon} =16 Ω		300		T _j =25°C T _j =150°C			27	μA	
Peak reverse recovery current	I _{RRM}	R _{gon} =16 Ω	±15	300	20	T _j =25°C T _j =150°C	18 21			A	
Reverse recovery time	t _{rr}					T _j =25°C T _j =150°C	31 197			ns	
Reverse recovered charge	Q _{rr}					T _j =25°C T _j =150°C	0,39 0,39			μC	
Peak rate of fall of recovery current	di(rec)max /dt					T _j =25°C T _j =150°C	1762 927			A/μs	
Reverse recovery energy	E _{rec}					T _j =25°C T _j =150°C	0,05 0,25			mWs	
Thermal resistance chip to heatsink per chip	R _{thJH}	Thermal grease thickness≤50um λ = 1 W/mK						3,04		K/W	
Thermistor											
Rated resistance	R					T _j =25°C		22000		Ω	
Deviation of R100	ΔR/R	R100=1486 Ω				T _c =100°C	-5		5	%	
Power dissipation	P					T _c =100°C		210		mW	
Power dissipation constant						T _j =25°C		3,5		mW/K	
B-value	B _(25/50)	Tol. ±3%				T _j =25°C				K	
B-value	B _(25/100)	Tol. ±3%				T _j =25°C		4000		K	
Vincotech NTC Reference						T _j =25°C			A		

Output Inverter

Figure 1
Typical output characteristics
 $I_C = f(V_{CE})$



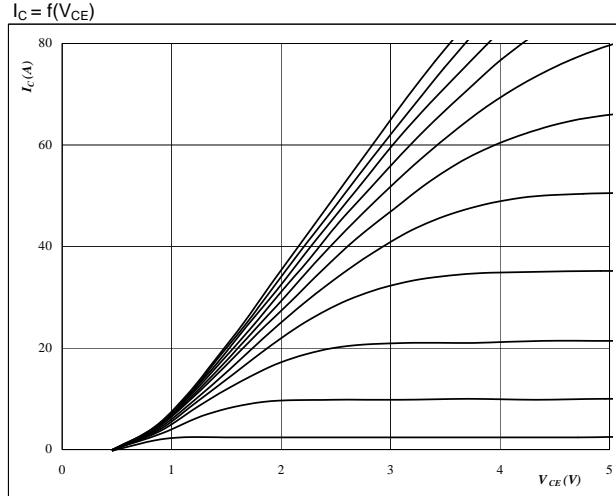
At

$t_p = 250 \mu s$

$T_j = 25^\circ C$

V_{GE} from 7 V to 17 V in steps of 1 V

Figure 2
Typical output characteristics
 $I_C = f(V_{CE})$



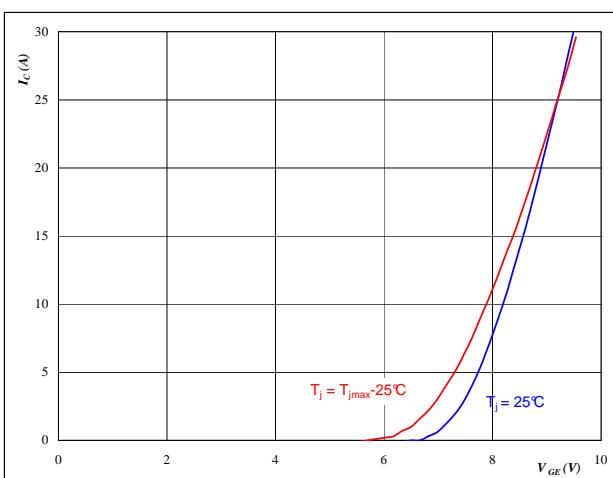
At

$t_p = 250 \mu s$

$T_j = 125^\circ C$

V_{GE} from 7 V to 17 V in steps of 1 V

Figure 3
Typical transfer characteristics
 $I_C = f(V_{GE})$

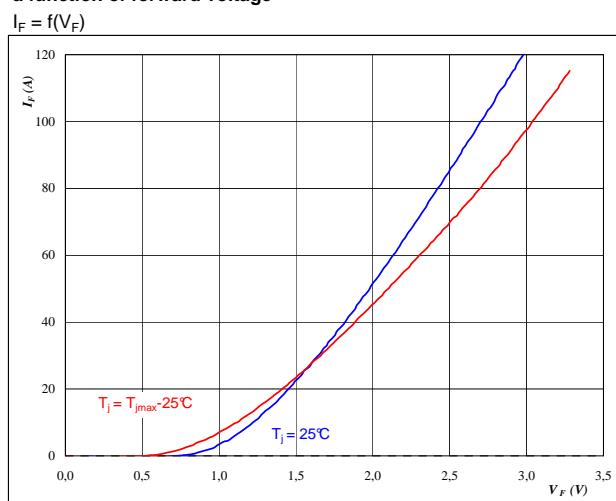


At

$t_p = 250 \mu s$

$V_{CE} = 10 V$

Figure 4
Typical diode forward current as a function of forward voltage
 $I_F = f(V_F)$

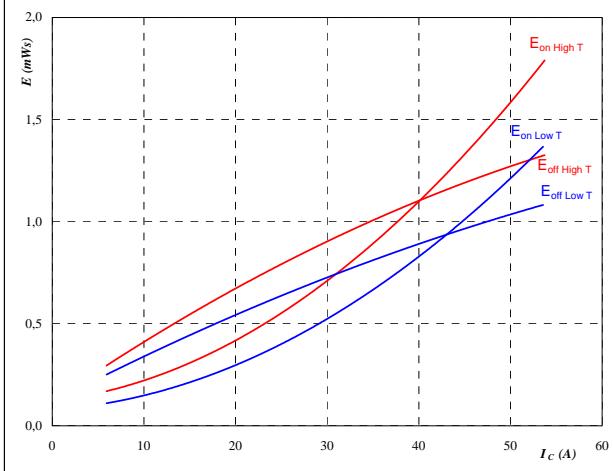


At

$t_p = 250 \mu s$

Output Inverter

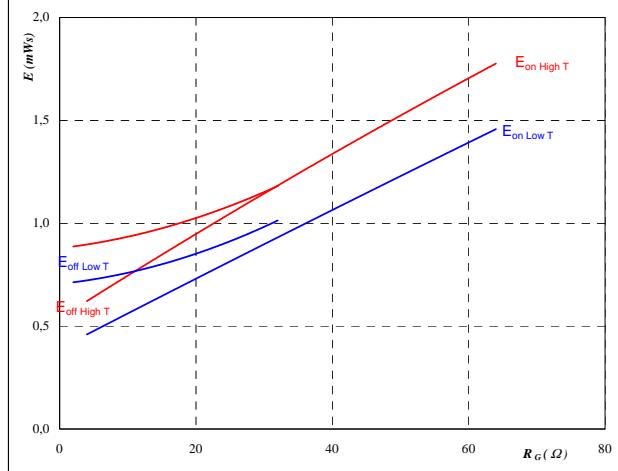
Figure 5 Output inverter IGBT
Typical switching energy losses as a function of collector current
 $E = f(I_C)$



With an inductive load at

$T_j = 25/125 \text{ }^\circ\text{C}$
 $V_{CE} = 300 \text{ V}$
 $V_{GE} = 15 \text{ V}$
 $R_{gon} = 8 \Omega$
 $R_{goff} = 4 \Omega$

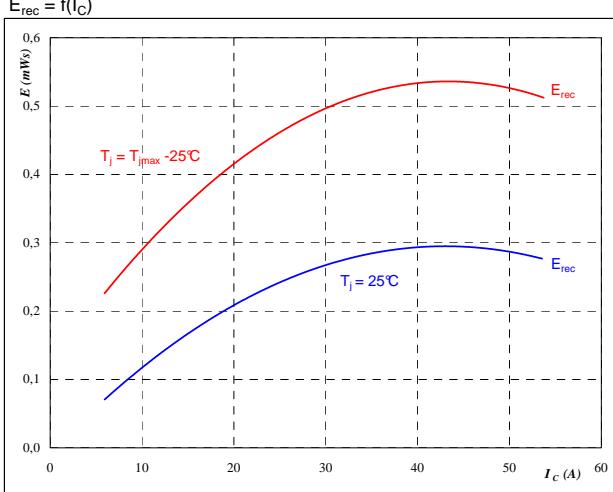
Figure 6 Output inverter IGBT
Typical switching energy losses as a function of gate resistor
 $E = f(R_G)$



With an inductive load at

$T_j = 25/125 \text{ }^\circ\text{C}$
 $V_{CE} = 300 \text{ V}$
 $V_{GE} = 15 \text{ V}$
 $I_C = 30 \text{ A}$

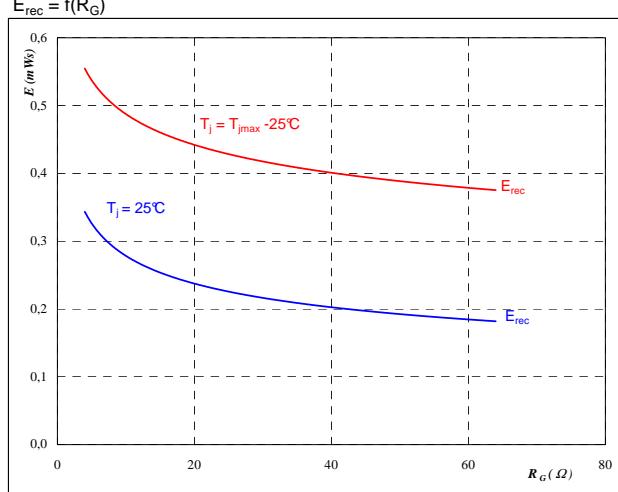
Figure 7 Output inverter FWD
Typical reverse recovery energy loss as a function of collector current
 $E_{rec} = f(I_C)$



With an inductive load at

$T_j = 25/125 \text{ }^\circ\text{C}$
 $V_{CE} = 300 \text{ V}$
 $V_{GE} = 15 \text{ V}$
 $R_{gon} = 8 \Omega$

Figure 8 Output inverter FWD
Typical reverse recovery energy loss as a function of gate resistor
 $E_{rec} = f(R_G)$

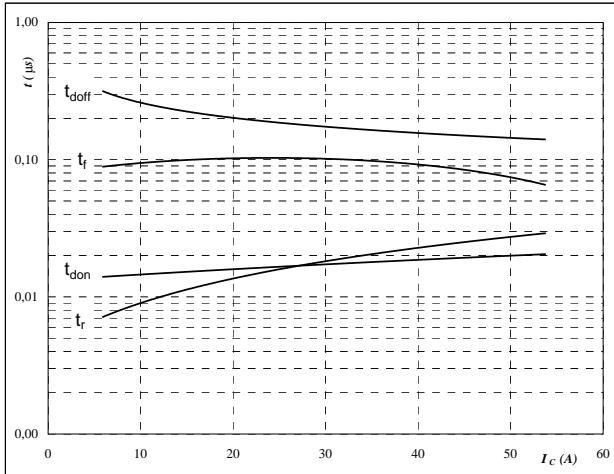


With an inductive load at

$T_j = 25/125 \text{ }^\circ\text{C}$
 $V_{CE} = 300 \text{ V}$
 $V_{GE} = 15 \text{ V}$
 $I_C = 30 \text{ A}$

Output Inverter

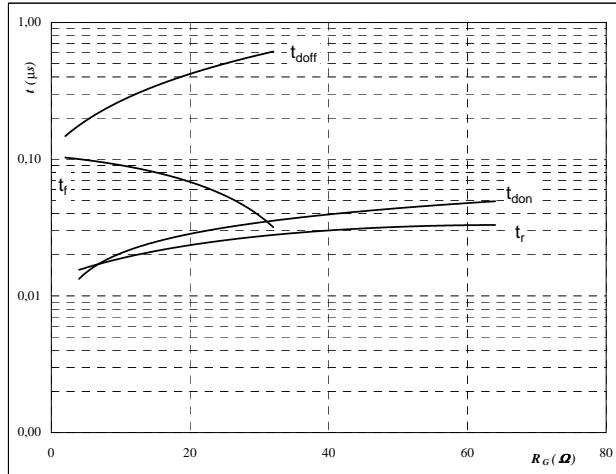
Figure 9
Typical switching times as a function of collector current
 $t = f(I_C)$



With an inductive load at

$T_j = 125^\circ\text{C}$
 $V_{CE} = 300 \text{ V}$
 $V_{GE} = 15 \text{ V}$
 $R_{gon} = 8 \Omega$
 $R_{goff} = 4 \Omega$

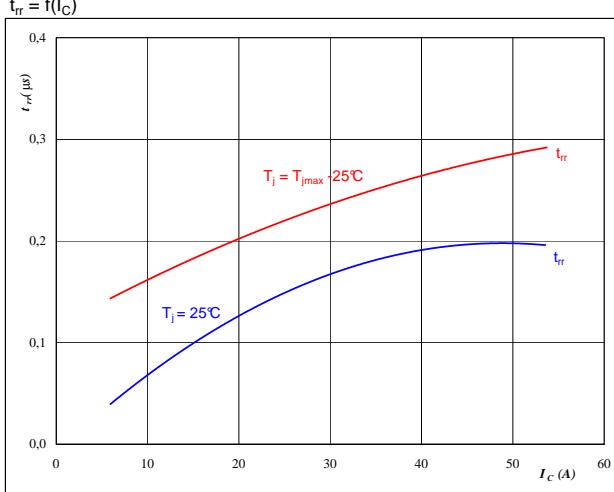
Figure 10
Typical switching times as a function of gate resistor
 $t = f(R_G)$



With an inductive load at

$T_j = 125^\circ\text{C}$
 $V_{CE} = 300 \text{ V}$
 $V_{GE} = 15 \text{ V}$
 $I_C = 30 \text{ A}$

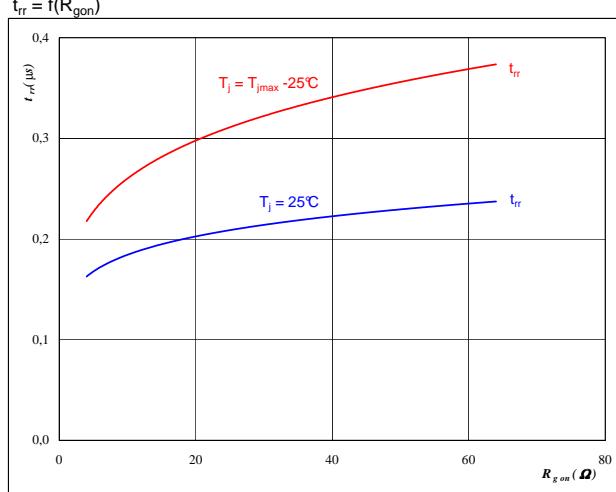
Figure 11
Typical reverse recovery time as a function of collector current
 $t_{rr} = f(I_C)$



At

$T_j = 25/125^\circ\text{C}$
 $V_{CE} = 300 \text{ V}$
 $V_{GE} = 15 \text{ V}$
 $R_{gon} = 8 \Omega$

Figure 12
Typical reverse recovery time as a function of IGBT turn on gate resistor
 $t_{rr} = f(R_{gon})$

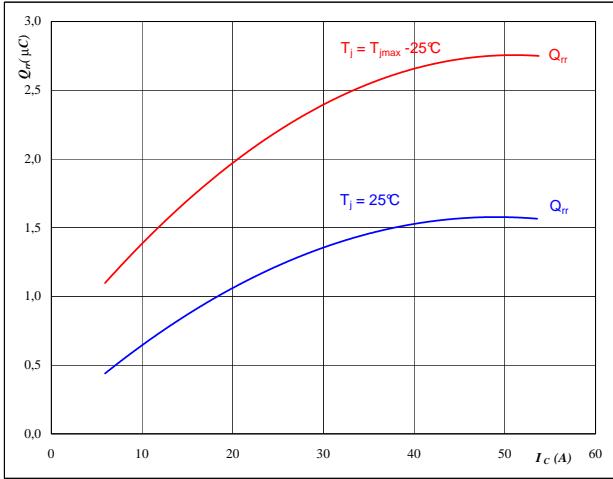


At

$T_j = 25/125^\circ\text{C}$
 $V_R = 300 \text{ V}$
 $I_F = 30 \text{ A}$
 $V_{GE} = 15 \text{ V}$

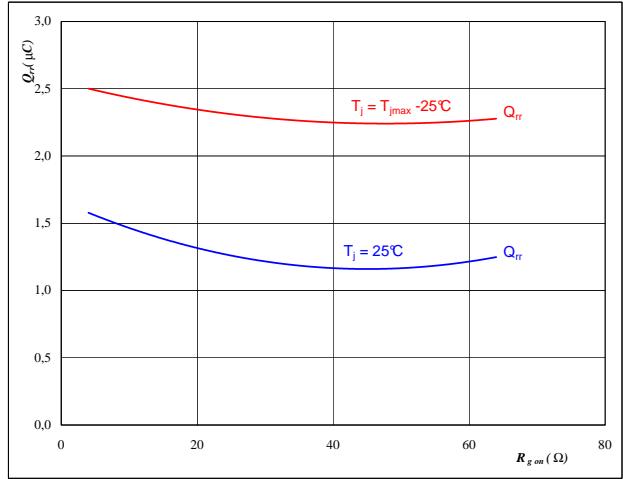
Output Inverter

Figure 13 Output inverter FWD
Typical reverse recovery charge as a function of collector current
 $Q_{rr} = f(I_c)$



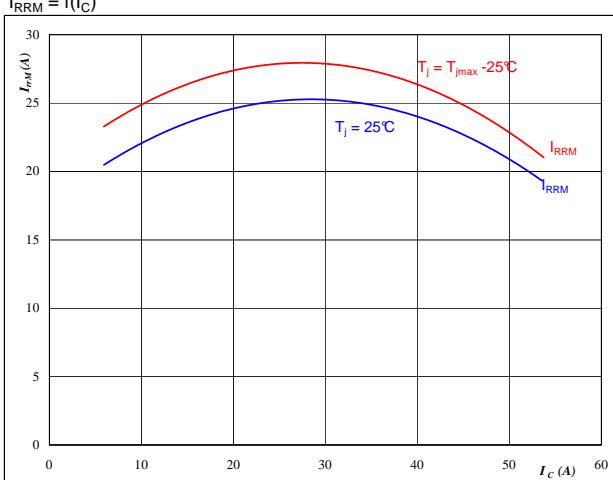
At
 $T_j = 25/125^\circ\text{C}$
 $V_{CE} = 300\text{ V}$
 $V_{GE} = 15\text{ V}$
 $R_{gon} = 8\Omega$

Figure 14 Output inverter FWD
Typical reverse recovery charge as a function of IGBT turn on gate resistor
 $Q_{rr} = f(R_{gon})$



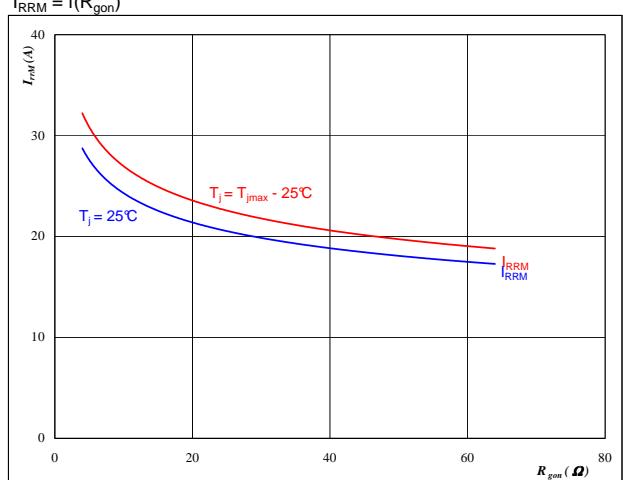
At
 $T_j = 25/125^\circ\text{C}$
 $V_R = 300\text{ V}$
 $I_F = 30\text{ A}$
 $V_{GE} = 15\text{ V}$

Figure 15 Output inverter FWD
Typical reverse recovery current as a function of collector current
 $I_{RRM} = f(I_c)$



At
 $T_j = 25/125^\circ\text{C}$
 $V_{CE} = 300\text{ V}$
 $V_{GE} = 15\text{ V}$
 $R_{gon} = 8\Omega$

Figure 16 Output inverter FWD
Typical reverse recovery current as a function of IGBT turn on gate resistor
 $I_{RRM} = f(R_{gon})$

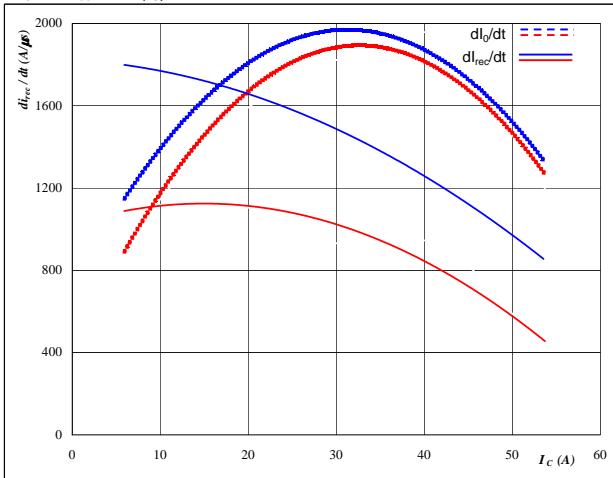


At
 $T_j = 25/125^\circ\text{C}$
 $V_R = 300\text{ V}$
 $I_F = 30\text{ A}$
 $V_{GE} = 15\text{ V}$

Output Inverter

Figure 17

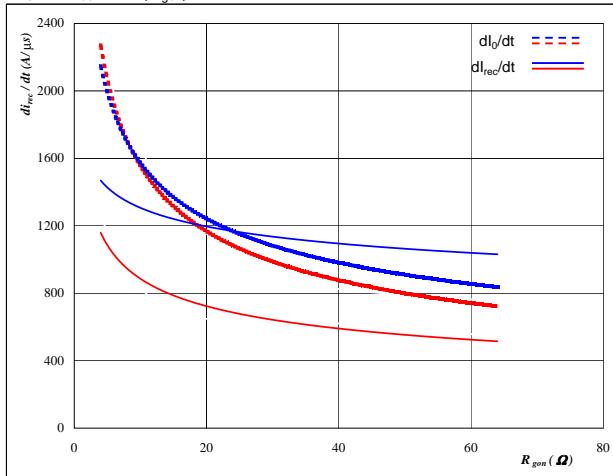
Typical rate of fall of forward and reverse recovery current as a function of collector current
 $dI_0/dt, dI_{rec}/dt = f(I_C)$


At

$T_J = 25/125^\circ\text{C}$
 $V_{CE} = 300\text{ V}$
 $V_{GE} = 15\text{ V}$
 $R_{gon} = 8\Omega$

Output inverter FWD
Figure 18

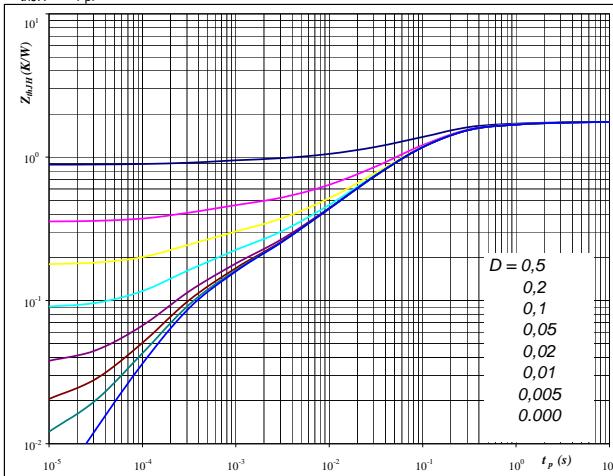
Typical rate of fall of forward and reverse recovery current as a function of IGBT turn on gate resistor
 $dI_0/dt, dI_{rec}/dt = f(R_{gon})$


At

$T_J = 25/125^\circ\text{C}$
 $V_R = 300\text{ V}$
 $I_F = 30\text{ A}$
 $V_{GE} = 15\text{ V}$

Figure 19

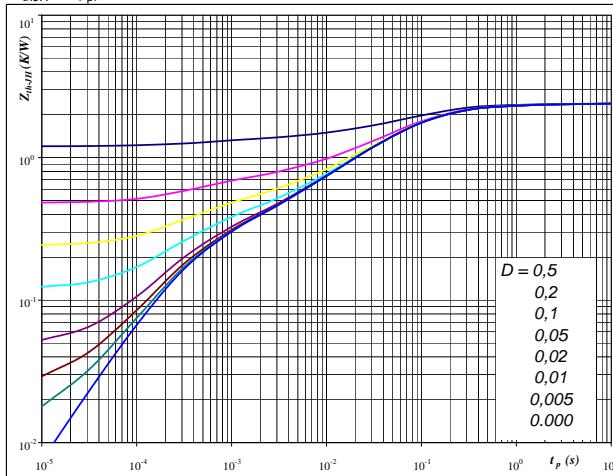
IGBT transient thermal impedance as a function of pulse width

 $Z_{thJH} = f(t_p)$

At

$D = t_p / T$ With phase change interface
 $R_{thJH} = 1,76 \text{ K/W}$ $R_{thJH} = 1,43 \text{ K/W}$

Output inverter IGBT
Figure 20

FWD transient thermal impedance as a function of pulse width

 $Z_{thJH} = f(t_p)$

At

$D = t_p / T$ With phase change interface
 $R_{thJH} = 2,40 \text{ K/W}$ $R_{thJH} = 1,94 \text{ K/W}$

IGBT thermal model values

Thermal grease		Phase change interface	
R (C/W)	Tau (s)	R (C/W)	Tau (s)
0,06	4,6E+00	0,05	3,7E+00
0,22	5,4E-01	0,17	4,3E-01
0,94	1,0E-01	0,76	8,4E-02
0,34	2,0E-02	0,27	1,6E-02
0,11	3,1E-03	0,09	2,5E-03
0,11	3,0E-04	0,09	2,4E-04

FWD thermal model values

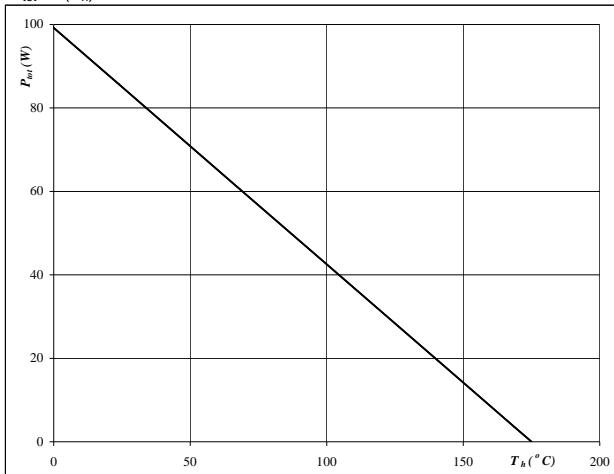
Thermal grease		Phase change interface	
R (C/W)	Tau (s)	R (C/W)	Tau (s)
0,07	4,6E+00	0,06	3,7E+00
0,27	4,8E-01	0,22	3,9E-01
1,13	8,5E-02	0,92	6,9E-02
0,52	2,0E-02	0,42	1,6E-02
0,20	2,8E-03	0,16	2,3E-03
0,21	3,3E-04	0,17	2,7E-04

Output Inverter

Figure 21

Power dissipation as a function of heatsink temperature

$$P_{\text{tot}} = f(T_h)$$

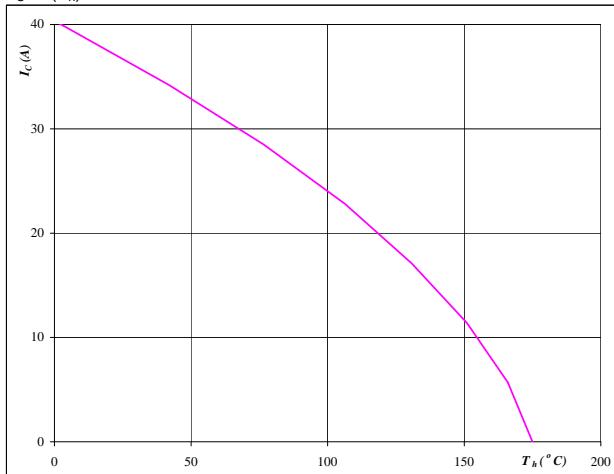

At

$$T_j = 175 \quad ^\circ\text{C}$$

Output inverter IGBT
Figure 22

Collector current as a function of heatsink temperature

$$I_C = f(T_h)$$


At

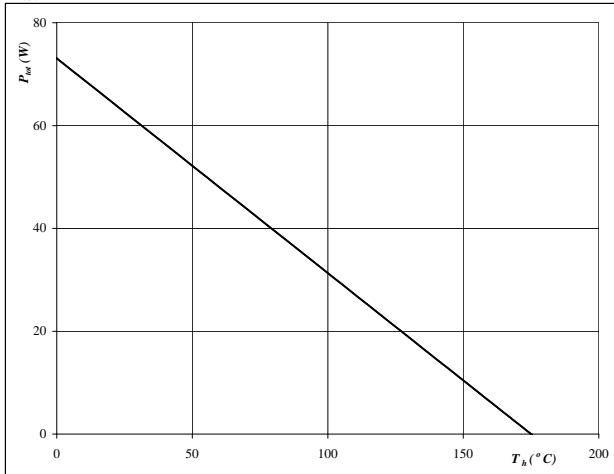
$$T_j = 175 \quad ^\circ\text{C}$$

$$V_{GE} = 15 \quad \text{V}$$

Figure 23
Output inverter FWD

Power dissipation as a function of heatsink temperature

$$P_{\text{tot}} = f(T_h)$$

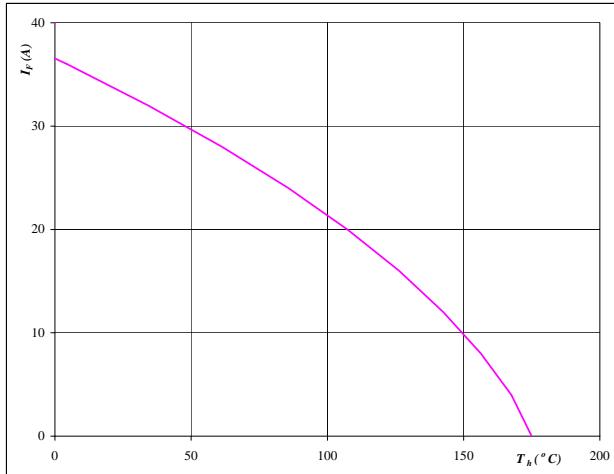

At

$$T_j = 175 \quad ^\circ\text{C}$$

Figure 24
Output inverter FWD

Forward current as a function of heatsink temperature

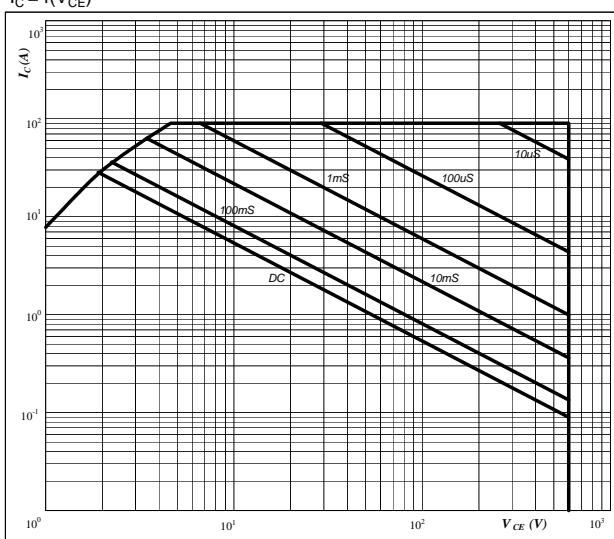
$$I_F = f(T_h)$$


At

$$T_j = 175 \quad ^\circ\text{C}$$

Output Inverter

Figure 25
Safe operating area as a function
of collector-emitter voltage
 $I_C = f(V_{CE})$

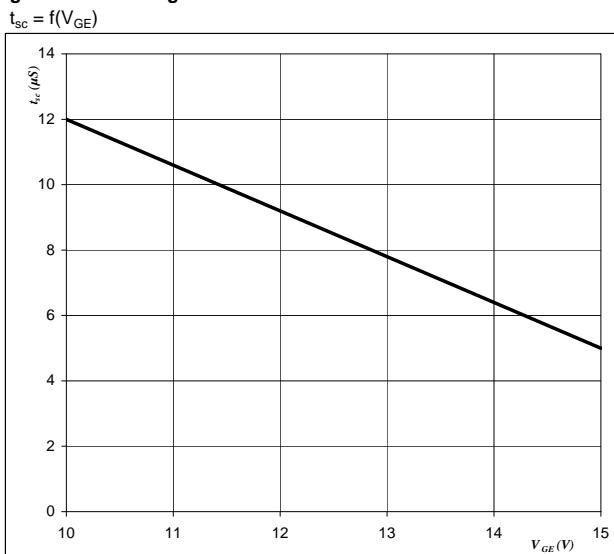


At

D = single pulse
 $T_h = 80 \text{ } ^\circ\text{C}$
 $V_{GE} = 15 \text{ V}$
 $T_j = T_{jmax} \text{ } ^\circ\text{C}$

Figure 27
Short circuit withstand time as a function of
gate-emitter voltage

Output inverter IGBT

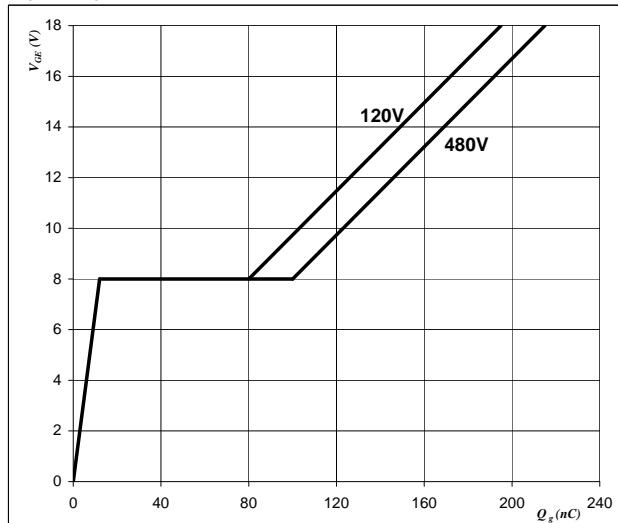


At

$V_{CE} = 600 \text{ V}$
 $T_j \leq 175 \text{ } ^\circ\text{C}$

Figure 26
Gate voltage vs Gate charge

$V_{GE} = f(Q_{GE})$



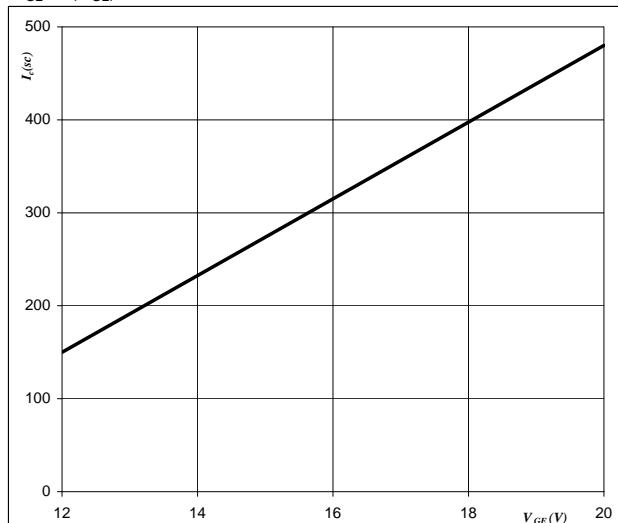
At

$I_C = 30 \text{ A}$

Figure 28
Typical short circuit collector current as a function of
gate-emitter voltage

Output inverter IGBT

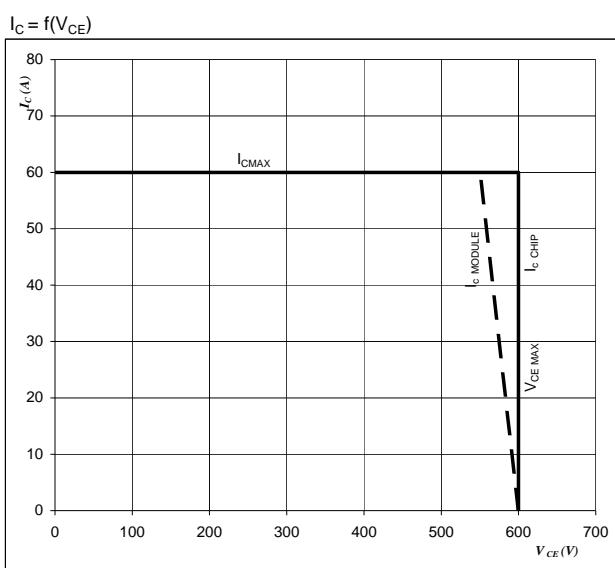
$V_{GE} = f(Q_{GE})$



At

$V_{CE} \leq 600 \text{ V}$
 $T_j = 175 \text{ } ^\circ\text{C}$

Figure 29
Reverse bias safe operating area



At

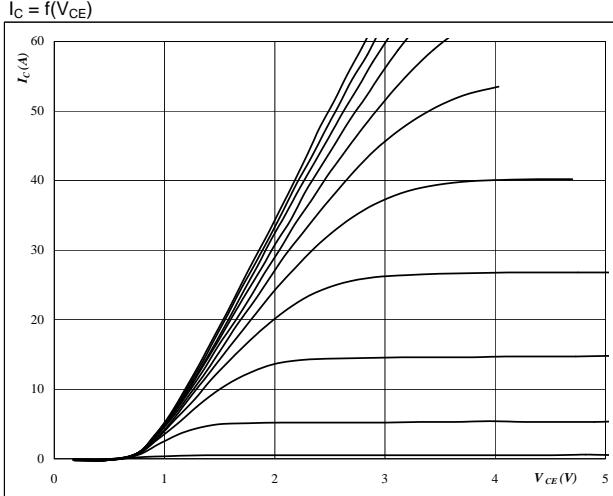
$$T_j = T_{j\max} - 25 \quad ^\circ\text{C}$$

$$U_{cominus} = U_{ccplus}$$

Switching mode : 3phase SPWM

Brake

Figure 1
Typical output characteristics
 $I_C = f(V_{CE})$



At

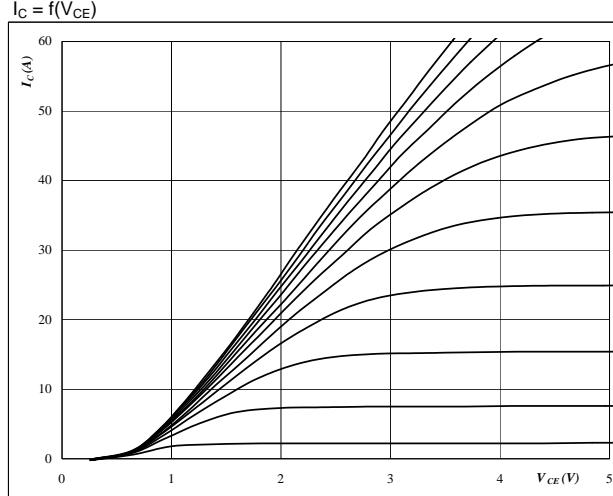
$t_p = 250 \mu s$

$T_j = 25^\circ C$

V_{GE} from 7 V to 17 V in steps of 1 V

Brake IGBT

Figure 2
Typical output characteristics
 $I_C = f(V_{CE})$



At

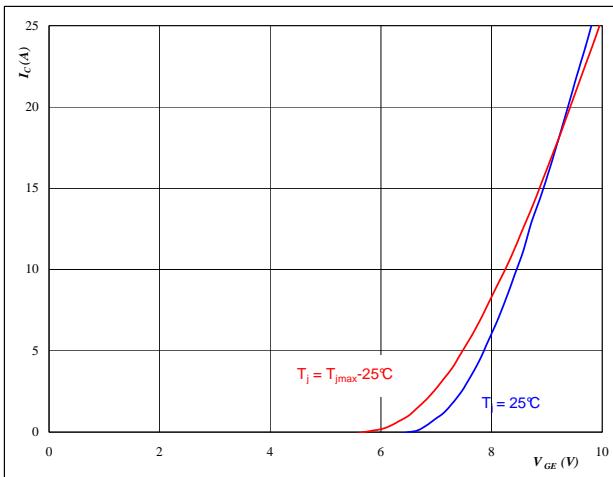
$t_p = 250 \mu s$

$T_j = 125^\circ C$

V_{GE} from 7 V to 17 V in steps of 1 V

Figure 3
Typical transfer characteristics
 $I_C = f(V_{GE})$

Brake IGBT



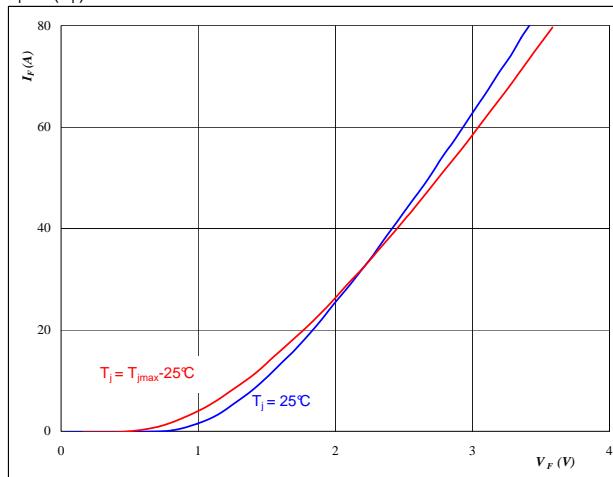
At

$t_p = 250 \mu s$

$V_{CE} = 10 V$

Figure 4
Typical diode forward current as
a function of forward voltage
 $I_F = f(V_F)$

Brake FWD

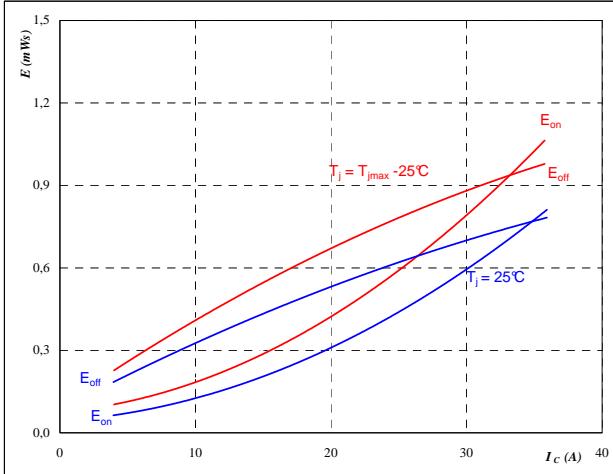


At

$t_p = 250 \mu s$

Brake

Figure 5
Typical switching energy losses
as a function of collector current
 $E = f(I_C)$

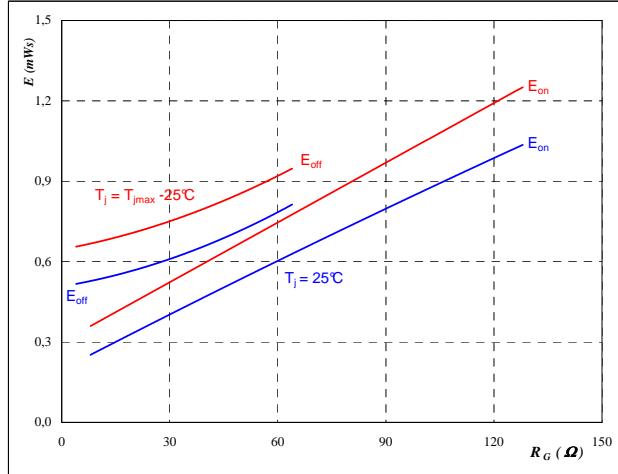


With an inductive load at

$T_j = 25/125^\circ\text{C}$
 $V_{CE} = 300\text{ V}$
 $V_{GE} = 15\text{ V}$
 $R_{gon} = 16\Omega$
 $R_{goff} = 8\Omega$

Brake IGBT

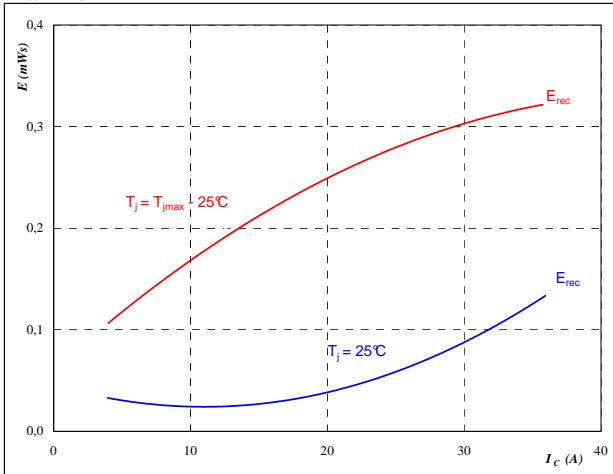
Figure 6
Typical switching energy losses
as a function of gate resistor
 $E = f(R_G)$



With an inductive load at

$T_j = 25/125^\circ\text{C}$
 $V_{CE} = 300\text{ V}$
 $V_{GE} = 15\text{ V}$
 $I_C = 20\text{ A}$

Figure 7
Typical reverse recovery energy loss
as a function of collector current
 $E_{rec} = f(I_C)$

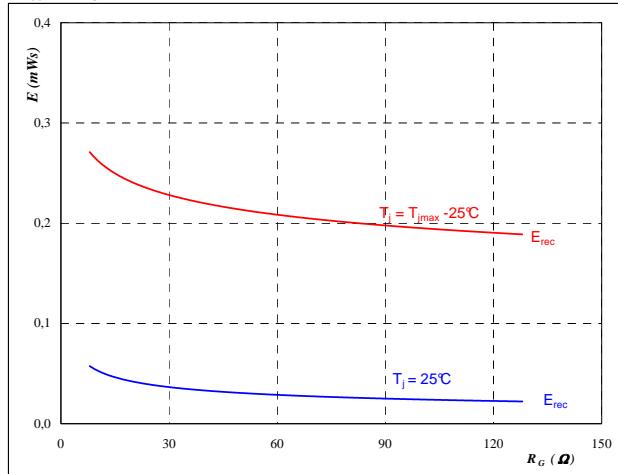


With an inductive load at

$T_j = 25/125^\circ\text{C}$
 $V_{CE} = 300\text{ V}$
 $V_{GE} = 15\text{ V}$
 $R_{gon} = 16\Omega$

Brake FWD

Figure 8
Typical reverse recovery energy loss
as a function of gate resistor
 $E_{rec} = f(R_G)$

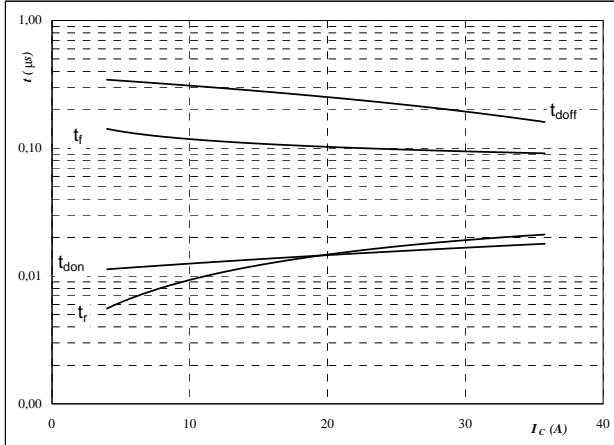


With an inductive load at

$T_j = 25/125^\circ\text{C}$
 $V_{CE} = 300\text{ V}$
 $V_{GE} = 15\text{ V}$
 $I_C = 20\text{ A}$

Brake

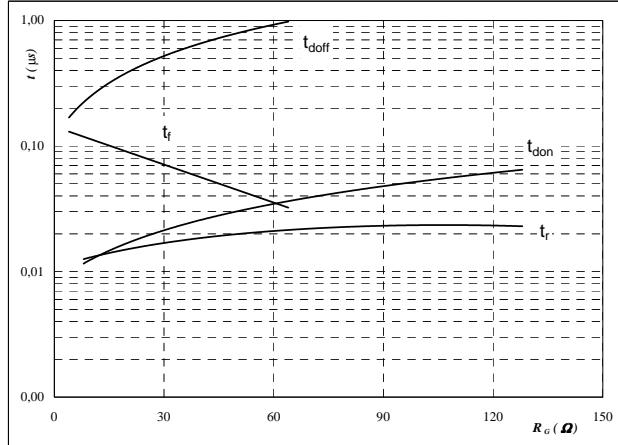
Figure 9
Typical switching times as a function of collector current
 $t = f(I_C)$



With an inductive load at

$T_J =$	125	°C
$V_{CE} =$	300	V
$V_{GE} =$	15	V
$R_{gon} =$	16	Ω
$R_{goff} =$	8	Ω

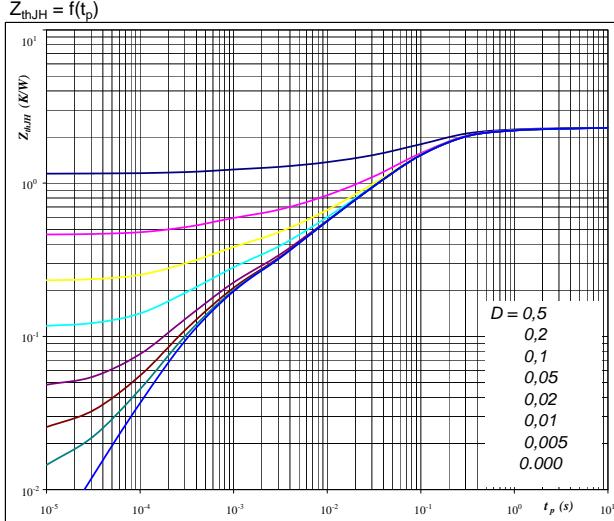
Figure 10
Typical switching times as a function of gate resistor
 $t = f(R_G)$



With an inductive load at

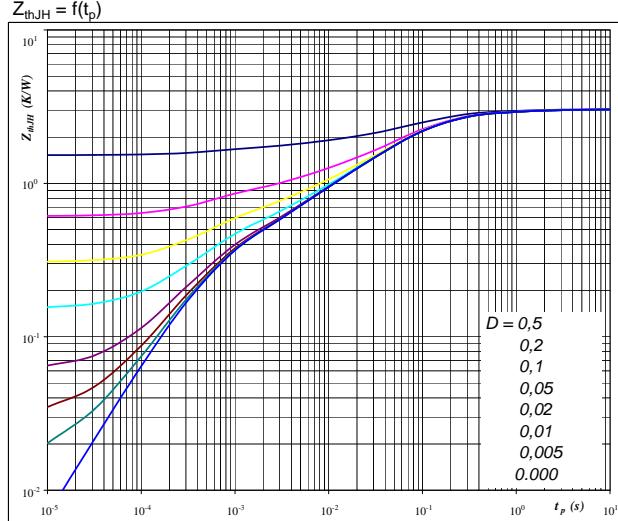
$T_J =$	125	°C
$V_{CE} =$	300	V
$V_{GE} =$	15	V
$I_C =$	20	A

Figure 11
IGBT transient thermal impedance as a function of pulse width



At Thermal grease $R_{thJH} = 2.30$ K/W D = tp / T Phase change interface $R_{thJH} = 0.60$ K/W

Figure 12
FWD transient thermal impedance as a function of pulse width



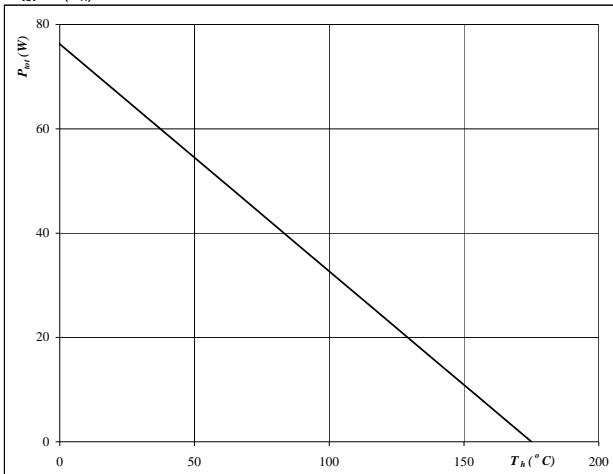
At Thermal grease $R_{thJH} = 3.04$ K/W D = tp / T Phase change interface $R_{thJH} = 1.27$ K/W

Brake

Figure 13

Power dissipation as a function of heatsink temperature

$$P_{\text{tot}} = f(T_h)$$

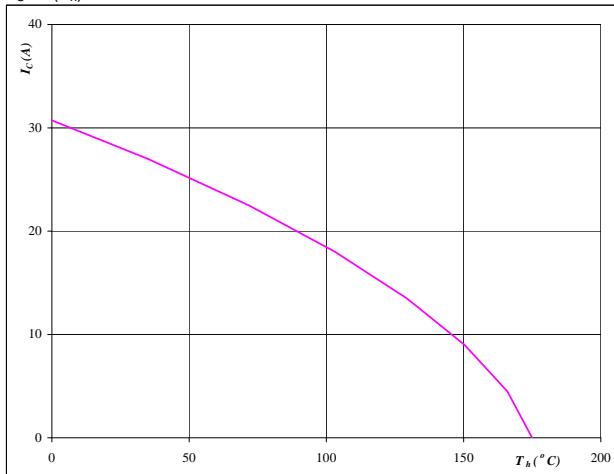

At

$$T_j = 175 \quad {}^\circ\text{C}$$

Brake IGBT
Figure 14

Collector current as a function of heatsink temperature

$$I_C = f(T_h)$$


At

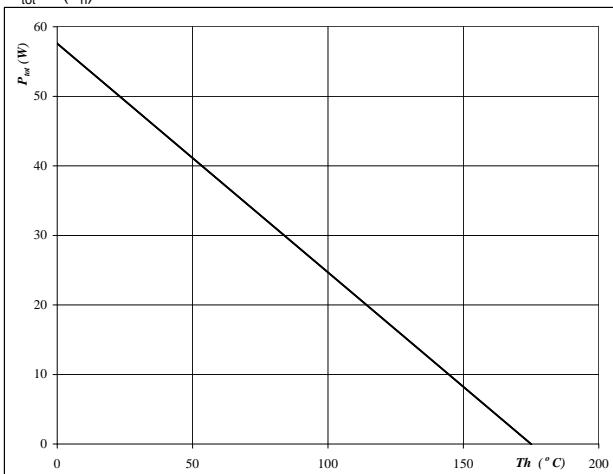
$$T_j = 175 \quad {}^\circ\text{C}$$

$$V_{GE} = 15 \quad \text{V}$$

Figure 15

Power dissipation as a function of heatsink temperature

$$P_{\text{tot}} = f(T_h)$$

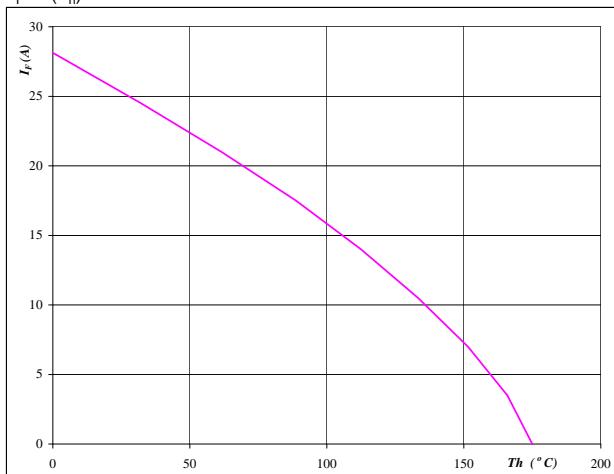

At

$$T_j = 175 \quad {}^\circ\text{C}$$

Brake FWD
Figure 16

Forward current as a function of heatsink temperature

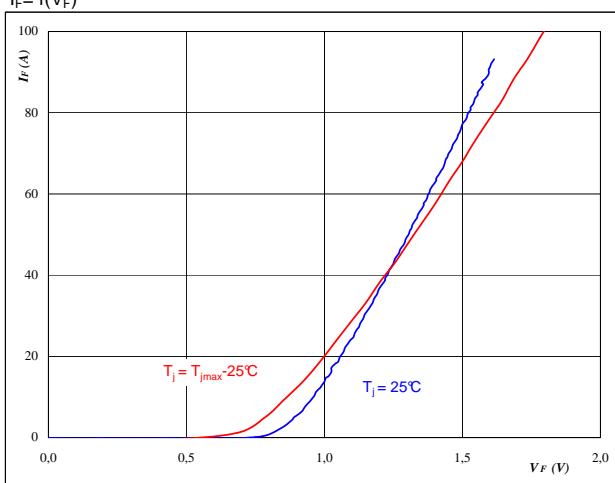
$$I_F = f(T_h)$$


At

$$T_j = 175 \quad {}^\circ\text{C}$$

Input Rectifier Bridge

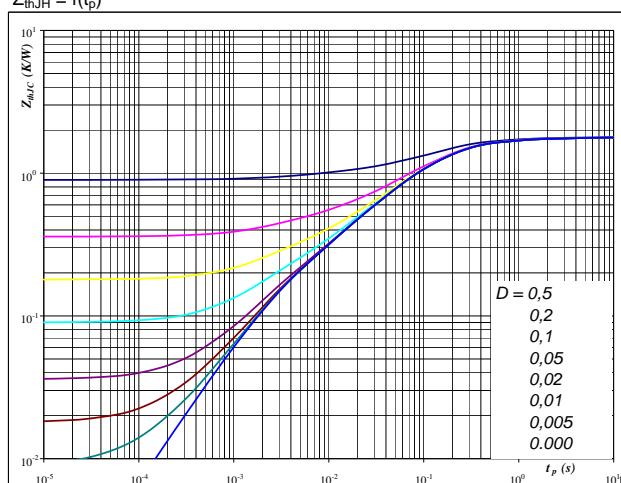
Figure 1
Typical diode forward current as a function of forward voltage
 $I_F = f(V_F)$



At
 $t_p = 250 \mu\text{s}$

Rectifier diode

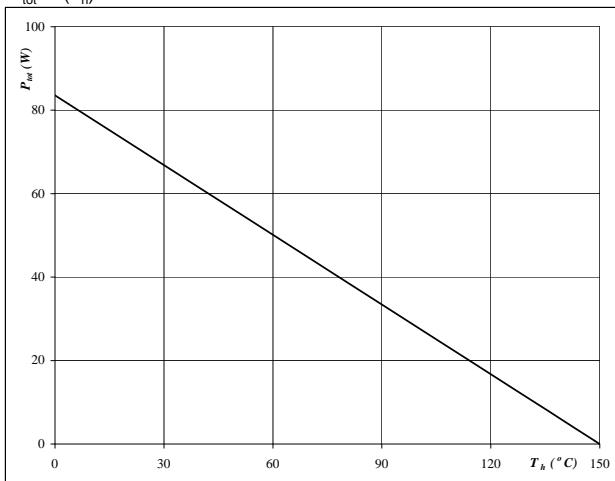
Figure 2
Diode transient thermal impedance as a function of pulse width
 $Z_{thJH} = f(t_p)$



At
 $D = t_p / T$
 $R_{thJH} = 1.89 \text{ K/W}$

Rectifier diode

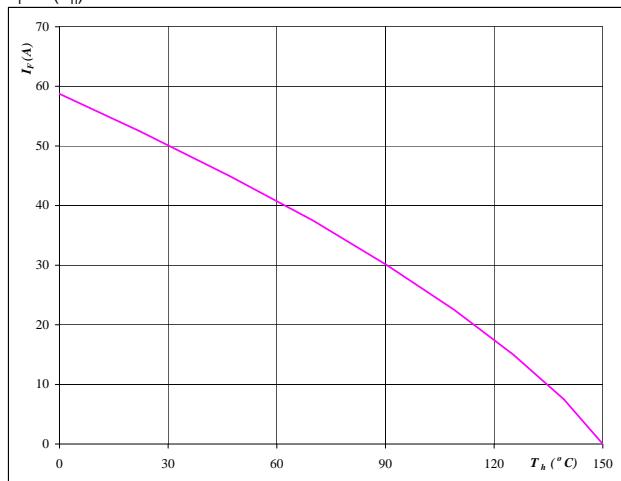
Figure 3
Power dissipation as a function of heatsink temperature
 $P_{tot} = f(T_h)$



At
 $T_j = 150^\circ\text{C}$

Rectifier diode

Figure 4
Forward current as a function of heatsink temperature
 $I_F = f(T_h)$

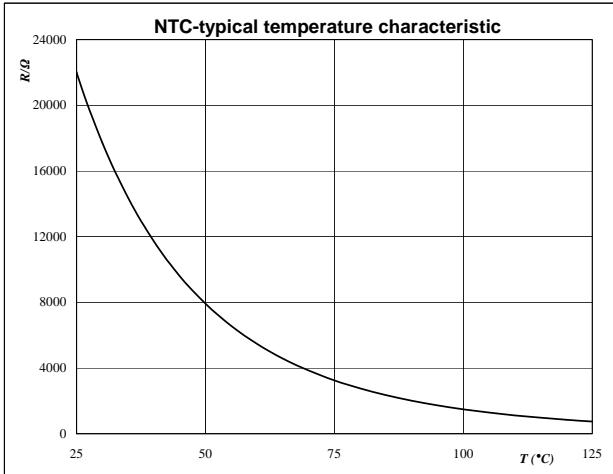


At
 $T_j = 150^\circ\text{C}$

Rectifier diode

Thermistor

Figure 1
**Typical NTC characteristic
as a function of temperature**
 $R_T = f(T)$



Thermistor

Figure 2
Typical NTC resistance values

$$R(T) = R_{25} \cdot e^{\left(B_{25/100} \left(\frac{1}{T} - \frac{1}{T_{25}} \right) \right)} \quad [\Omega]$$

T [°C]	R _{nom} [Ω]	R _{min} [Ω]	R _{max} [Ω]	△R/R [%]
-55	2089434,5	1506495,4	2672373,6	27,9
0	71804,2	59724,4	83884	16,8
10	43780,4	37094,4	50466,5	15,3
20	27484,6	23684,6	31284,7	13,8
25	22000	19109,3	24890,7	13,1
30	17723,3	15512,2	19934,4	12,5
60	5467,9	4980,6	5955,1	8,9
70	3848,6	3546	4151,1	7,9
80	2757,7	2568,2	2947,1	6,9
90	2008,9	1889,7	2128,2	5,9
100	1486,1	1411,8	1560,4	5
150	400,2	364,8	435,7	8,8

Switching Definitions Output Inverter

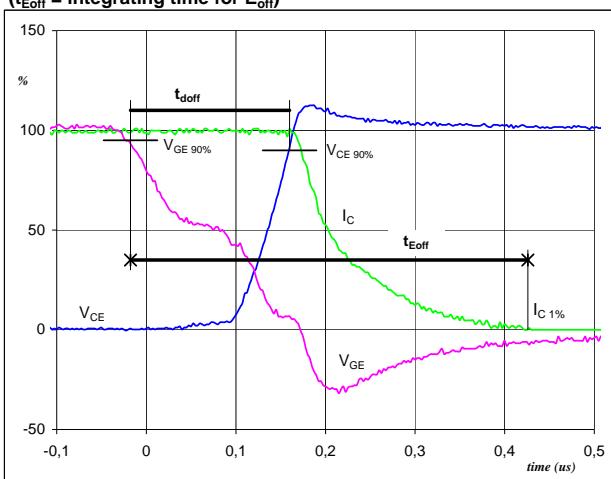
General conditions

T_j	= 125 °C
R_{gon}	= 16 Ω
R_{goff}	= 8 Ω

Figure 1

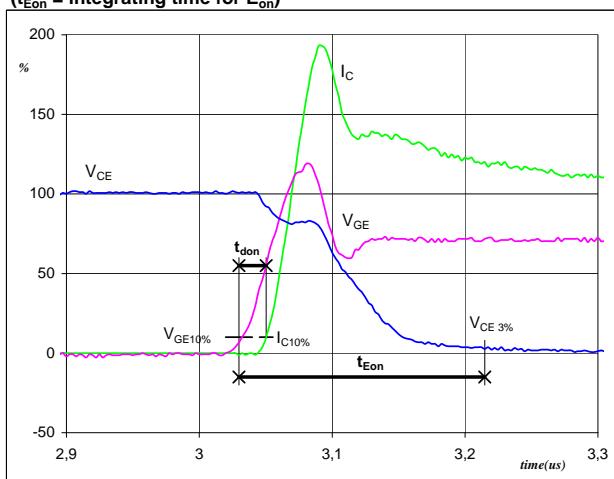
Output inverter IGBT

Turn-off Switching Waveforms & definition of t_{doff} , t_{Eoff}

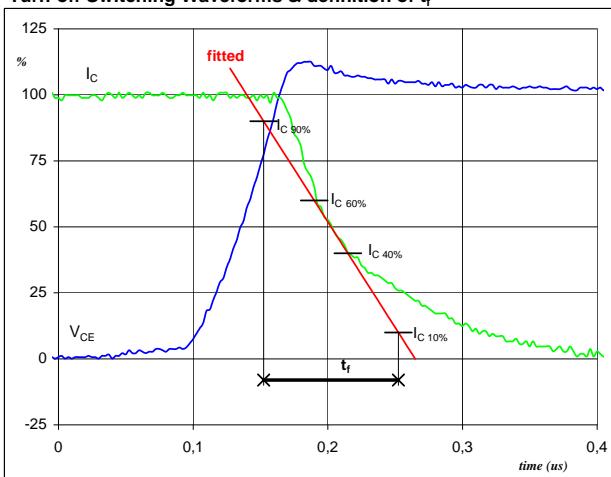
(t_{Eoff} = integrating time for E_{off})

 $V_{GE}(0\%) = 0 \text{ V}$
 $V_{GE}(100\%) = 15 \text{ V}$
 $V_C(100\%) = 300 \text{ V}$
 $I_C(100\%) = 30 \text{ A}$
 $t_{doff} = 0,17 \mu\text{s}$
 $t_{Eoff} = 0,44 \mu\text{s}$
Figure 2

Output inverter IGBT

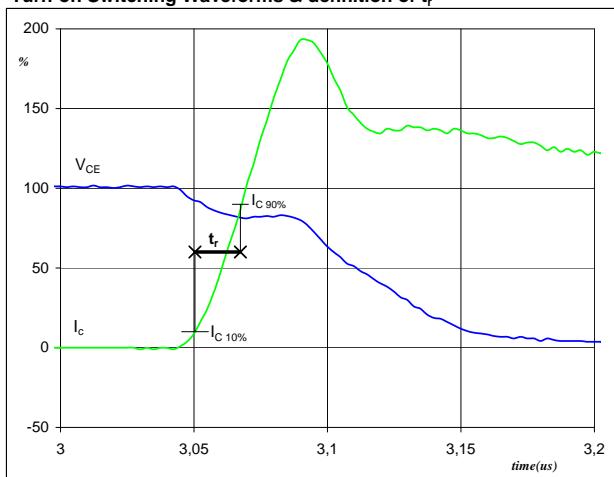
Turn-on Switching Waveforms & definition of t_{don} , t_{Eon}

(t_{Eon} = integrating time for E_{on})

 $V_{GE}(0\%) = 0 \text{ V}$
 $V_{GE}(100\%) = 15 \text{ V}$
 $V_C(100\%) = 300 \text{ V}$
 $I_C(100\%) = 30 \text{ A}$
 $t_{don} = 0,02 \mu\text{s}$
 $t_{Eon} = 0,18 \mu\text{s}$
Figure 3

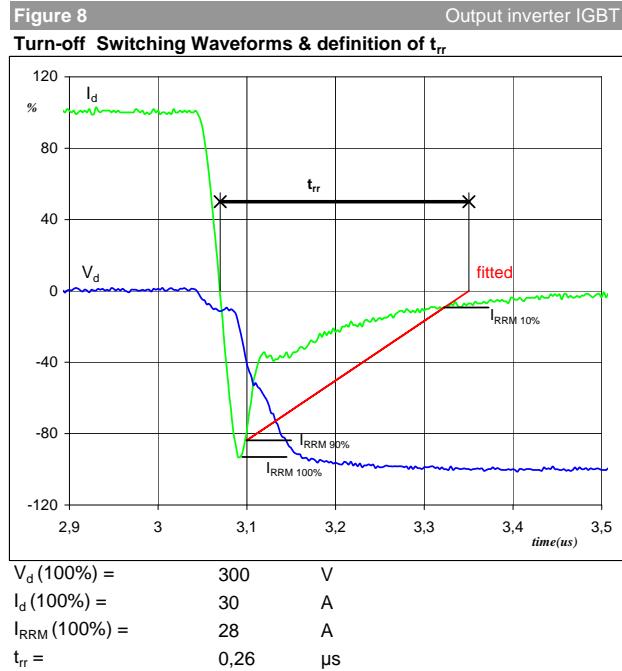
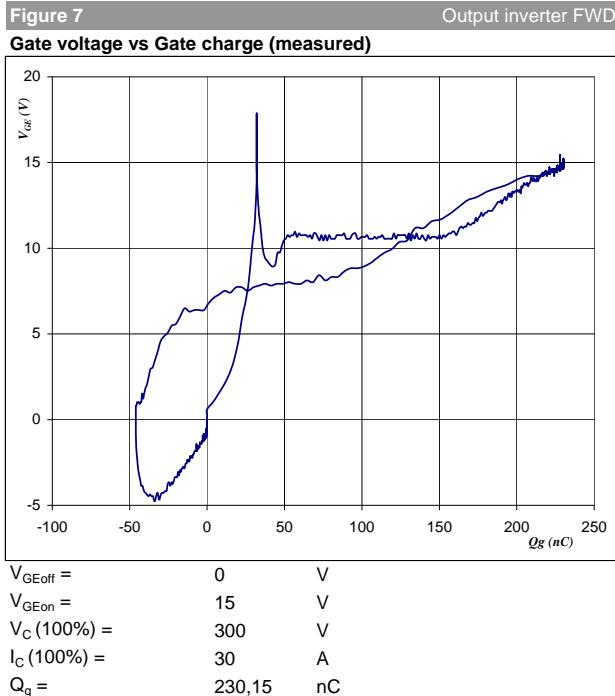
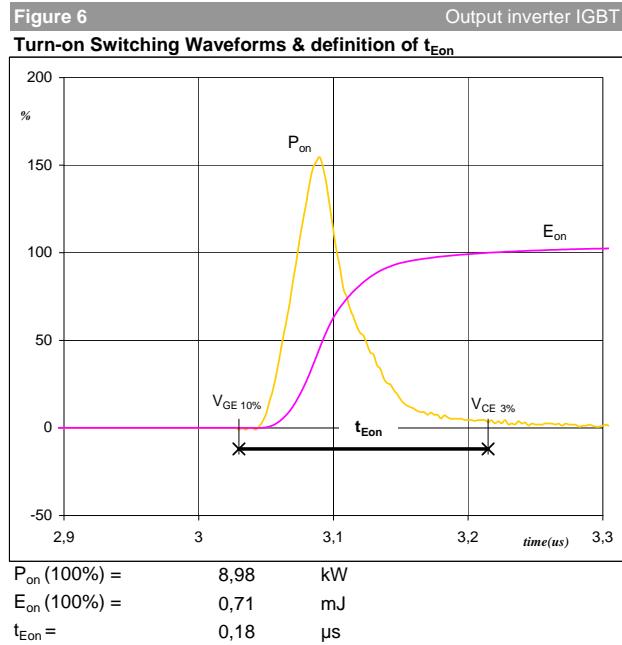
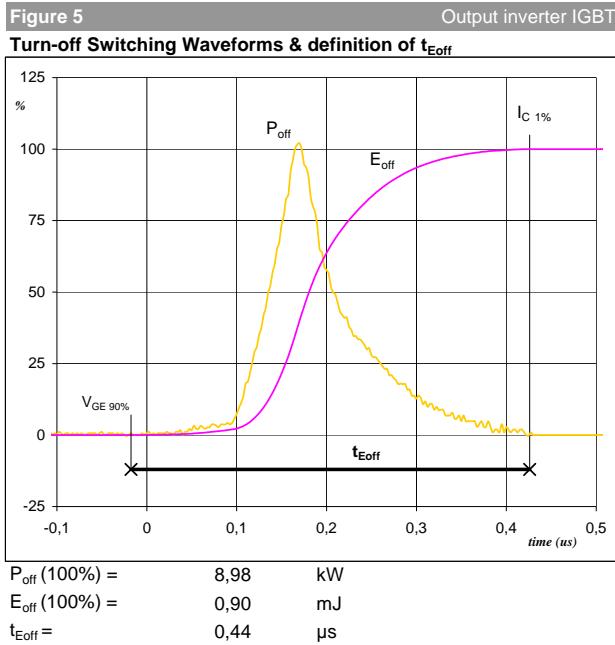
Output inverter IGBT

Turn-off Switching Waveforms & definition of t_f

 $V_C(100\%) = 300 \text{ V}$
 $I_C(100\%) = 30 \text{ A}$
 $t_f = 0,10 \mu\text{s}$
Figure 4

Output inverter IGBT

Turn-on Switching Waveforms & definition of t_r

 $V_C(100\%) = 300 \text{ V}$
 $I_C(100\%) = 30 \text{ A}$
 $t_r = 0,02 \mu\text{s}$

Switching Definitions Output Inverter

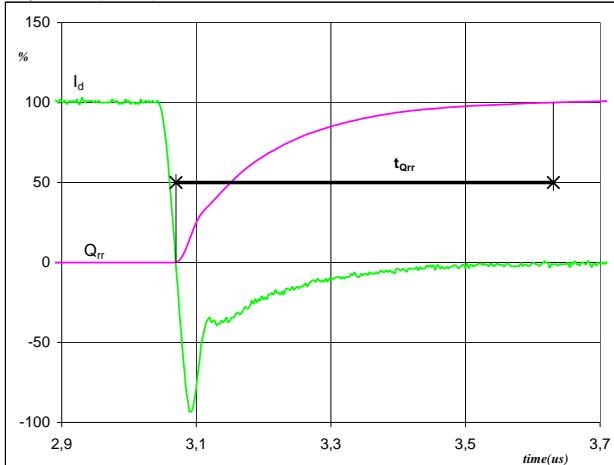


Switching Definitions Output Inverter

Figure 9

Output inverter FWD

Turn-on Switching Waveforms & definition of t_{Qrr}
 $(t_{Qrr} = \text{integrating time for } Q_{rr})$

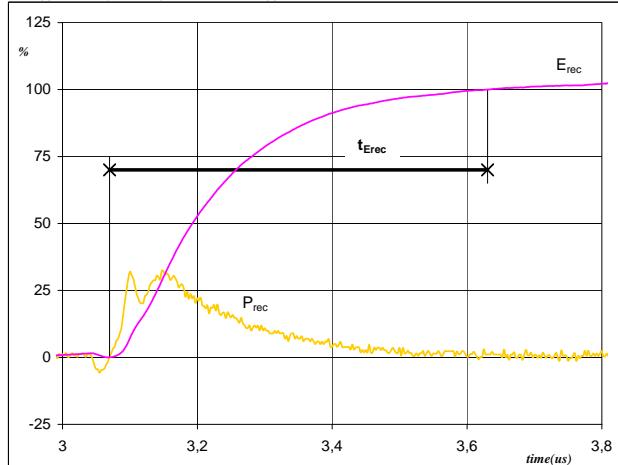


$I_d(100\%) = 30 \text{ A}$
 $Q_{rr}(100\%) = 2,45 \mu\text{C}$
 $t_{Qrr} = 0,56 \mu\text{s}$

Figure 10

Output inverter FWD

Turn-on Switching Waveforms & definition of t_{Erec}
 $(t_{Erec} = \text{integrating time for } E_{rec})$



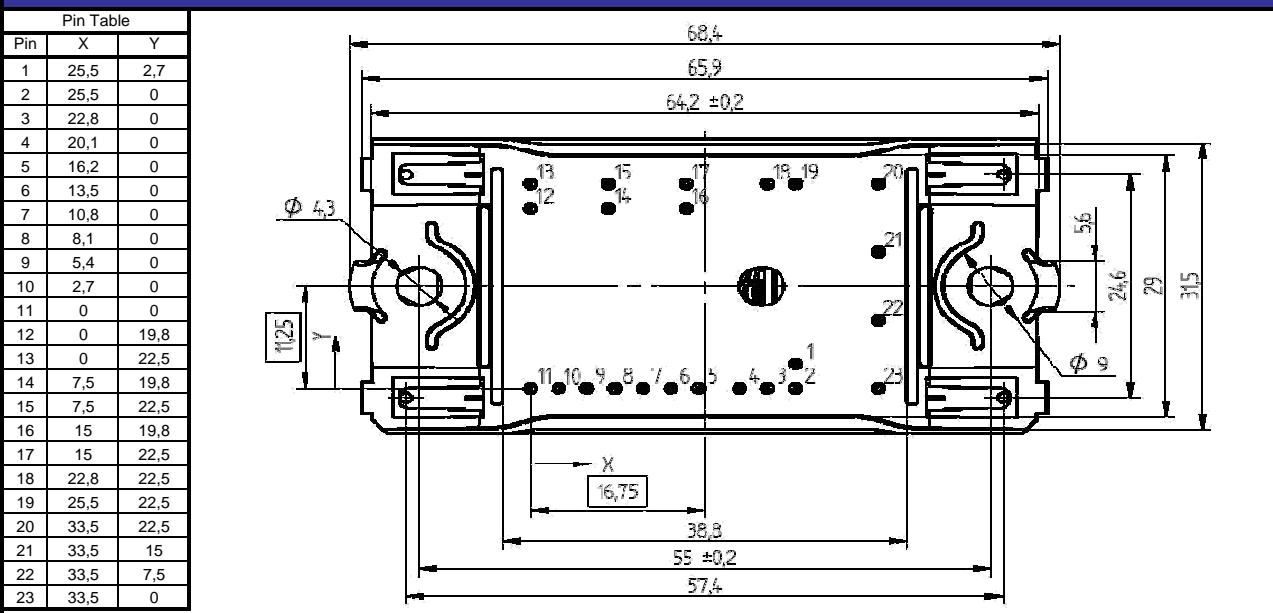
$P_{rec}(100\%) = 8,98 \text{ kW}$
 $E_{rec}(100\%) = 0,51 \text{ mJ}$
 $t_{Erec} = 0,56 \mu\text{s}$

Ordering Code and Marking - Features - Outline - Pinout

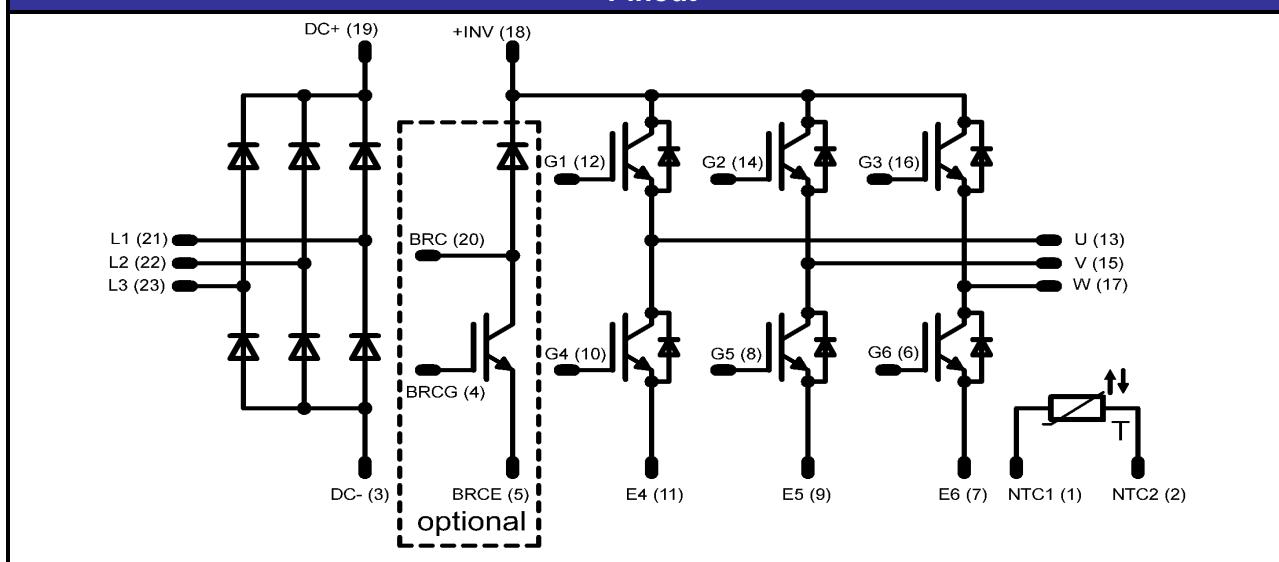
Ordering Code & Marking

Version	Ordering Code	in DataMatrix as	in packaging barcode as
without thermal paste 12mm housing	V23990-P546-A38-PM	P546-A38	P546-A38
without thermal paste 17mm housing	V23990-P546-A39-PM	P546-A39	P546-A39
without thermal paste, w/o brake, 12mm housing	V23990-P546-C38-PM	P546-C38	P546-C38
without thermal paste, w/o brake, 17mm housing	V23990-P546-C39-PM	P546-C39	P546-C39

Outline



Pinout



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2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.