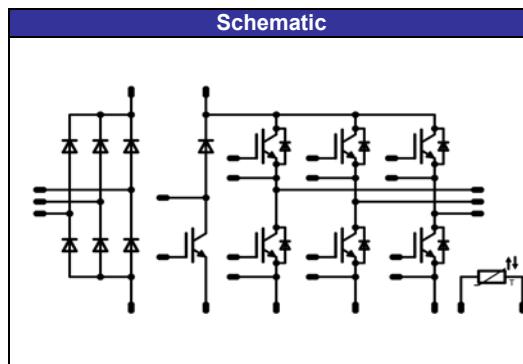


flowPIM 1 3rd gen
1200V / 35A

Features
• 3~ rectifier, BRC, Inverter, NTC
• Very compact housing, easy to route
• IGBT4 / EmCon4 technology for low saturation losses and improved EMC behaviour



Target Applications
• Motor Drives
• Power Generation



Types
• V23990-P580-A41-PM

Maximum Ratings

T_j=25°C, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
-----------	--------	-----------	-------	------

Input Rectifier Diode

Peak repetitive reverse voltage	V _{RRM}		1600	V
Forward current per diode	I _{FAV}	DC current T _h =80°C	36	A
Surge forward current	I _{FSM}		320	A
I ² t-value	I ² t	t _p =10ms T _j =45°C	510	A ² s
Power dissipation per diode	P _{tot}	T _j =T _j max T _h =80°C	40	W
Maximum junction temperature	T _j max		150	°C

Inverter Transistor

Collector-emitter break down voltage	V _{CE}		1200	V
DC collector current	I _C	T _j =T _j max T _h =80°C	33	A
Repetitive peak collector current	I _{Cpulse}	t _p limited by T _j max	105	A
Power dissipation per IGBT	P _{tot}	T _j =T _j max T _h =80°C	79	W
Gate-emitter peak voltage	V _{GE}		±20	V
Short circuit ratings	t _{SC} V _{CC}	T _j ≤150°C V _{GE} =15V	10 800	μs V
Maximum junction temperature	T _j max		175	°C

Maximum Ratings

T_j=25°C, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
Inverter Diode				
Peak repetitive reverse voltage	V _{RRM}	T _j =25°C	1200	V
DC forward current	I _F	T _j =T _{jmax} T _h =80°C	34	A
Repetitive peak forward current	I _{FRM}	t _p limited by T _{jmax}	70	A
Power dissipation per diode	P _{tot}	T _j =T _{jmax} T _h =80°C	61	W
Maximum junction temperature	T _{jmax}		175	°C
Brc Transistor				
Collector-emitter break down voltage	V _{CE}		1200	V
DC collector current	I _C	T _j =T _{jmax} T _h =80°C	25	A
Repetitive peak collector current	I _{Cpuls}	t _p limited by T _{jmax}	75	A
Power dissipation per IGBT	P _{tot}	T _j =T _{jmax} T _h =80°C	62	W
Gate-emitter peak voltage	V _{GE}		±20	V
Short circuit ratings	t _{SC} V _{CC}	T _j ≤150°C V _{GE} =15V	10 800	μs V
Maximum junction temperature	T _{jmax}		175	°C
Brc Diode				
Peak repetitive reverse voltage	V _{RRM}	T _j =25°C	1200	V
DC forward current	I _F	T _j =T _{jmax} T _h =80°C	14	A
Repetitive peak forward current	I _{FRM}	t _p limited by T _{jmax}	20	A
Power dissipation per diode	P _{tot}	T _j =T _{jmax} T _h =80°C	29	W
Maximum junction temperature	T _{jmax}		175	°C
Thermal Properties				
Storage temperature	T _{stg}		-40...+125	°C
Operation temperature under switching condition	T _{op}		-40...+(T _{jmax} - 25)	°C
Insulation Properties				
Insulation voltage	V _{is}	t=2s DC voltage	4000	V
Creepage distance			min 12.7	mm
Clearance			min 12.7	mm

Characteristic Values

Parameter	Symbol	Conditions					Value			Unit
			V_{GE} [V] or V_{GS} [V]	V_r [V] or V_{CE} [V] or V_{DS} [V]	I_c [A] or I_F [A] or I_D [A]	T_j	Min	Typ	Max	
Input Rectifier Diode										
Forward voltage	V_F				50	$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$	0.8	1.29 1.24	1.6	V
Threshold voltage (for power loss calc. only)	V_{to}				50	$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$		0.93 0.82		V
Slope resistance (for power loss calc. only)	r_t				50	$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$		0.007 0.009		Ω
Reverse current	I_r			1500		$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$			0.02 2	mA
Thermal resistance chip to heatsink per chip	R_{thJH}	Thermal grease thickness≤50μm $\lambda=0.61\text{W/mK}$						1.77		K/W
Thermal resistance chip to case per chip	R_{thJC}									
Inverter Transistor										
Gate emitter threshold voltage	$V_{GE(\text{th})}$	$V_{CE}=V_{GE}$			0.0012	$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	5	5.8	6.5	V
Collector-emitter saturation voltage	$V_{CE(\text{sat})}$		15		35	$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	1.6	1.95 2.39	2.3	V
Collector-emitter cut-off current incl. diode	I_{CES}		0	1200		$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$			0.01	mA
Gate-emitter leakage current	I_{GES}		20	0		$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$			200	nA
Integrated gate resistor	R_{gint}							-		Ω
Turn-on delay time	$t_{d(on)}$	$R_{goff}=16\Omega$ $R_{gon}=16\Omega$	± 15	600	35	$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		92 91.6		ns
Rise time	t_r					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		18 23.4		
Turn-off delay time	$t_{d(off)}$					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		213 274		
Fall time	t_f					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		75.3 105		
Turn-on energy loss per pulse	E_{on}					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		1.62 2.49		mWs
Turn-off energy loss per pulse	E_{off}					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		1.81 2.82		
Input capacitance	C_{ies}	$f=1\text{MHz}$	0	25		$T_j=25^\circ\text{C}$		1950		pF
Output capacitance	C_{oss}							155		
Reverse transfer capacitance	C_{rss}							115		
Gate charge	Q_{Gate}	$V_{cc}=960\text{V}$	±15		35	$T_j=25^\circ\text{C}$		270		nC
Thermal resistance chip to heatsink per chip	R_{thJH}	Thermal grease thickness≤50μm $\lambda=0.61\text{W/mK}$						1.2		K/W
Thermal resistance chip to case per chip	R_{thJC}							N/A		
Inverter Diode										
Diode forward voltage	V_F				35	$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	1	1.83 1.8	2.2	V
Peak reverse recovery current	I_{RRM}	$R_{goff}=16\Omega$	± 15	600	35	$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		68.9 78.7		A
Reverse recovery time	t_{rr}					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		150 277		
Reverse recovered charge	Q_{rr}					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		3.93 7.47		
Peak rate of fall of recovery current	$di(\text{rec})/\text{dt}$					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		4100 2080		
Reverse recovered energy	E_{rec}					$T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$		1.69 3.31		
Thermal resistance chip to heatsink per chip	R_{thJH}						1.55		K/W	
Thermal resistance chip to case per chip	R_{thJC}						N/A			

Characteristic Values

Parameter	Symbol	Conditions				Value			Unit
			V _{GE} [V] or V _{GS} [V]	V _I [V] or V _{CE} [V] or V _{DS} [V]	I _C [A] or I _F [A] or I _D [A]	T _J	Min	Typ	Max

Brc Transistor

Gate emitter threshold voltage	V _{GE(th)}	V _{CE} =V _{GE}			0.00085	T _J =25°C T _J =150°C	5	5.8	6.5	V
Collector-emitter saturation voltage	V _{CE(sat)}		15		25	T _J =25°C T _J =150°C	1.6	1.86 2.31	2.2	V
Collector-emitter cut-off incl. diode	I _{CES}		0	1200		T _J =25°C T _J =150°C			0.005	mA
Gate-emitter leakage current	I _{GES}		20	0		T _J =25°C T _J =150°C			200	nA
Integrated gate resistor	R _{gint}							-		Ω
Turn-on delay time	t _{d(on)}	R _{gon} =32Ω R _{goff} =32Ω	±15	600	25	T _J =25°C T _J =150°C	127			ns
Rise time	t _r					T _J =25°C T _J =150°C	36			
Turn-off delay time	t _{d(off)}					T _J =25°C T _J =150°C	232			
Fall time	t _f					T _J =25°C T _J =150°C	276			
Turn-on energy loss per pulse	E _{on}					T _J =25°C T _J =150°C	73.7			mWs
Turn-off energy loss per pulse	E _{off}					T _J =25°C T _J =150°C	112			
Input capacitance	C _{ies}	f=1MHz	0	25	T _J =25°C		1.81			pF
Output capacitance	C _{oss}						2.42			
Reverse transfer capacitance	C _{rss}						85			
Gate charge	Q _{Gate}	V _{CC} =960V	±15		25	T _J =25°C		200		nC
Thermal resistance chip to heatsink per chip	R _{thJH}	Thermal grease thickness≤50μm λ=0.61W/mK					1.53			K/W
Thermal resistance chip to case per chip	R _{thJC}							N/A		

Brc Diode

Diode forward voltage	V _F				10	T _J =25°C T _J =150°C	1.3	1.85 1.76	2.2	V
Reverse leakage current	I _r		±15	600	10	T _J =25°C T _J =150°C			5	μA
Peak reverse recovery current	I _{RRM}	R _{gon} =32Ω	±15	600	10	T _J =25°C T _J =150°C	10.2			A
Reverse recovery time	t _{rr}					T _J =25°C T _J =150°C	12.3			ns
Reverse recovered charge	Q _{rr}					T _J =25°C T _J =150°C	396			
Peak rate of fall of recovery current	di(rec)max /dt					T _J =25°C T _J =150°C	624			
Reverse recovery energy	E _{rec}					T _J =25°C T _J =150°C	1.55			μC
Reverse recovery energy	E _{rec}					T _J =25°C T _J =150°C	3.03			
Thermal resistance chip to heatsink per chip	R _{thJH}	Thermal grease thickness≤50μm λ=0.61W/mK					36			A/μs
Thermal resistance chip to case per chip	R _{thJC}						32			
							0.631			mWs
							1.3			
							3.28			K/W
							N/A			

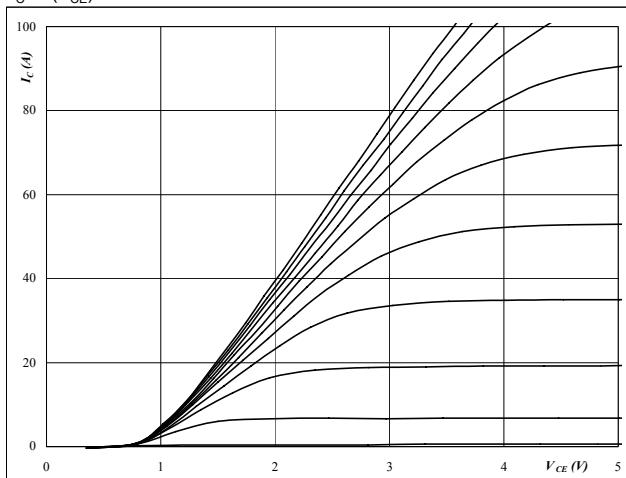
Thermistor

Rated resistance	R					T _J =25°C T _J =125°C	20.9	22 0.75	23.1	kΩ
Operating current	I					T _J =25°C			0.3	mA
Power dissipation	P					T _J =25°C		200		mW
B-value	B _(25/50)	Tol. ±3%				T _J =25°C		3950		K

Output Inverter

Figure 1
Typical output characteristics

$$I_C = f(V_{CE})$$


At

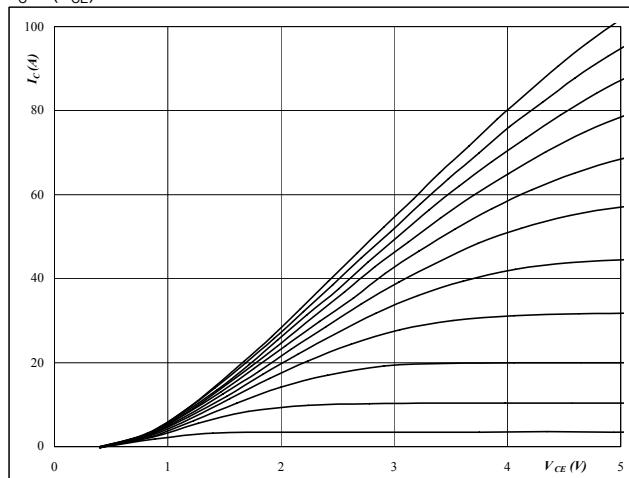
$$t_p = 250 \mu\text{s}$$

$$T_j = 25^\circ\text{C}$$

 V_{GE} from 7 V to 17 V in steps of 1 V

Figure 2
Typical output characteristics

$$I_C = f(V_{CE})$$


At

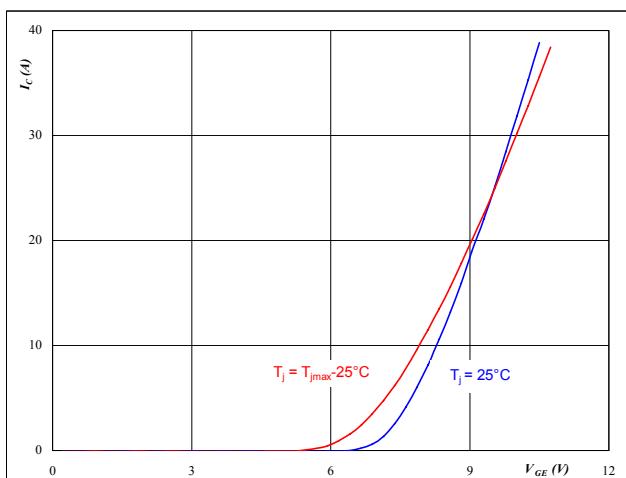
$$t_p = 250 \mu\text{s}$$

$$T_j = 150^\circ\text{C}$$

 V_{GE} from 7 V to 17 V in steps of 1 V

Figure 3
Typical transfer characteristics

$$I_C = f(V_{GE})$$

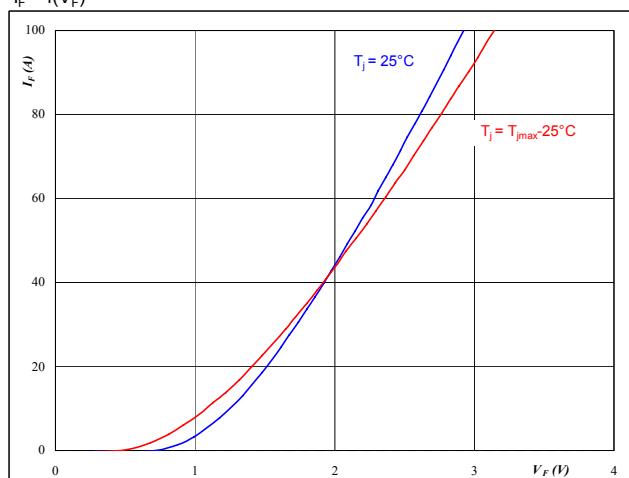

At

$$t_p = 250 \mu\text{s}$$

$$V_{CE} = 10 \text{ V}$$

Figure 4
Typical diode forward current as a function of forward voltage

$$I_F = f(V_F)$$


At

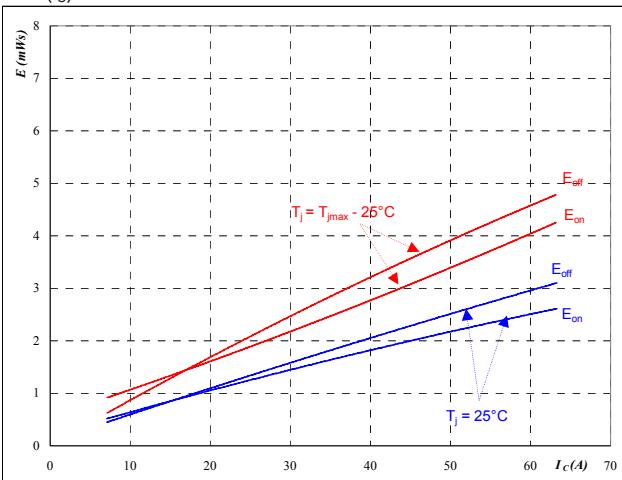
$$t_p = 250 \mu\text{s}$$

Output Inverter

Figure 5

**Typical switching energy losses
as a function of collector current**

$$E = f(I_C)$$



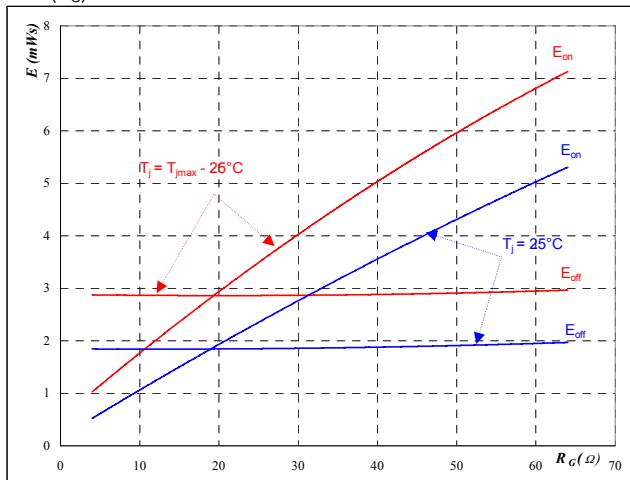
With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 16 \quad \Omega \\ R_{goff} &= 16 \quad \Omega \end{aligned}$$

Output inverter IGBT
Figure 6

**Typical switching energy losses
as a function of gate resistor**

$$E = f(R_G)$$



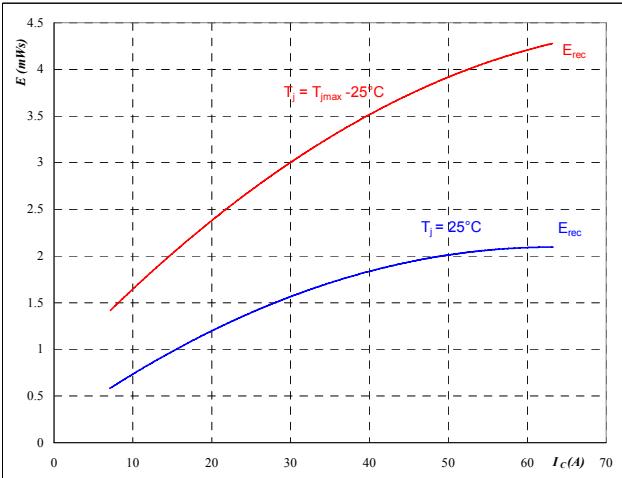
With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ I_C &= 35 \quad \text{A} \end{aligned}$$

Figure 7
Output inverter IGBT

**Typical reverse recovery energy loss
as a function of collector current**

$$E_{rec} = f(I_C)$$



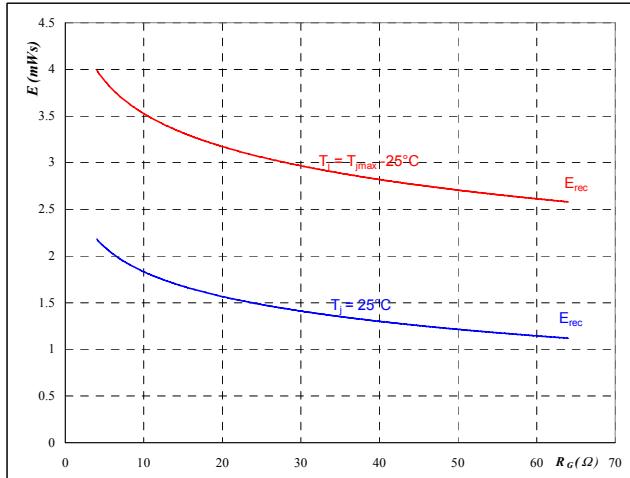
With an inductive load at

$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ R_{gon} &= 16 \quad \Omega \end{aligned}$$

Figure 8
Output inverter IGBT

**Typical reverse recovery energy loss
as a function of gate resistor**

$$E_{rec} = f(R_G)$$



With an inductive load at

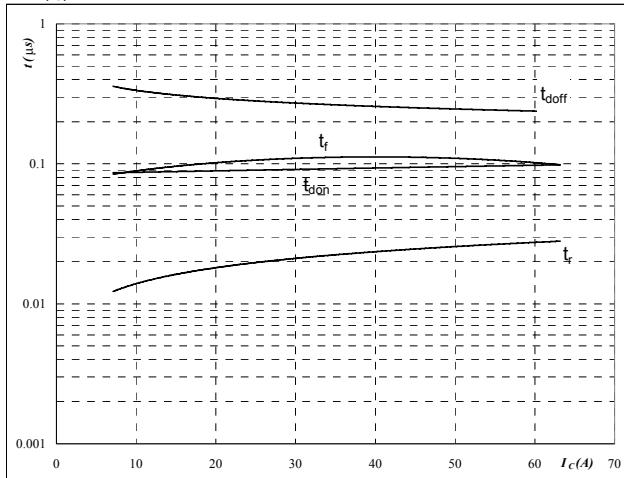
$$\begin{aligned} T_j &= 25/150 \quad ^\circ\text{C} \\ V_{CE} &= 600 \quad \text{V} \\ V_{GE} &= \pm 15 \quad \text{V} \\ I_C &= 35 \quad \text{A} \end{aligned}$$

Output Inverter

Figure 9

Typical switching times as a function of collector current

$$t = f(I_C)$$



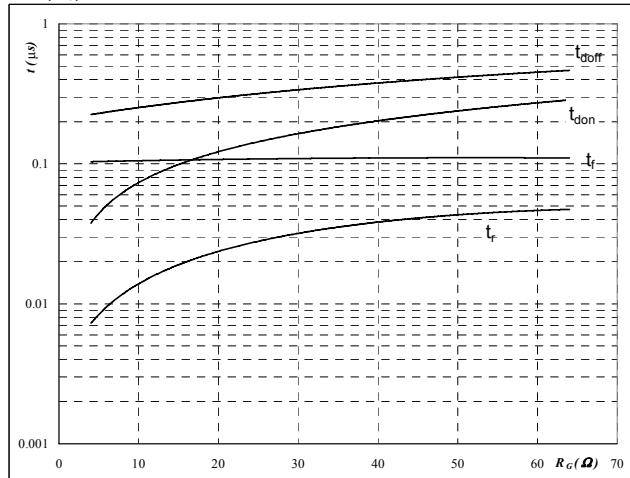
With an inductive load at

$$\begin{aligned} T_j &= 150 & ^\circ\text{C} \\ V_{CE} &= 600 & \text{V} \\ V_{GE} &= \pm 15 & \text{V} \\ R_{gon} &= 16 & \Omega \\ R_{goff} &= 16 & \Omega \end{aligned}$$

Output inverter IGBT
Figure 10

Typical switching times as a function of gate resistor

$$t = f(R_G)$$



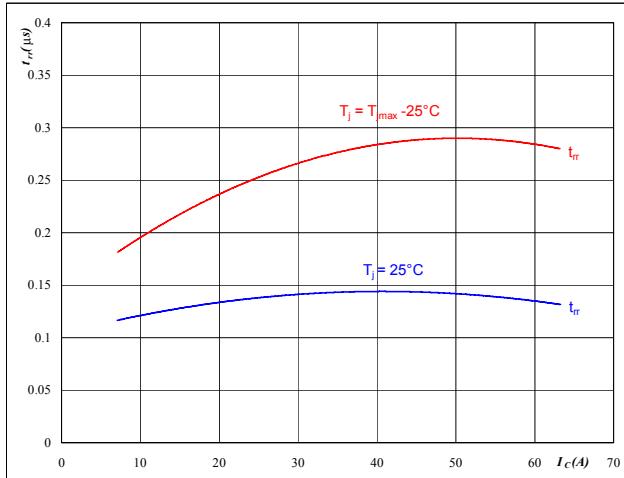
With an inductive load at

$$\begin{aligned} T_j &= 150 & ^\circ\text{C} \\ V_{CE} &= 600 & \text{V} \\ V_{GE} &= \pm 15 & \text{V} \\ I_C &= 35 & \text{A} \end{aligned}$$

Figure 11
Output inverter FRED

Typical reverse recovery time as a function of collector current

$$t_{rr} = f(I_C)$$



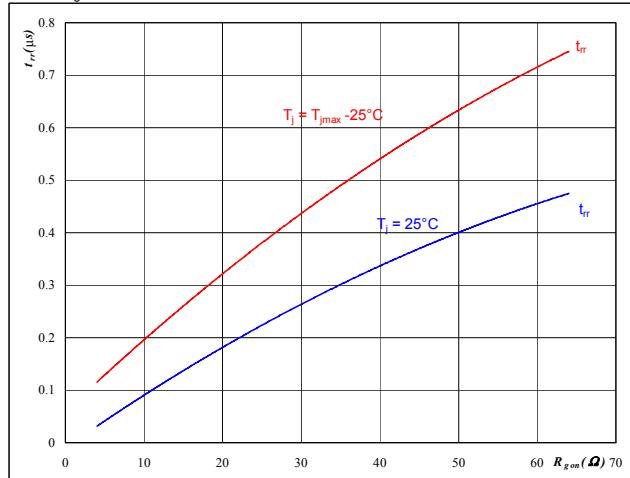
At

$$\begin{aligned} T_j &= 25/150 & ^\circ\text{C} \\ V_{CE} &= 600 & \text{V} \\ V_{GE} &= \pm 15 & \text{V} \\ R_{gon} &= 16 & \Omega \end{aligned}$$

Figure 12
Output inverter FRED

Typical reverse recovery time as a function of IGBT turn on gate resistor

$$t_{rr} = f(R_{gon})$$



At

$$\begin{aligned} T_j &= 25/150 & ^\circ\text{C} \\ V_R &= 600 & \text{V} \\ I_F &= 35 & \text{A} \\ V_{GE} &= \pm 15 & \text{V} \end{aligned}$$

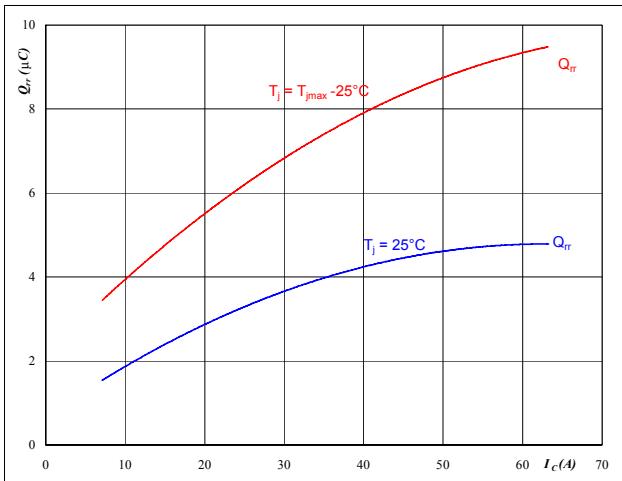
Output Inverter

Figure 13

Output inverter FRED

Typical reverse recovery charge as a function of collector current

$$Q_{rr} = f(I_C)$$


At

$$T_j = 25/150 \quad ^\circ C$$

$$V_{CE} = 600 \quad V$$

$$V_{GE} = \pm 15 \quad V$$

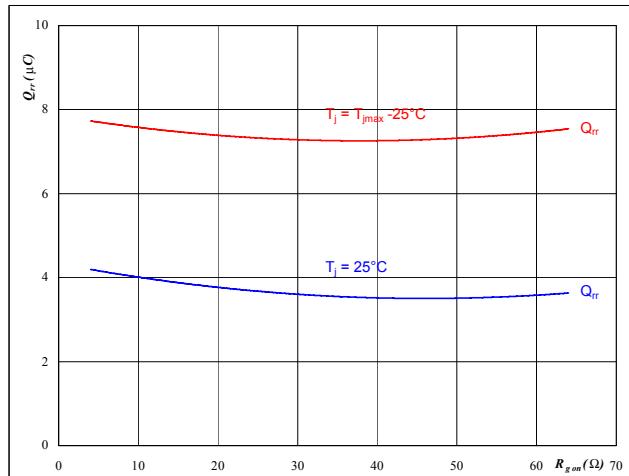
$$R_{gon} = 16 \quad \Omega$$

Figure 14

Output inverter FRED

Typical reverse recovery charge as a function of IGBT turn on gate resistor

$$Q_{rr} = f(R_{gon})$$


At

$$T_j = 25/150 \quad ^\circ C$$

$$V_R = 600 \quad V$$

$$I_F = 35 \quad A$$

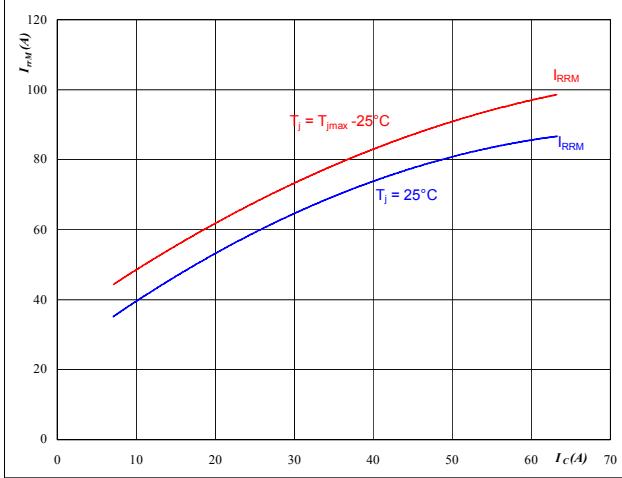
$$V_{GE} = \pm 15 \quad V$$

Figure 15

Output inverter FRED

Typical reverse recovery current as a function of collector current

$$I_{RRM} = f(I_C)$$


At

$$T_j = 25/150 \quad ^\circ C$$

$$V_{CE} = 600 \quad V$$

$$V_{GE} = \pm 15 \quad V$$

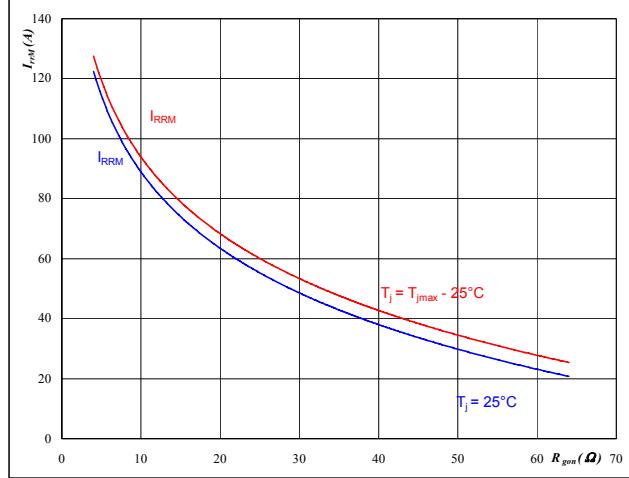
$$R_{gon} = 16 \quad \Omega$$

Figure 16

Output inverter FRED

Typical reverse recovery current as a function of IGBT turn on gate resistor

$$I_{RRM} = f(R_{gon})$$


At

$$T_j = 25/150 \quad ^\circ C$$

$$V_R = 600 \quad V$$

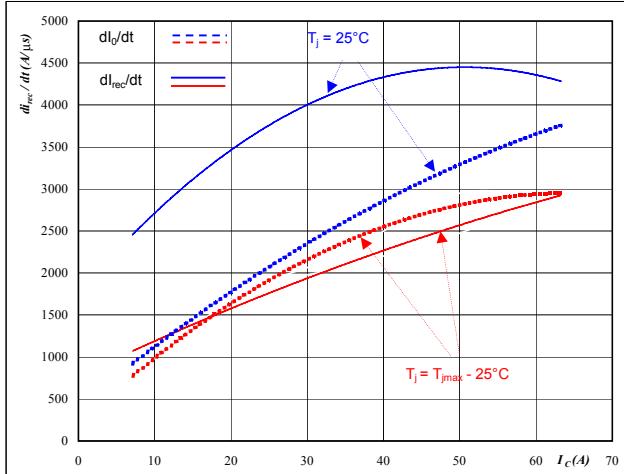
$$I_F = 35 \quad A$$

$$V_{GE} = \pm 15 \quad V$$

Output Inverter

Figure 17

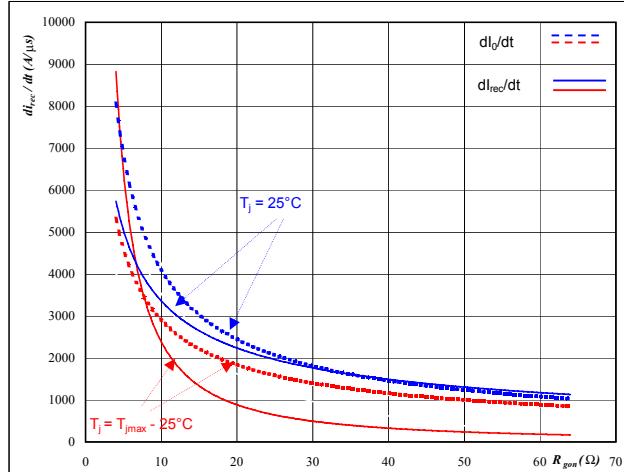
Typical rate of fall of forward
and reverse recovery current as a
function of collector current
 $dI_0/dt, dI_{rec}/dt = f(I_C)$


At

$T_j =$	25/150	°C
$V_{CE} =$	600	V
$V_{GE} =$	±15	V
$R_{gon} =$	16	Ω

Output inverter FRED
Figure 18

Typical rate of fall of forward
and reverse recovery current as a
function of IGBT turn on gate resistor
 $dI_0/dt, dI_{rec}/dt = f(R_{gon})$

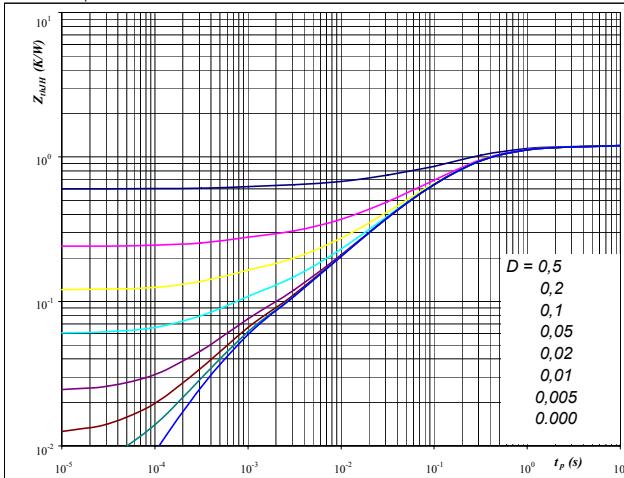

At

$T_j =$	25/150	°C
$V_R =$	600	V
$I_F =$	35	A
$V_{GE} =$	±15	V

Figure 19
Output inverter IGBT

IGBT transient thermal impedance
as a function of pulse width

$$Z_{thJH} = f(t_p)$$


At

$D =$	t_p / T
$R_{thJH} =$	1.20 K/W

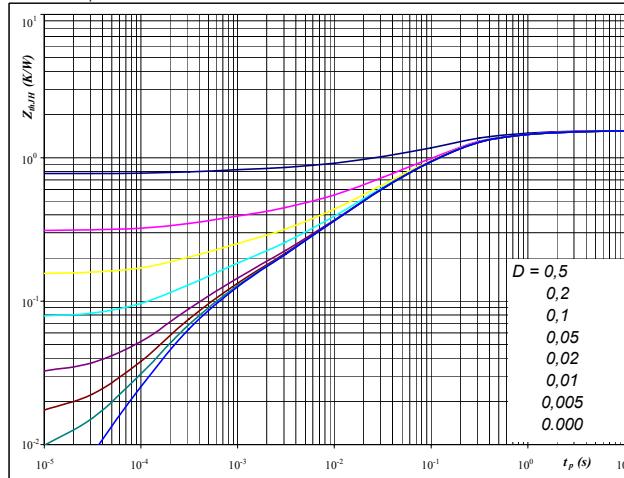
IGBT thermal model values

R (C/W)	Tau (s)
0.09	2.9E+00
0.42	3.4E-01
0.48	9.0E-02
0.16	1.1E-02
0.05	6.6E-04

Figure 20
Output inverter FRED

FRED transient thermal impedance
as a function of pulse width

$$Z_{thJH} = f(t_p)$$


At

$D =$	t_p / T
$R_{thJH} =$	1.55 K/W

FRED thermal model values

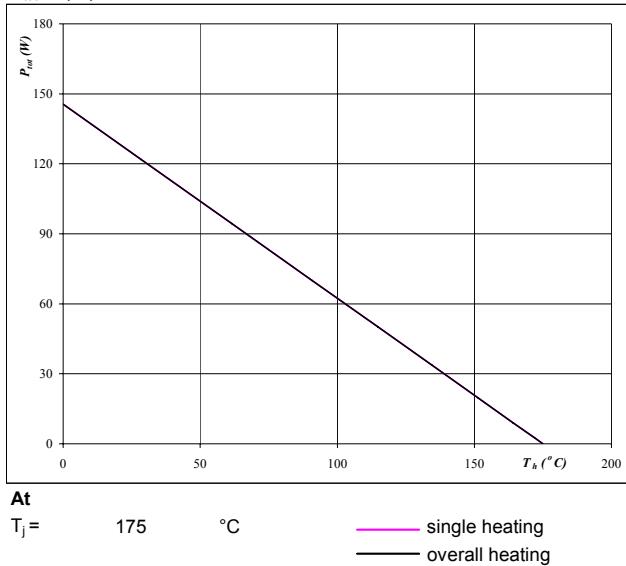
R (C/W)	Tau (s)
0.04	9.7E+00
0.22	8.1E-01
0.77	1.4E-01
0.33	2.1E-02
0.11	3.0E-03
0.08	3.4E-04

Output Inverter

Figure 21

Power dissipation as a function of heatsink temperature

$$P_{\text{tot}} = f(T_h)$$


At

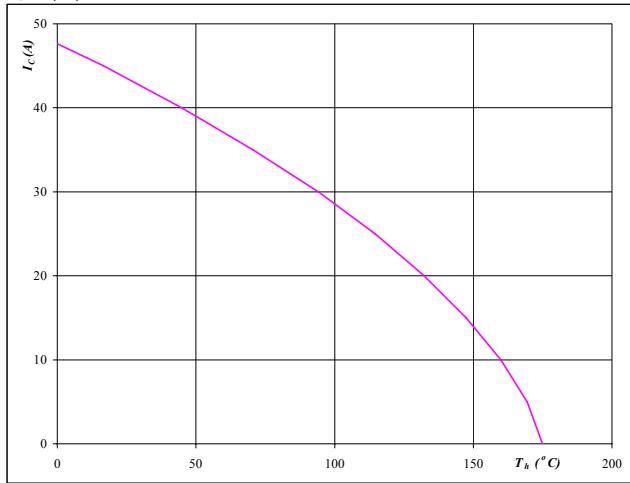
T_j = 175 °C

— single heating
— overall heating

Figure 22

Collector current as a function of heatsink temperature

$$I_C = f(T_h)$$


At

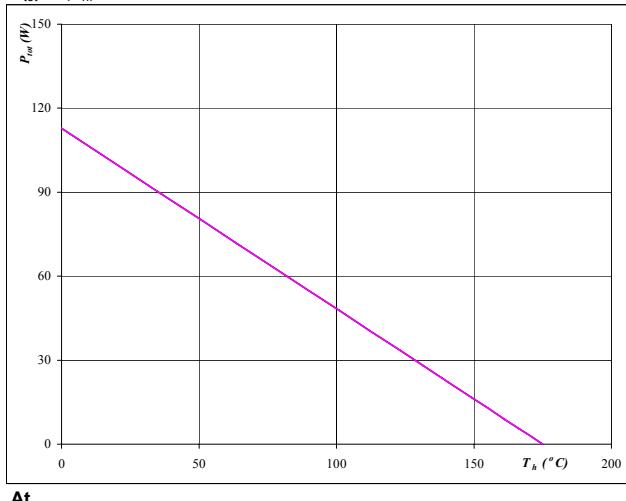
T_j = 175 °C

V_{GE} = 15 V

Figure 23
Output inverter FRED

Power dissipation as a function of heatsink temperature

$$P_{\text{tot}} = f(T_h)$$


At

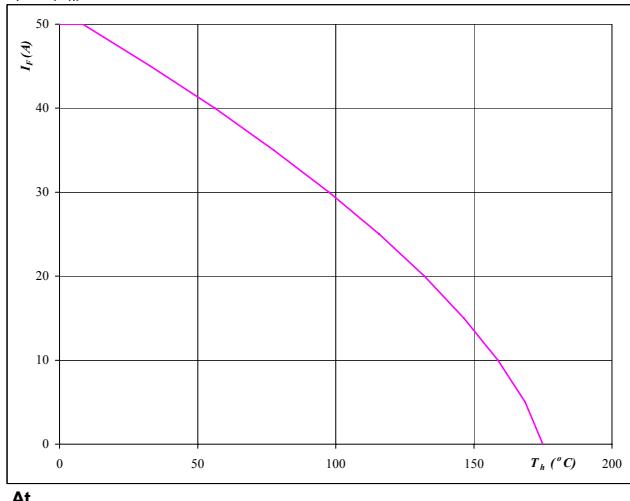
T_j = 175 °C

— single heating
— overall heating

Figure 24

Forward current as a function of heatsink temperature

$$I_F = f(T_h)$$


At

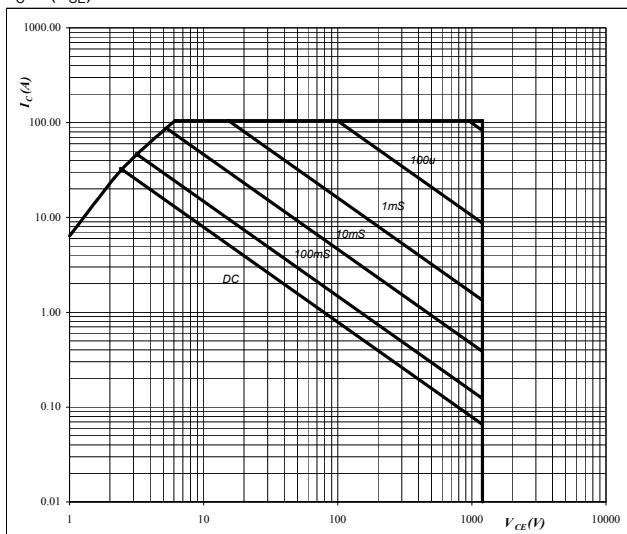
T_j = 175 °C

Output Inverter

Figure 25

**Safe operating area as a function
of collector-emitter voltage**

$$I_C = f(V_{CE})$$


At

D = single pulse

Th = 80 °C

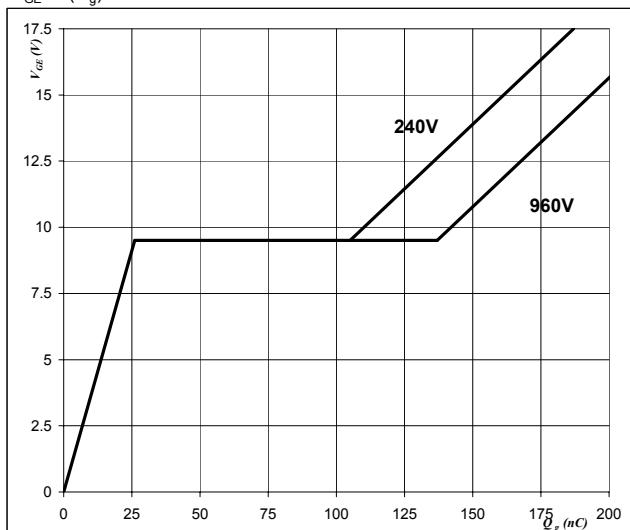
 V_{GE} = ±15 V

 T_j = T_{jmax} °C

Figure 26

Gate voltage vs Gate charge

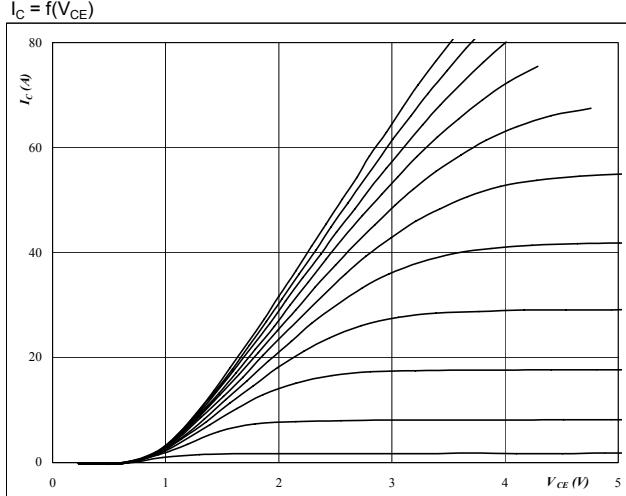
$$V_{GE} = f(Q_g)$$


At

 I_C = 35 A

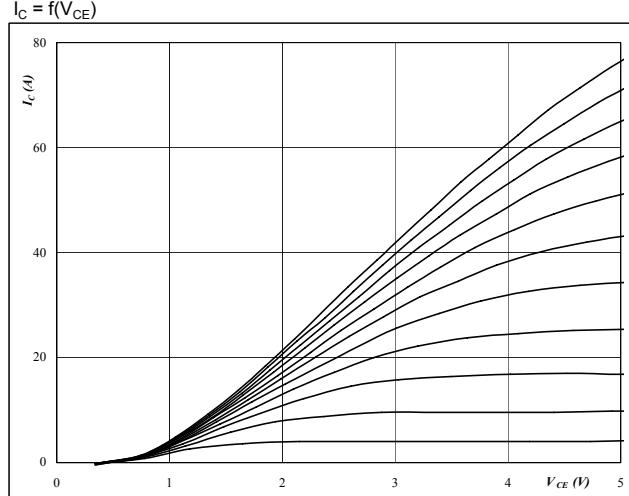
Brake

Figure 1
Typical output characteristics
 $I_C = f(V_{CE})$



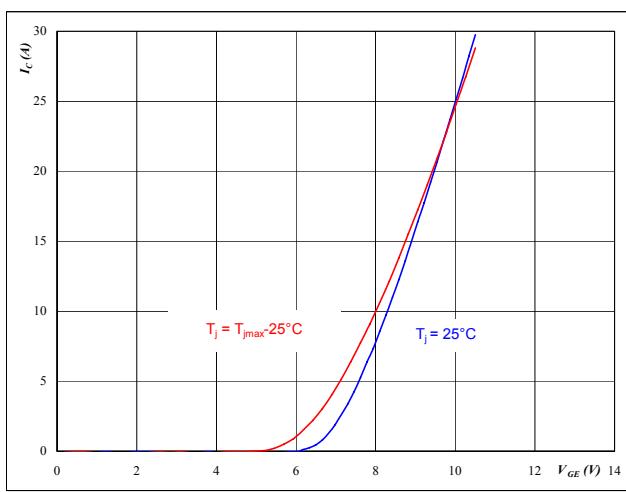
At
 $t_p = 250 \mu s$
 $T_j = 25 {}^\circ C$
 V_{GE} from 7 V to 17 V in steps of 1 V

Figure 2
Typical output characteristics
 $I_C = f(V_{CE})$



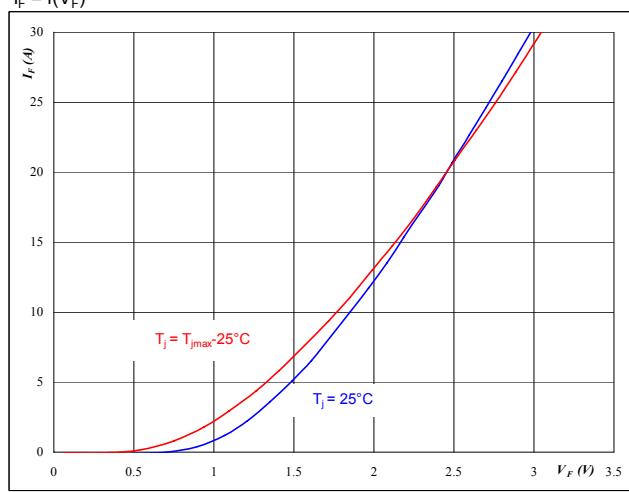
At
 $t_p = 250 \mu s$
 $T_j = 150 {}^\circ C$
 V_{GE} from 7 V to 17 V in steps of 1 V

Figure 3
Typical transfer characteristics
 $I_C = f(V_{GE})$



At
 $t_p = 250 \mu s$
 $V_{CE} = 10 V$

Figure 4
Typical diode forward current as a function of forward voltage
 $I_F = f(V_F)$



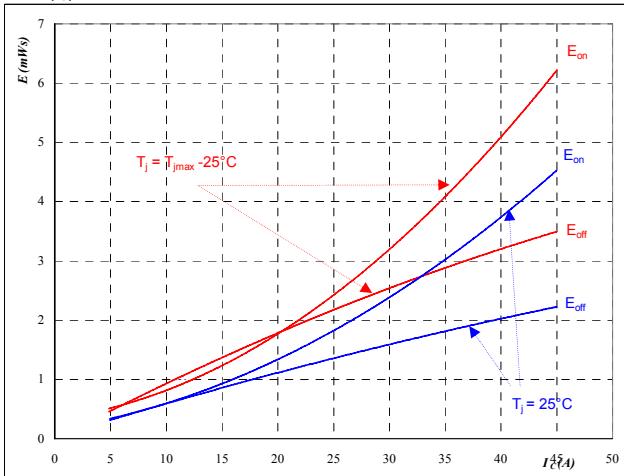
At
 $t_p = 250 \mu s$

Brake

Figure 5

**Typical switching energy losses
as a function of collector current**

$$E = f(I_C)$$



With an inductive load at

$$T_j = 25/150 \quad ^\circ C$$

$$V_{CE} = 600 \quad V$$

$$V_{GE} = \pm 15 \quad V$$

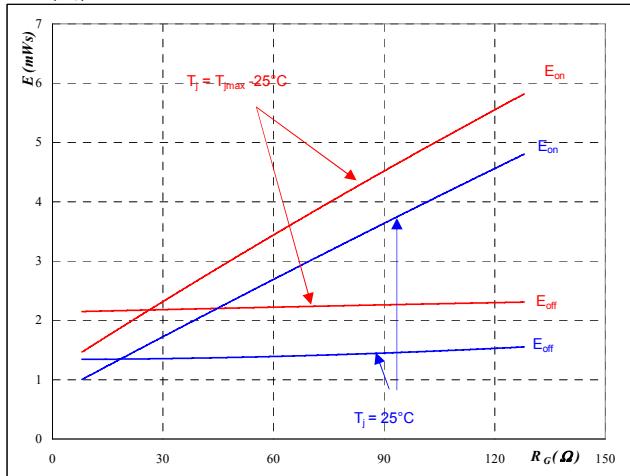
$$R_{gon} = 32 \quad \Omega$$

$$R_{goff} = 32 \quad \Omega$$

Brake IGBT
Figure 6

**Typical switching energy losses
as a function of gate resistor**

$$E = f(R_G)$$



With an inductive load at

$$T_j = 25/150 \quad ^\circ C$$

$$V_{CE} = 600 \quad V$$

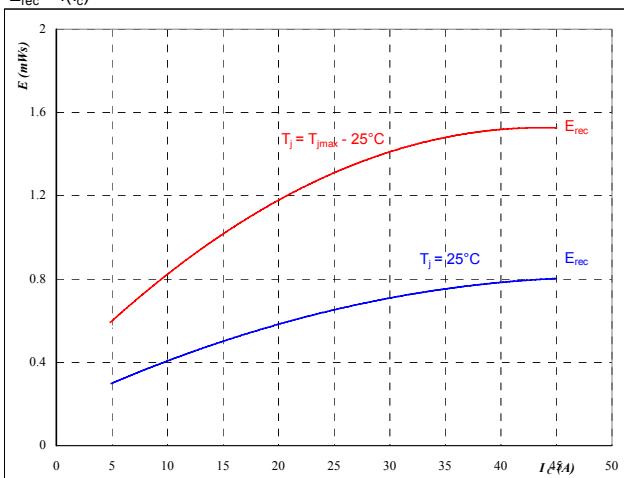
$$V_{GE} = \pm 15 \quad V$$

$$I_C = 25 \quad A$$

Figure 7

**Typical reverse recovery energy loss
as a function of collector current**

$$E_{rec} = f(I_C)$$



With an inductive load at

$$T_j = 25/150 \quad ^\circ C$$

$$V_{CE} = 600 \quad V$$

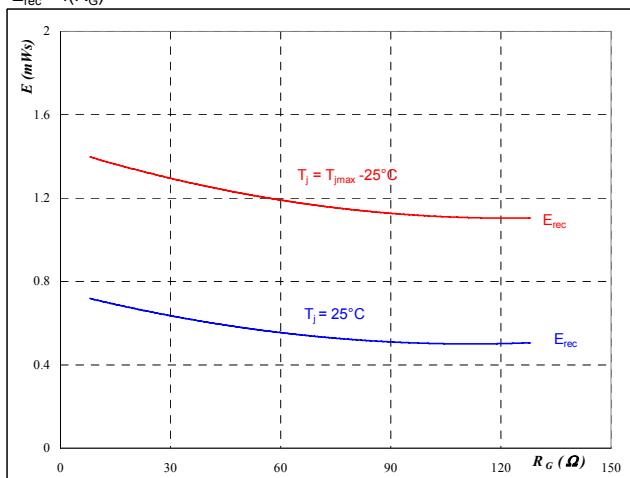
$$V_{GE} = \pm 15 \quad V$$

$$R_{gon} = 32 \quad \Omega$$

Brake IGBT
Figure 8

**Typical reverse recovery energy loss
as a function of gate resistor**

$$E_{rec} = f(R_G)$$



With an inductive load at

$$T_j = 25/150 \quad ^\circ C$$

$$V_{CE} = 600 \quad V$$

$$V_{GE} = \pm 15 \quad V$$

$$I_C = 25 \quad A$$

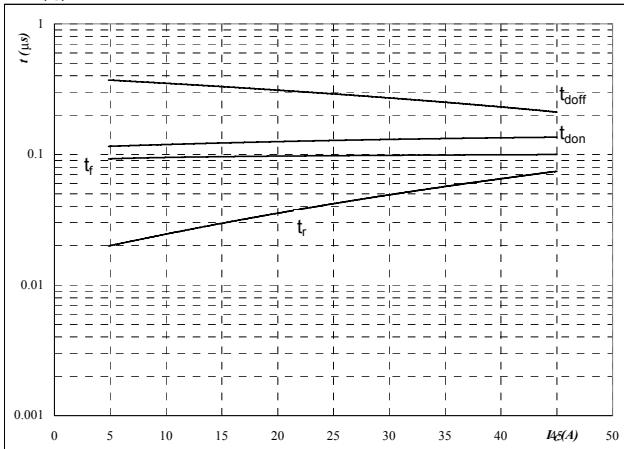
Brake

Figure 9

Brake IGBT

Typical switching times as a function of collector current

$t = f(I_C)$



With an inductive load at

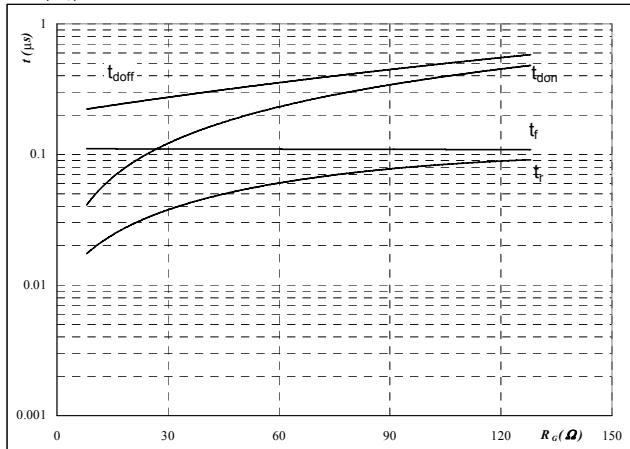
$T_j =$	25/150	°C
$V_{CE} =$	600	V
$V_{GE} =$	± 15	V
$R_{gon} =$	32	Ω
$R_{goff} =$	32	Ω

Figure 10

Brake IGBT

Typical switching times as a function of gate resistor

$t = f(R_G)$



With an inductive load at

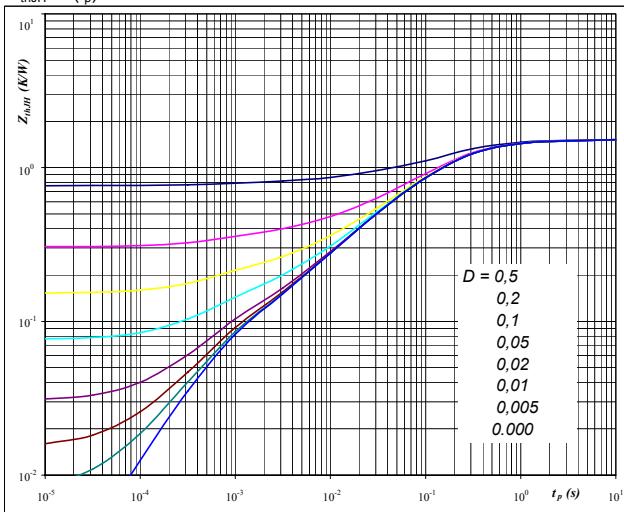
$T_j =$	25/150	°C
$V_{CE} =$	600	V
$V_{GE} =$	± 15	V
$I_C =$	25	A

Figure 11

Brake IGBT

IGBT transient thermal impedance as a function of pulse width

$Z_{thJH} = f(t_p)$



At

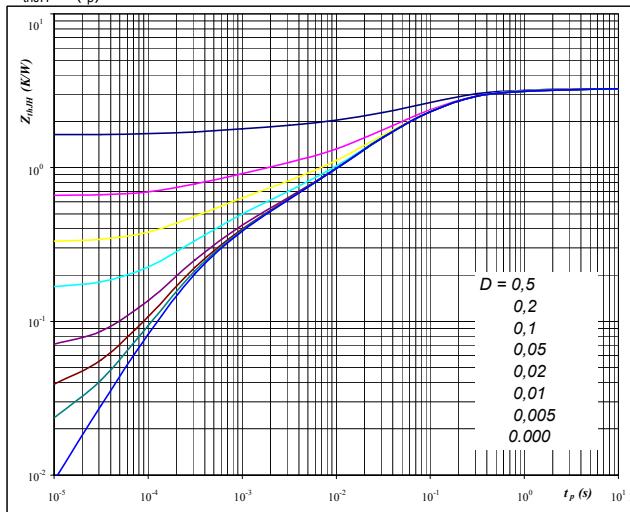
$D =$	t_p / T
$R_{thJH} =$	1.53 K/W

Figure 12

Brake FRED

FRED transient thermal impedance as a function of pulse width

$Z_{thJH} = f(t_p)$



At

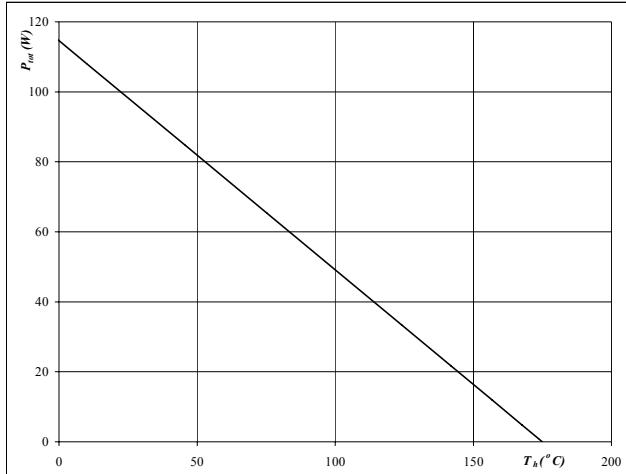
$D =$	t_p / T
$R_{thJH} =$	3.28 K/W

Brake

Figure 13

Power dissipation as a function of heatsink temperature

$$P_{\text{tot}} = f(T_h)$$

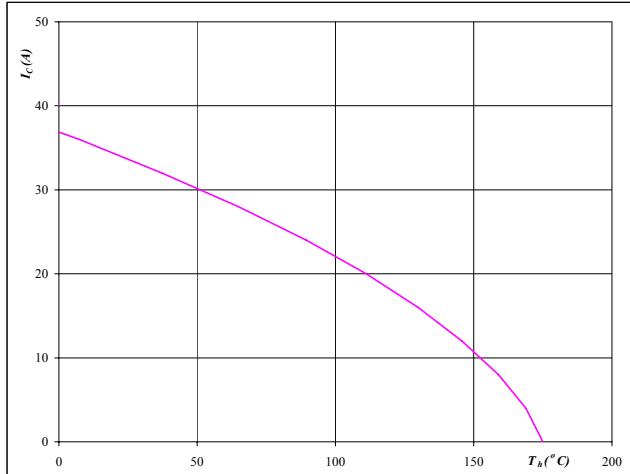

At

$$T_j = 175 \quad {}^\circ\text{C}$$

Brake IGBT
Figure 14

Collector current as a function of heatsink temperature

$$I_C = f(T_h)$$


At

$$T_j = 175 \quad {}^\circ\text{C}$$

$$V_{GE} = 15 \quad \text{V}$$

Figure 15

Power dissipation as a function of heatsink temperature

$$P_{\text{tot}} = f(T_h)$$

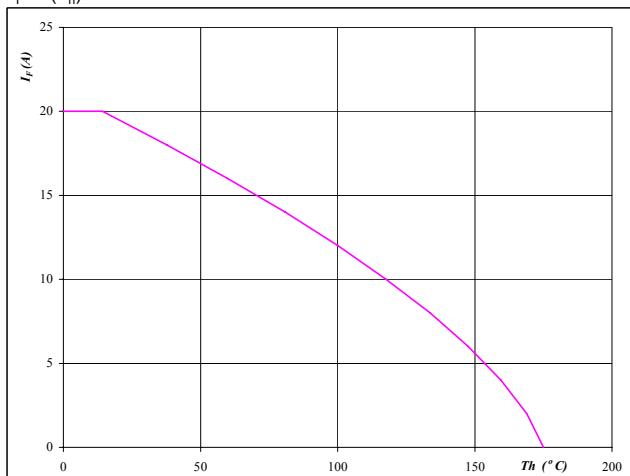

At

$$T_j = 175 \quad {}^\circ\text{C}$$

Brake IGBT
Figure 16

Forward current as a function of heatsink temperature

$$I_F = f(T_h)$$


At

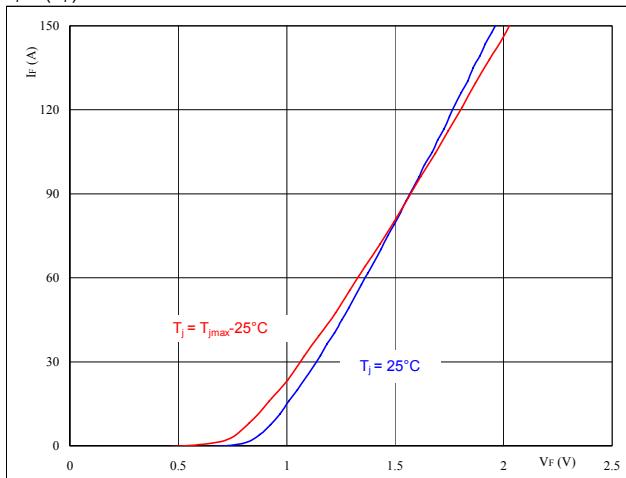
$$T_j = 175 \quad {}^\circ\text{C}$$

Input Rectifier Bridge

Figure 1

Typical diode forward current as a function of forward voltage

$$I_F = f(V_F)$$

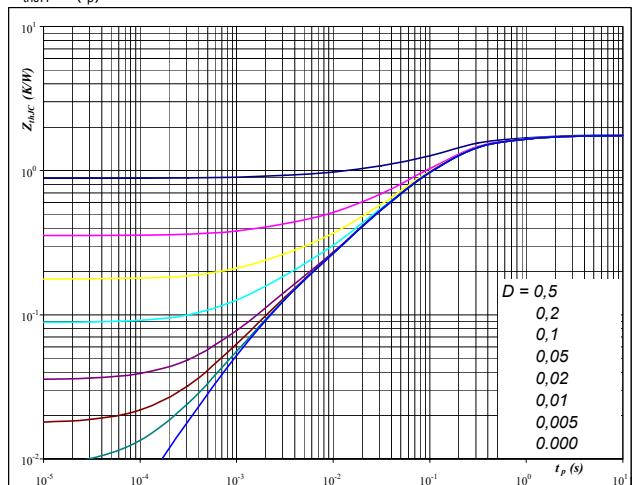

At

$$t_p = 250 \mu\text{s}$$

Rectifier diode
Figure 2

Diode transient thermal impedance as a function of pulse width

$$Z_{thJH} = f(t_p)$$


At

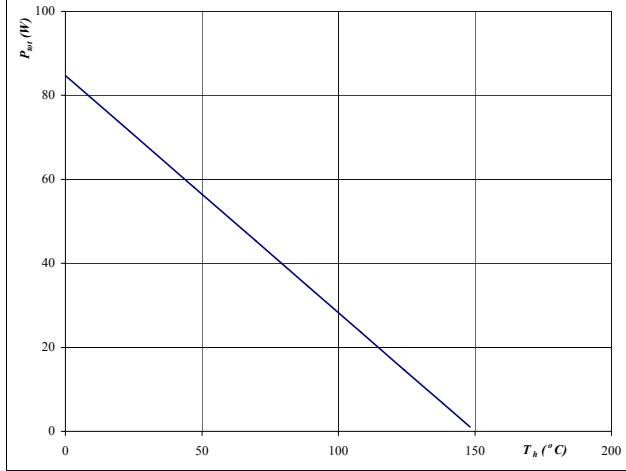
$$D = t_p / T$$

$$R_{thJH} = 1.770 \text{ K/W}$$

Rectifier diode
Figure 3
Rectifier diode

Power dissipation as a function of heatsink temperature

$$P_{tot} = f(T_h)$$

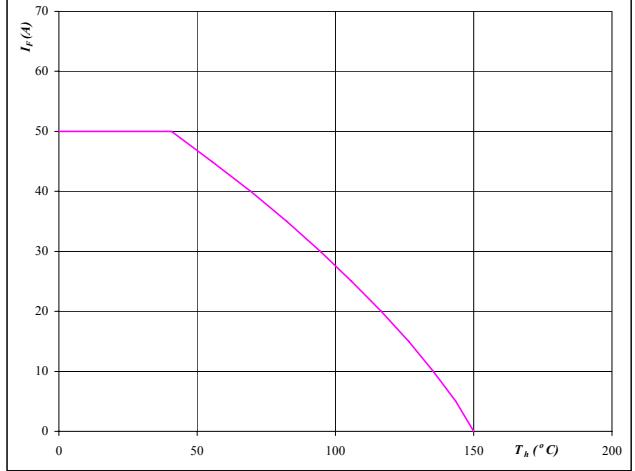

At

$$T_j = 150 ^\circ\text{C}$$

Figure 4
Rectifier diode

Forward current as a function of heatsink temperature

$$I_F = f(T_h)$$


At

$$T_j = 150 ^\circ\text{C}$$

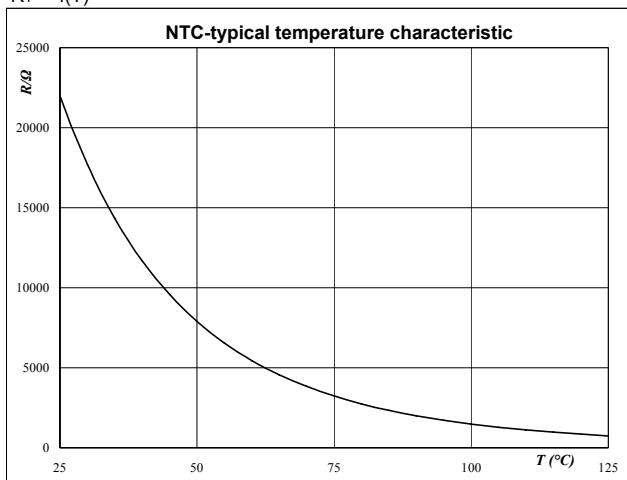
Thermistor

Figure 1

Thermistor

Typical NTC characteristic
as a function of temperature

$$R_T = f(T)$$



Switching Definitions Output Inverter

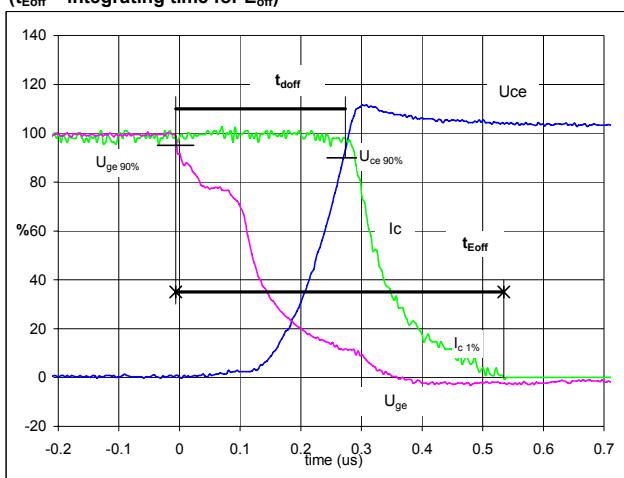
General conditions

T_j	= 150 °C
R_{gon}	= 16 Ω
R_{goff}	= 16 Ω

Figure 1

Output inverter IGBT

Turn-off Switching Waveforms & definition of t_{doff} , t_{Eoff}
 $(t_{Eoff} = \text{integrating time for } E_{off})$

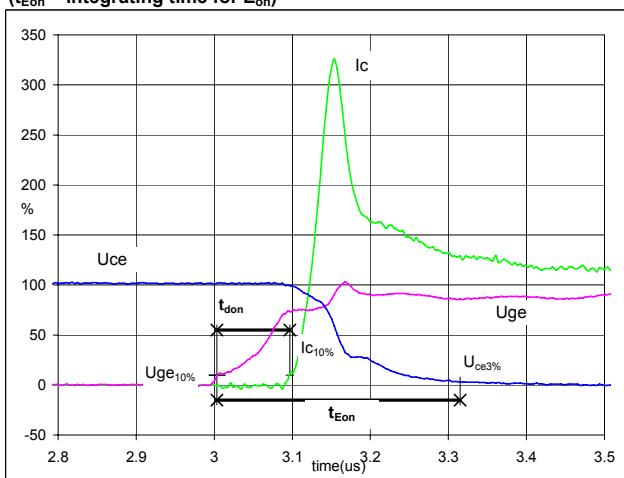


$V_{GE}(0\%) = -15$ V
 $V_{GE}(100\%) = 15$ V
 $V_C(100\%) = 600$ V
 $I_C(100\%) = 35$ A
 $t_{doff} = 0.27$ μs
 $t_{Eoff} = 0.54$ μs

Figure 2

Output inverter IGBT

Turn-on Switching Waveforms & definition of t_{don} , t_{Eon}
 $(t_{Eon} = \text{integrating time for } E_{on})$

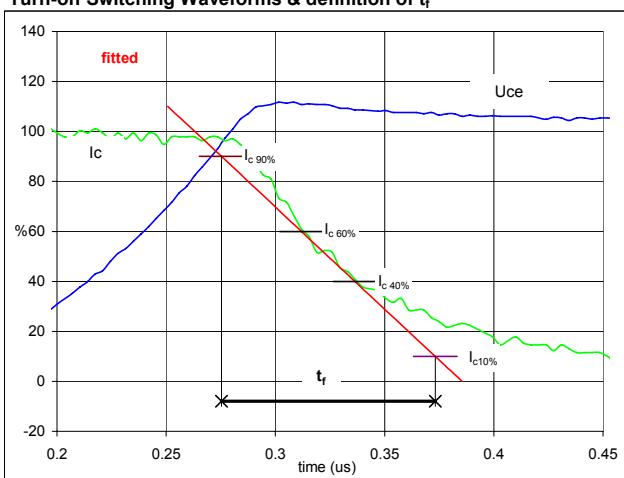


$V_{GE}(0\%) = -15$ V
 $V_{GE}(100\%) = 15$ V
 $V_C(100\%) = 600$ V
 $I_C(100\%) = 35$ A
 $t_{don} = 0.09$ μs
 $t_{Eon} = 0.31$ μs

Figure 3

Output inverter IGBT

Turn-off Switching Waveforms & definition of t_f

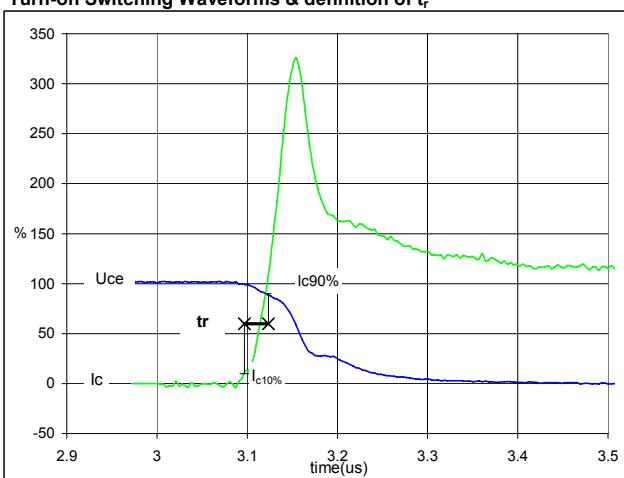


$V_C(100\%) = 600$ V
 $I_C(100\%) = 35$ A
 $t_f = 0.11$ μs

Figure 4

Output inverter IGBT

Turn-on Switching Waveforms & definition of t_r

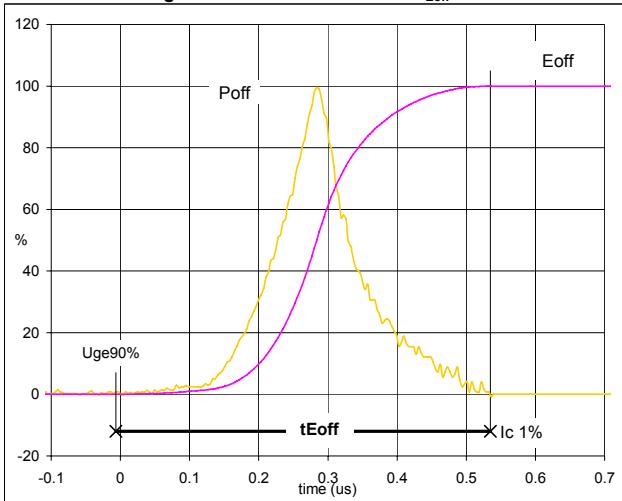


$V_C(100\%) = 600$ V
 $I_C(100\%) = 35$ A
 $t_r = 0.02$ μs

Switching Definitions Output Inverter

Figure 5

Output inverter IGBT

Turn-off Switching Waveforms & definition of t_{Eoff}


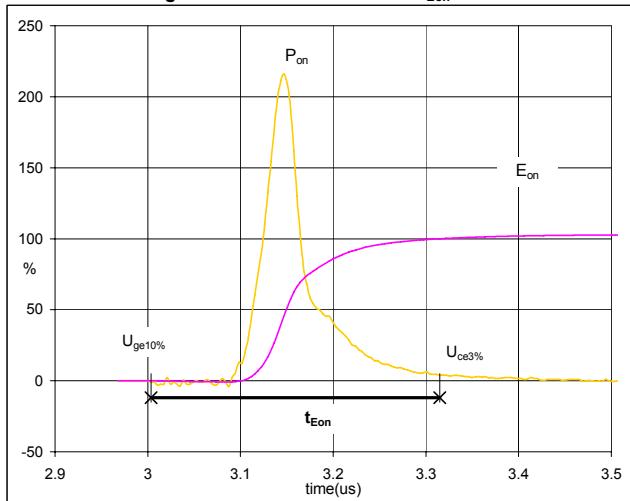
$P_{off} (100\%) = 21.01 \text{ kW}$

$E_{off} (100\%) = 2.82 \text{ mJ}$

$t_{Eoff} = 0.54 \mu\text{s}$

Figure 6

Output inverter IGBT

Turn-on Switching Waveforms & definition of t_{Eon}


$P_{on} (100\%) = 21.01 \text{ kW}$

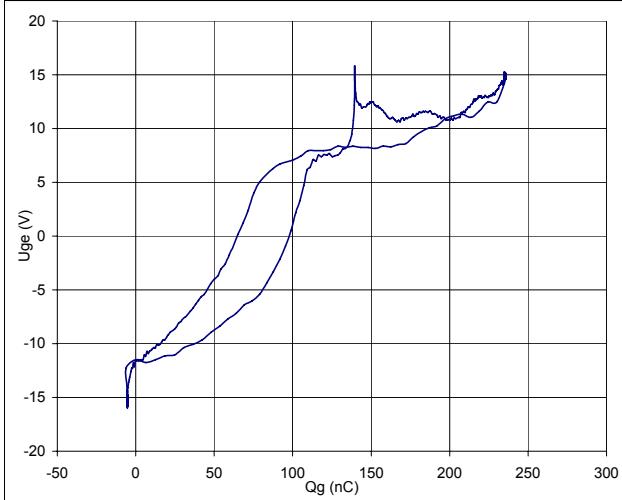
$E_{on} (100\%) = 2.49 \text{ mJ}$

$t_{Eon} = 0.31 \mu\text{s}$

Figure 7

Output inverter FRED

Gate voltage vs Gate charge (measured)



$V_{GEoff} = -15 \text{ V}$

$V_{GEon} = 15 \text{ V}$

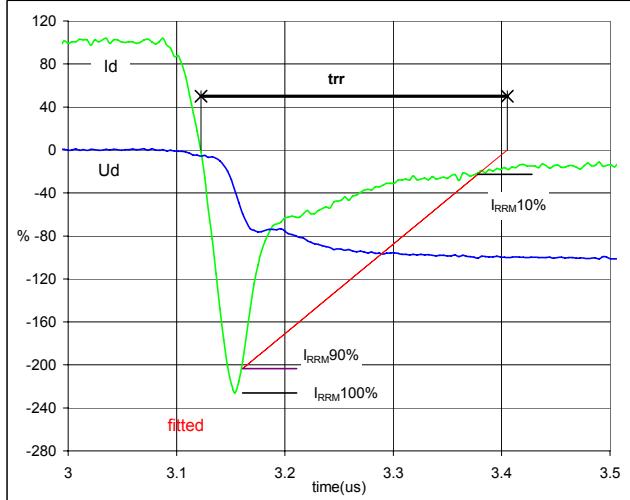
$V_C (100\%) = 600 \text{ V}$

$I_C (100\%) = 35 \text{ A}$

$Q_g = 1239.53 \text{ nC}$

Figure 8

Output inverter IGBT

Turn-off Switching Waveforms & definition of t_{trr}


$V_d (100\%) = 600 \text{ V}$

$I_d (100\%) = 35 \text{ A}$

$I_{RRM} (100\%) = -79 \text{ A}$

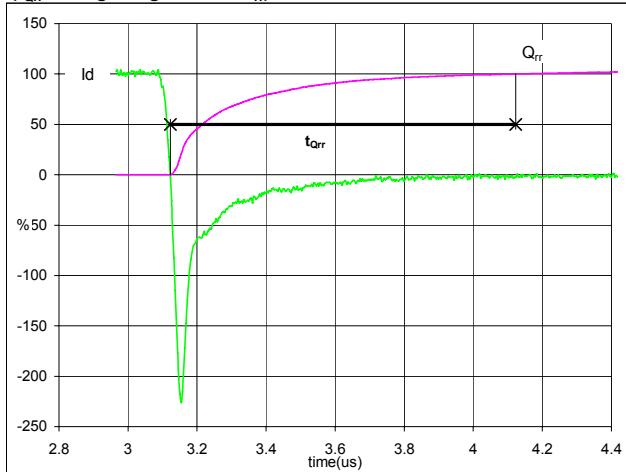
$t_{trr} = 0.28 \mu\text{s}$

Switching Definitions Output Inverter

Figure 9

Output inverter FRED

Turn-on Switching Waveforms & definition of t_{Qrr}
 $(t_{Qrr} = \text{integrating time for } Q_{rr})$

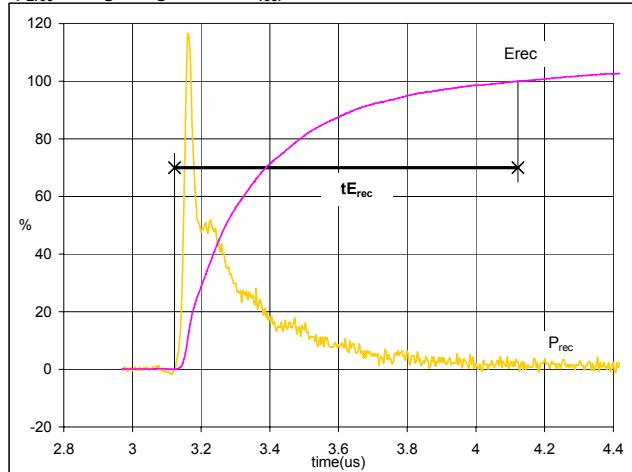


$I_d(100\%) = 35 \text{ A}$
 $Q_{rr}(100\%) = 7.47 \mu\text{C}$
 $t_{Qrr} = 1.00 \mu\text{s}$

Figure 10

Output inverter FRED

Turn-on Switching Waveforms & definition of t_{Erec}
 $(t_{Erec} = \text{integrating time for } E_{rec})$



$P_{rec}(100\%) = 21.01 \text{ kW}$
 $E_{rec}(100\%) = 3.31 \text{ mJ}$
 $t_{Erec} = 1.00 \mu\text{s}$

flowPIM 1 3rd gen
Output Inverter Application
1200V / 35A
General conditions
3phase SPWM

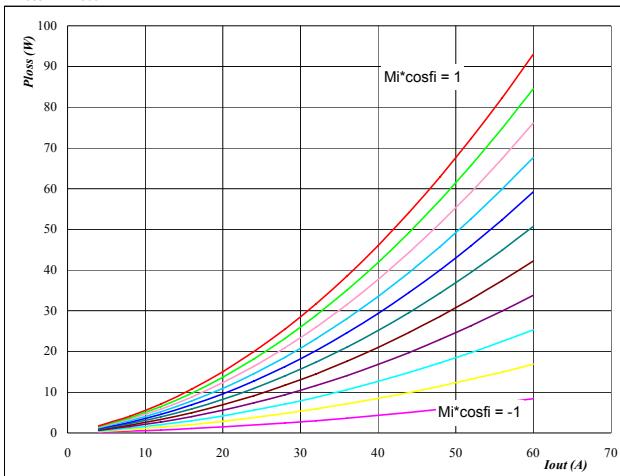
$V_{G\text{On}}$	= 15 V
$V_{G\text{Off}}$	= -15 V
$R_{g\text{on}}$	= 16 Ω
$R_{g\text{off}}$	= 16 Ω

Figure 1

IGBT

Typical average static loss as a function of output current

$$P_{\text{loss}} = f(I_{\text{out}})$$

**At**

$$T_j = 150 \text{ } ^\circ\text{C}$$

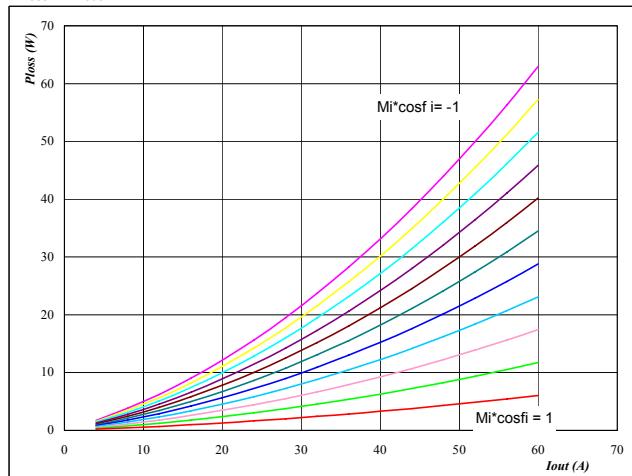
Mi*cosφ from -1 to 1 in steps of 0,2

Figure 2

FRED

Typical average static loss as a function of output current

$$P_{\text{loss}} = f(I_{\text{out}})$$

**At**

$$T_j = 150 \text{ } ^\circ\text{C}$$

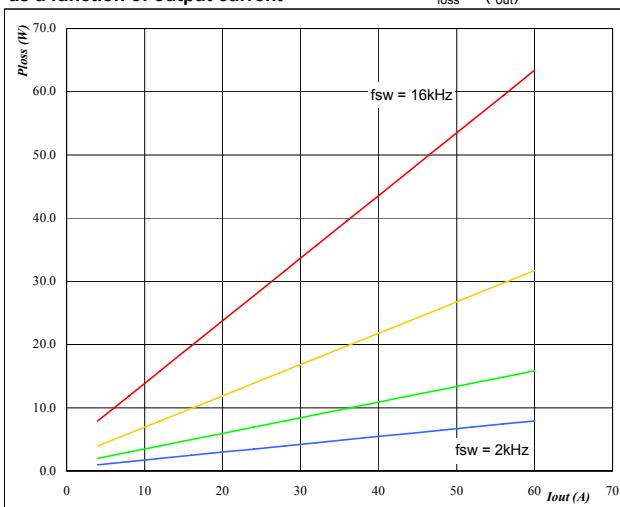
Mi*cosφ from -1 to 1 in steps of 0,2

Figure 3

IGBT

Typical average switching loss as a function of output current

$$P_{\text{loss}} = f(I_{\text{out}})$$

**At**

$$T_j = 150 \text{ } ^\circ\text{C}$$

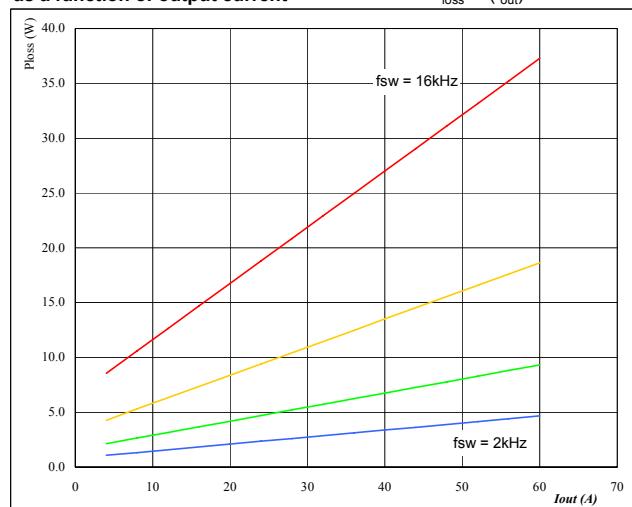
$$\text{DC link} = 600 \text{ V}$$

f_{sw} from 2 kHz to 16 kHz in steps of factor 2**Figure 4**

FRED

Typical average switching loss as a function of output current

$$P_{\text{loss}} = f(I_{\text{out}})$$

**At**

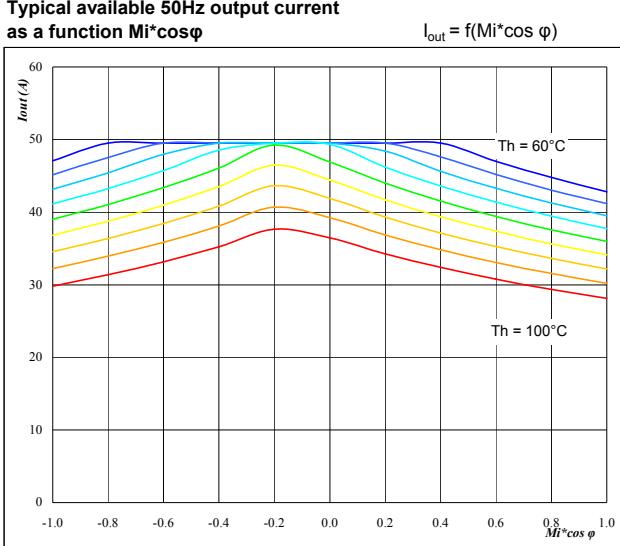
$$T_j = 150 \text{ } ^\circ\text{C}$$

$$\text{DC link} = 600 \text{ V}$$

f_{sw} from 2 kHz to 16 kHz in steps of factor 2

Figure 5

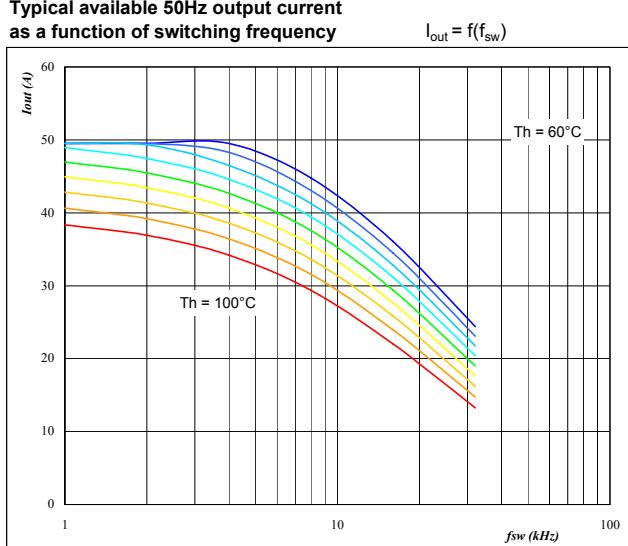
Typical available 50Hz output current
as a function $M_i \cos \varphi$

**At**

$T_j = 150^\circ C$
DC link = 600 V
 $f_{sw} = 8$ kHz
 T_h from $60^\circ C$ to $100^\circ C$ in steps of $5^\circ C$

Phase**Figure 6**

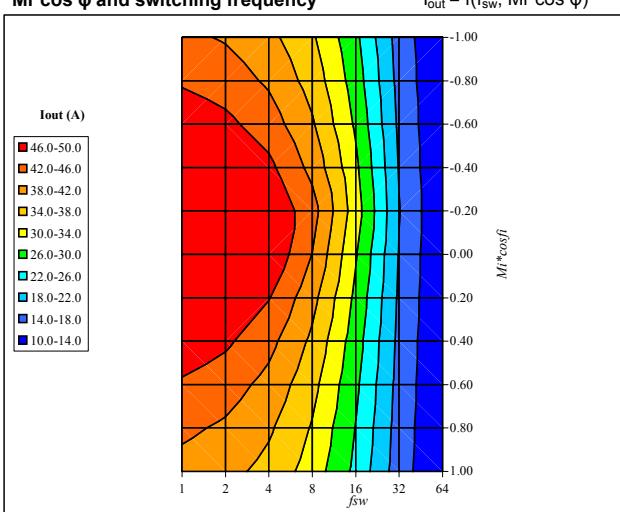
Typical available 50Hz output current
as a function of switching frequency

**At**

$T_j = 150^\circ C$
DC link = 600 V
 $M_i \cos \varphi = 0.8$
 T_h from $60^\circ C$ to $100^\circ C$ in steps of $5^\circ C$

Figure 7

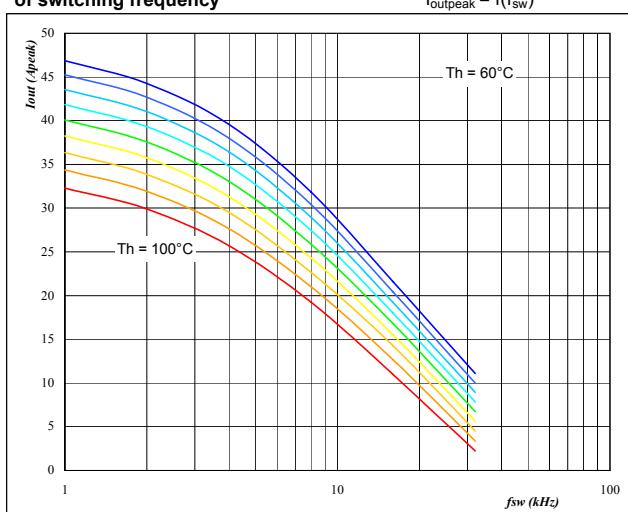
Typical available 50Hz output current as a function of
 $M_i \cos \varphi$ and switching frequency

**At**

$T_j = 150^\circ C$
DC link = 600 V
 $T_h = 90^\circ C$

Phase**Figure 8**

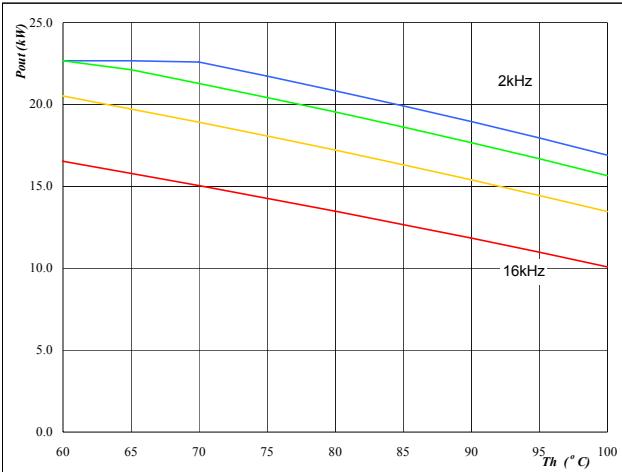
Typical available 0Hz output current as a function
of switching frequency

**At**

$T_j = 150^\circ C$
DC link = 600 V
 T_h from $60^\circ C$ to $100^\circ C$ in steps of $5^\circ C$
 $M_i = 0$

Figure 9

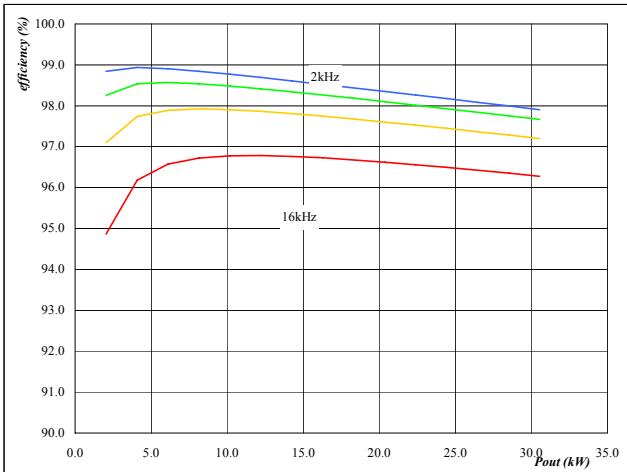
Typical available peak output power as a function of heatsink temperature
 $P_{out}=f(T_h)$

**At**

$T_j = 150 \text{ } ^\circ\text{C}$
 DC link = 600 V
 $M_i = 1$
 $\cos \varphi = 0.80$
 f_{sw} from 2 kHz to 16 kHz in steps of factor 2

Inverter**Figure 10**

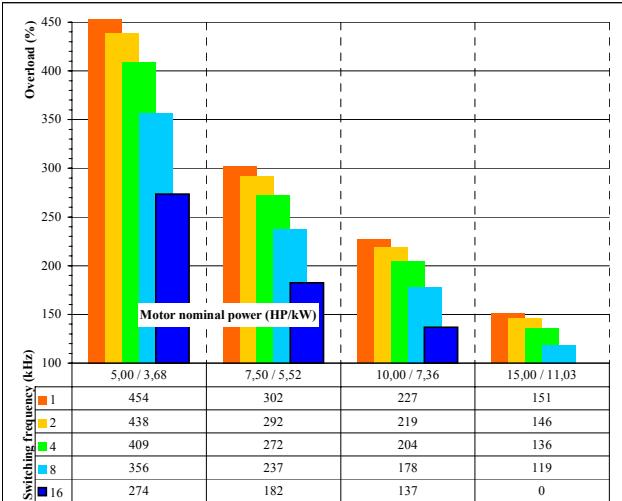
Typical efficiency as a function of output power
 $\text{efficiency}=f(P_{out})$

**At**

$T_j = 150 \text{ } ^\circ\text{C}$
 DC link = 600 V
 $M_i = 1$
 $\cos \varphi = 0.80$
 f_{sw} from 2 kHz to 16 kHz in steps of factor 2

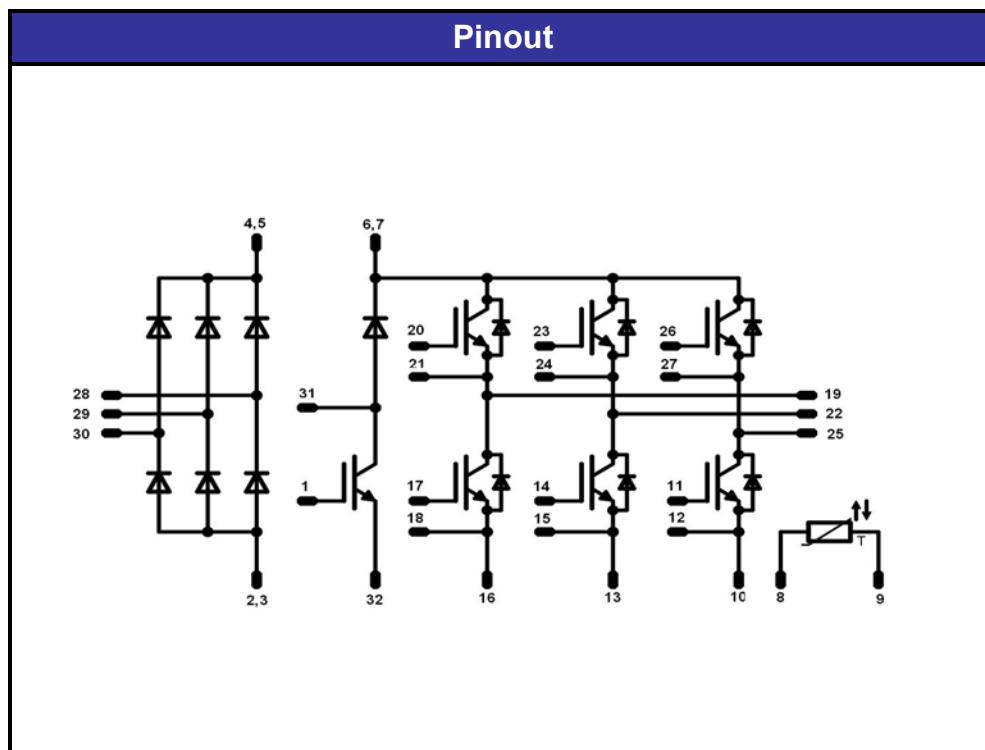
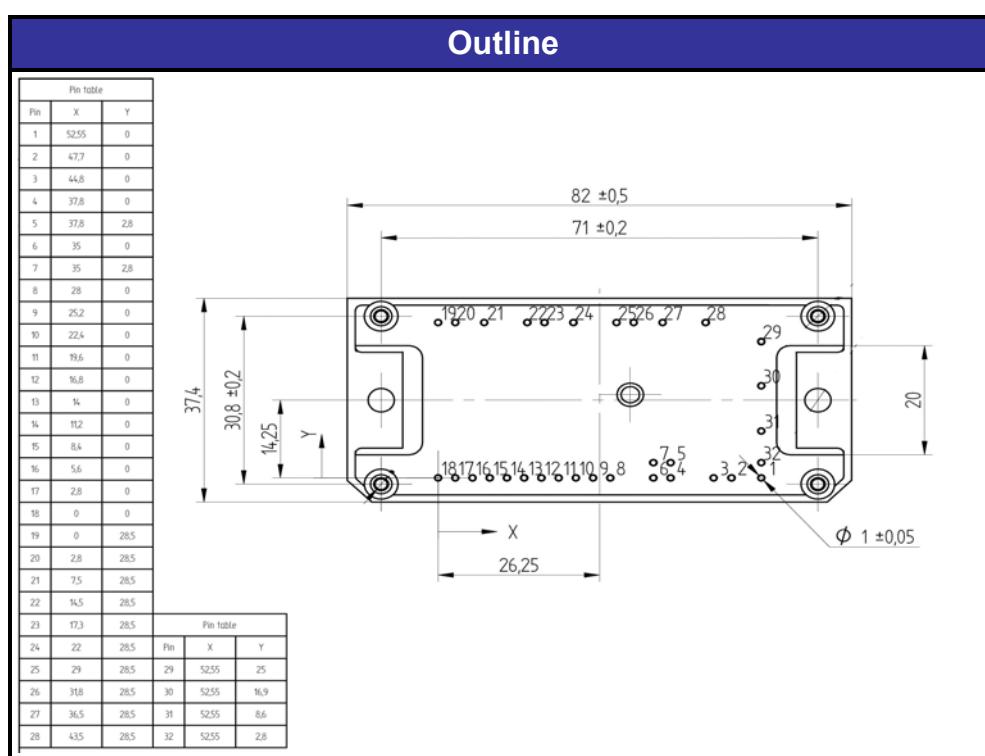
Figure 11

Typical available overload factor as a function of motor power and switching frequency
 $P_{peak} / P_{nom}=f(P_{nom}, f_{sw})$

**At**

$T_j = 150 \text{ } ^\circ\text{C}$
 DC link = 600 V
 $M_i = 1$
 $\cos \varphi = 0.8$
 f_{sw} from 1 kHz to 16 kHz in steps of factor 2
 $T_h = 90 \text{ } ^\circ\text{C}$
 Motor eff = 0.85

Package Outline and Pinout



PRODUCT STATUS DEFINITIONS

Datasheet Status	Product Status	Definition
Target	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice. The data contained is exclusively intended for technically trained staff.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data may be published at a later date. Vincotech reserves the right to make changes at any time without notice in order to improve design. The data contained is exclusively intended for technically trained staff.
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2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.