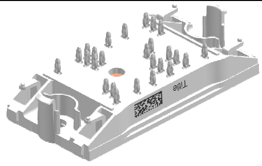
flow 3xBOOST0-SiC — 1200V/80mΩ

Features

- SiC-Power MOSFET´s and Schottky Diodes
- 3 channel boost topology
- Ultra Low Inductance with integrated DC-capacitors
- Switching frequency >100kHz
- Temperature sensor

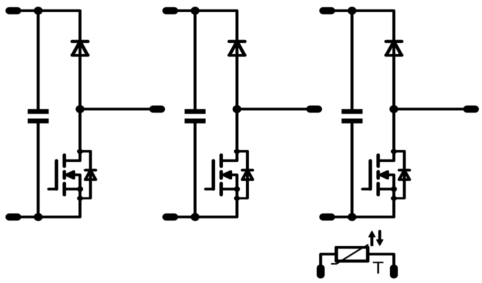
flow 0 12mm housing



Target Applications

- solar inverter
- Power Supply

Schematic



Types

- 10-PZ123BA080MR-M909L28Y

Maximum Ratings

Tj=25°C, unless otherwise specified

Parameter	Symbol	Condition		Value	Unit
T1, T3, T5					
Drain to source breakdown voltage	V_{DS}			1200	V
DC drain current	I_D	$T_j=T_jmax$	$T_h=80°C$	19	A
Pulsed drain current	I_{Dpulse}	t_p limited by T_jmax		80	A
Power dissipation	P_{tot}	$T_j=T_jmax$	$T_h=80°C$	50	W
Gate-source peak voltage	V_{GS}			-6/+22	V
Maximum Junction Temperature	T_jmax			150	°C
D1, D3, D5					
Peak Repetitive Reverse Voltage	V_{RRM}			1200	V
Forward average current	I_{FAV}	$T_j=T_jmax$	$T_h=80°C$	19	A
Non-Repetitive Peak Forward Surge Current	I_{FSM}	$t_p=8,3ms$	$T_j=25°C$	46	A
Repetitive Peak Forward Surge Current	I_{FRM}	t_p limited by T_jmax		50	A
Power dissipation per Diode	P_{tot}	$T_j=T_jmax$	$T_h=80°C$	56	W
Maximum Junction Temperature	T_jmax			175	°C

Maximum Ratings

Tj=25°C, unless otherwise specified

Parameter	Symbol	Condition	Value	Unit
C1, C2, C3				
Max.DC voltage	V_{MAX}	Tc=25°C	1000	V
Thermal Properties				
Storage temperature	T_{stg}		-40…+125	°C
Operation temperature under switching condition	T_{op}		-40…+(Tjmax - 25)	°C
Insulation Properties				
Insulation voltage		t=2s DC voltage	4000	V
Creepage distance			min 12,7	mm
Clearance			min 9,9	mm

Characteristic Values

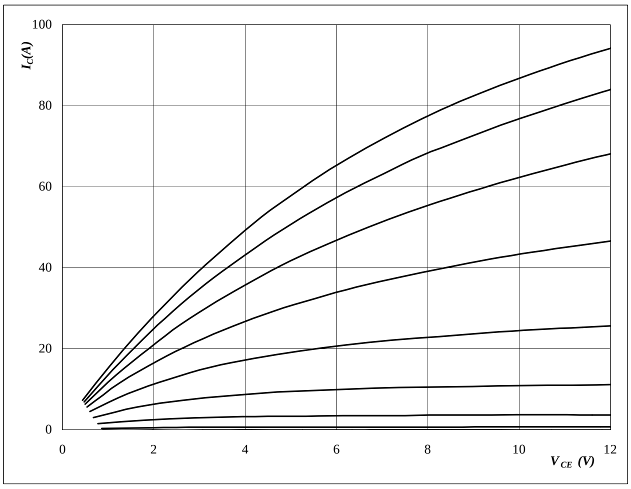
Parameter	Symbol	Conditions					Value			Unit
			V_{GE} [V] or V_{GS} [V]	V_r [V] or V_{CE} [V] or V_{DS} [V]	I_C [A] or I_F [A] or I_D [A]	T_j	Min	Typ	Max	
T1, T3, T5										
Static drain to source ON resistance	$R_{DS(on)}$		20		20	Tj=25°C Tj=125°C		70,00 110,00		mΩ
Gate threshold voltage	$V_{(GS)th}$	$V_{DS} = V_{GS}$			0,0044	Tj=25°C Tj=125°C	1,6		4	V
Gate to Source Leakage Current	I_{gss}		-6/22			Tj=25°C Tj=125°C			200	nA
Zero Gate Voltage Drain Current	I_{dss}		0	1200		Tj=25°C Tj=125°C			10	µA
Internal Gate Resistance	R_G							9		Ω
Turn On Delay Time	$t_{d(ON)}$	Rgoff=4 Ω Rgon=4 Ω	16	700	16	Tj=25°C Tj=125°C		16 14		ns
Rise Time	t_r					Tj=25°C Tj=125°C		10 9		
Turn off delay time	$t_{d(OFF)}$					Tj=25°C Tj=125°C		112 128		
Fall time	t_f					Tj=25°C Tj=125°C		7 7		
Turn-on energy loss per pulse	E_{on}					Tj=25°C Tj=125°C		0,629 0,425		mWs
Turn-off energy loss per pulse	E_{off}					Tj=25°C Tj=125°C		0,182 0,194		
Total gate charge	Q_g		18	400	10	Tj=25°C		106		nC
Gate to source charge	Q_{gs}							27		
Gate to drain charge	Q_{gd}							31		
Input capacitance	C_{iss}	f=1MHz	0	800				2080		pF
Output capacitance	C_{oss}							77		
Reverse transfer capacitance	C_{rss}							16		
Thermal resistance chip to heatsink per chip	R_{thJH}	Phase-Change Material						1,41		K/W
D1, D3, D5										
Forward voltage	V_F				10	Tj=25°C Tj=125°C	0,8	1,40 1,70	1,7	V
Reverse leakage current	I_{rm}			1200		Tj=25°C Tj=125°C			100	µA
Peak recovery current	I_{RRM}	Rgon=4 Ω	16	700	16	Tj=25°C Tj=125°C		6 8		A
Reverse recovery time	t_{rr}					Tj=25°C Tj=125°C		12 12		ns
Reverse recovery charge	Q_{rr}					Tj=25°C Tj=125°C		0,137 0,123		µC
Reverse recovered energy	E_{rec}					Tj=25°C Tj=125°C		0,050 0,044		mWs
Peak rate of fall of recovery current	di(rec)max /dt					Tj=25°C Tj=125°C		1336 1726		A/µs
Thermal resistance chip to heatsink per chip	R_{thJH}	Phase-Change Material						1,70		K/W
C1, C2, C3										
C value	C							47		nF
Thermistor										
Rated resistance	R					T=25°C		22000		Ω
Deviation of R100	ΔR/R	R100=1486 Ω				T=25°C	-5		5	%
Power dissipation	P					T=25°C		200		mW
Power dissipation constant						T=25°C		2		mW/K
B-value	$B_{(25/50)}$	Tol. ±3%				T=25°C		3950		K
B-value	$B_{(25/100)}$	Tol. ±3%				T=25°C		3996		K
Vincotech NTC Reference									B	

T1, T3, T5 / D1, D3, D5

Figure 1 T1, T3, T5 MOSFET

Typical output characteristics

$I_D = f(V_{DS})$



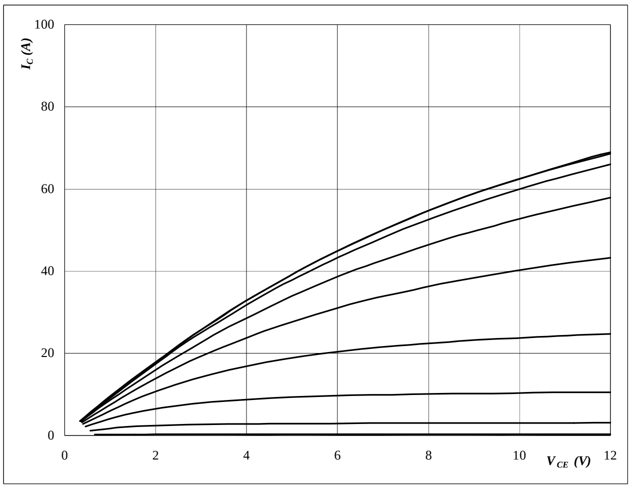
At

t_p =	250	µs
T_j =	25	°C
V_{GS} from	0 V to 20 V in steps of 2 V	

Figure 2 T1, T3, T5 MOSFET

Typical output characteristics

$I_D = f(V_{DS})$



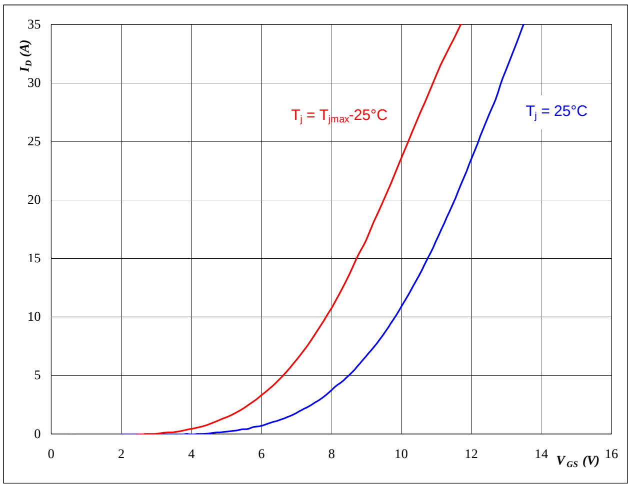
At

t_p =	250	µs
T_j =	125	°C
V_{GS} from	0 V to 20 V in steps of 2 V	

Figure 3 T1, T3, T5 MOSFET

Typical transfer characteristics

$I_D = f(V_{GS})$



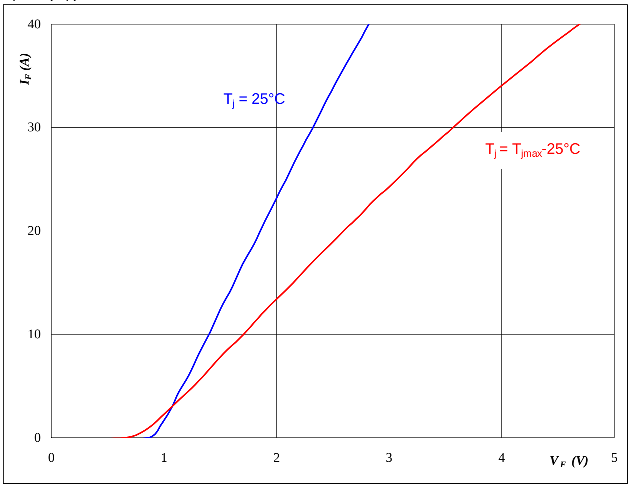
At

t_p =	250	µs
V_{DS} =	10	V

Figure 4 D1, D3, D5 FWD

Typical diode forward current as a function of forward voltage

$I_F = f(V_F)$



At

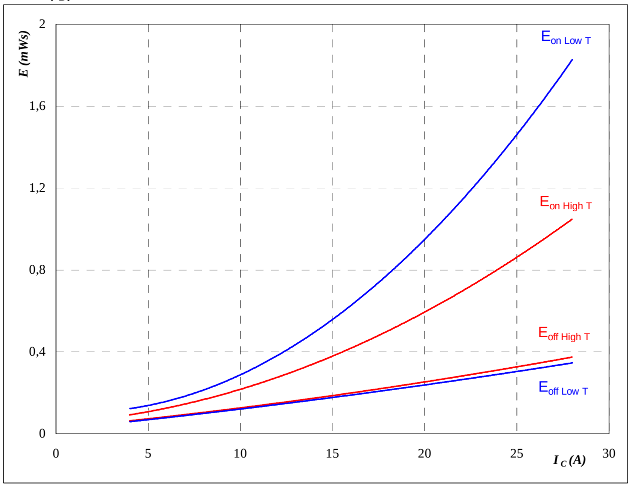
t_p =	250	µs

T1, T3, T5 / D1, D3, D5

Figure 5 T1, T3, T5 MOSFET

Typical switching energy losses
as a function of collector current

E = f(I_D)



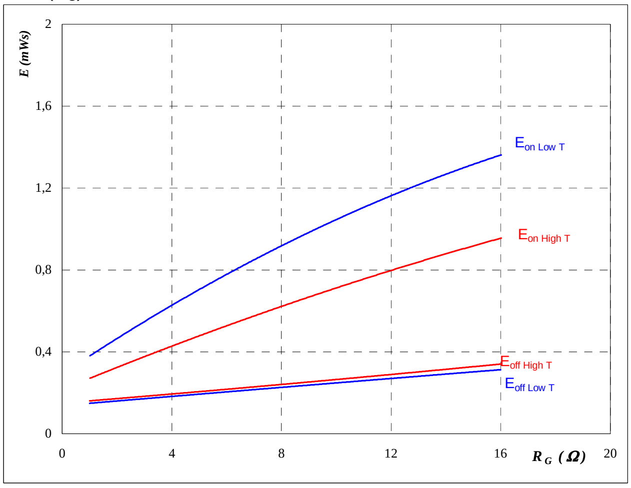
With an inductive load at

T_j =	25/125	°C
V_{DS} =	700	V
V_{GS} =	16	V
R_{gon} =	4	Ω
R_{goff} =	4	Ω

Figure 6 T1, T3, T5 MOSFET

Typical switching energy losses
as a function of gate resistor

E = f(R_G)



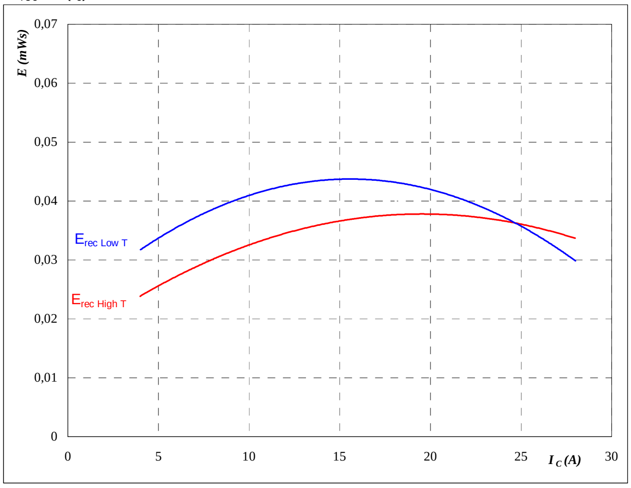
With an inductive load at

T_j =	25/125	°C
V_{DS} =	700	V
V_{GS} =	16	V
I_D =	16	A

Figure 7 D1, D3, D5 FWD

Typical reverse recovery energy loss
as a function of collector (drain) current

E_{rec} = f(I_c)



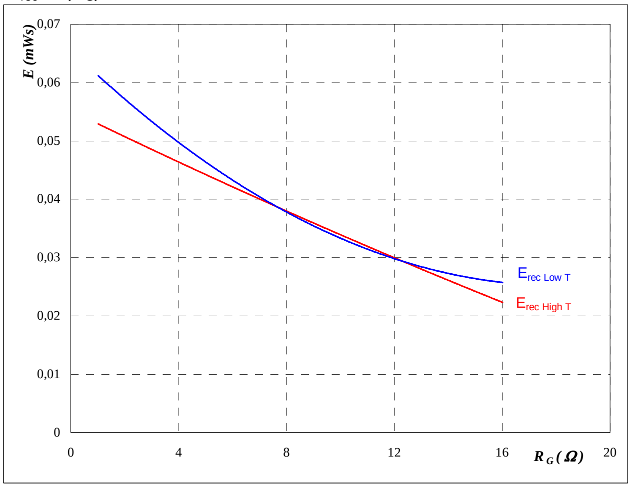
With an inductive load at

T_j =	25/125	°C
V_{DS} =	700	V
V_{GS} =	16	V
R_{gon} =	4	Ω
R_{goff} =	4	Ω

Figure 8 D1, D3, D5 FWD

Typical reverse recovery energy loss
as a function of gate resistor

E_{rec} = f(R_G)



With an inductive load at

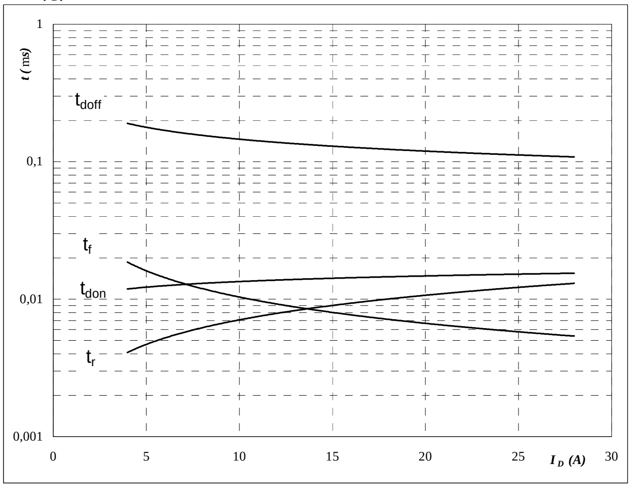
T_j =	25/125	°C
V_{DS} =	700	V
V_{GS} =	16	V
I_D =	16	A

T1, T3, T5 / D1, D3, D5

Figure 9 T1, T3, T5 MOSFET

Typical switching times as a function of collector current

$t = f(I_D)$



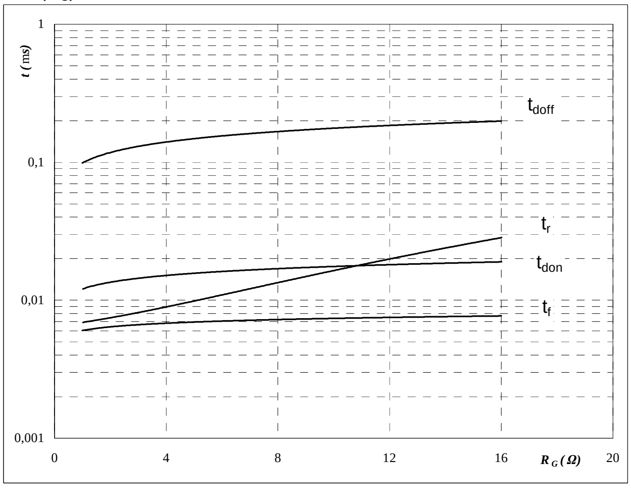
With an inductive load at

T_j =	125	°C
V_{DS} =	700	V
V_{GS} =	16	V
R_{gon} =	4	Ω
R_{goff} =	4	Ω

Figure 10 T1, T3, T5 MOSFET

Typical switching times as a function of gate resistor

$t = f(R_G)$



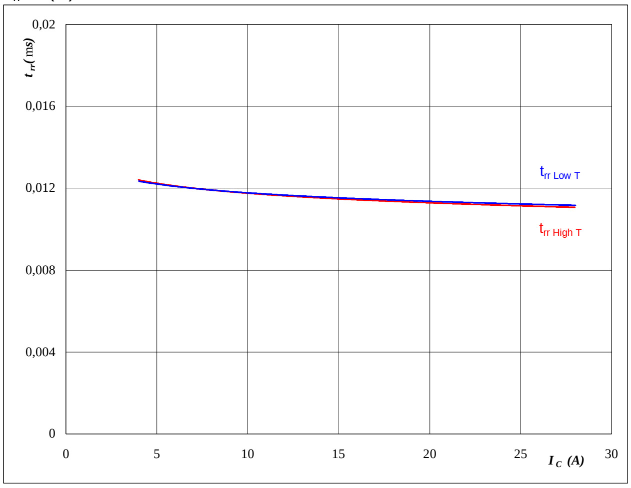
With an inductive load at

T_j =	125	°C
V_{DS} =	700	V
V_{GS} =	16	V
I_C =	16	A

Figure 11 D1, D3, D5 FWD

Typical reverse recovery time as a function of collector current

$t_{rr} = f(Ic)$



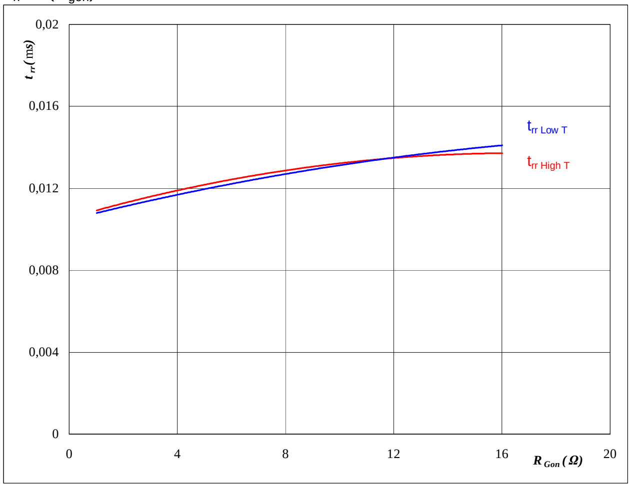
At

T_j =	25/125	°C
V_{CE} =	700	V
V_{GE} =	16	V
R_{gon} =	4	Ω

Figure 12 D1, D3, D5 FWD

Typical reverse recovery time as a function of IGBT turn on gate resistor

$t_{rr} = f(R_{gon})$



At

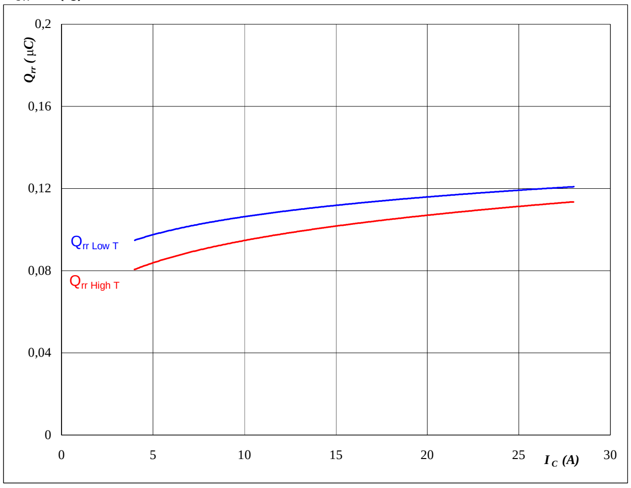
T_j =	25/125	°C
V_R =	700	V
I_F =	16	A
V_{GS} =	16	V

T1, T3, T5 / D1, D3, D5

Figure 13 D1, D3, D5 FWD

Typical reverse recovery charge as a function of collector current

$Q_{rr} = f(I_C)$



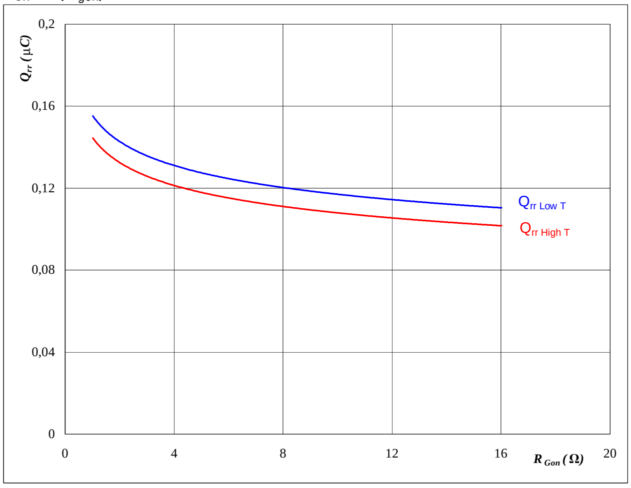
At

T_j =	25/125	°C
V_{CE} =	700	V
V_{GE} =	16	V
R_{gon} =	4	Ω

Figure 14 D1, D3, D5 FWD

Typical reverse recovery charge as a function of IGBT turn on gate resistor

$Q_{rr} = f(R_{gon})$



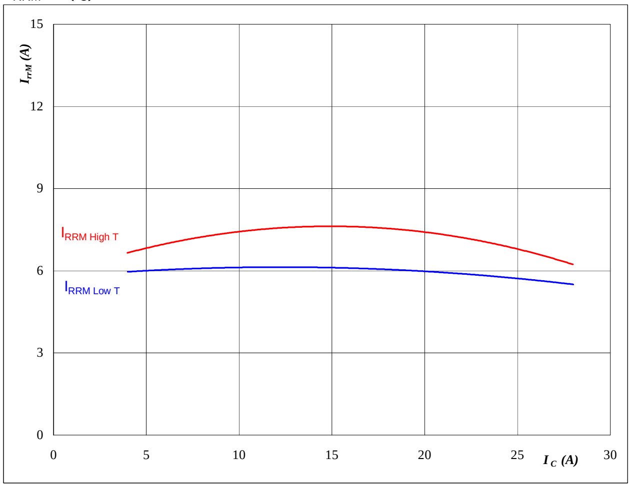
At

Tj =	25/125	°C
V_R =	700	V
I_F =	16	A
V_{GS} =	16	V

Figure 15 D1, D3, D5 FWD

Typical reverse recovery current as a function of collector current

$I_{RRM} = f(I_C)$



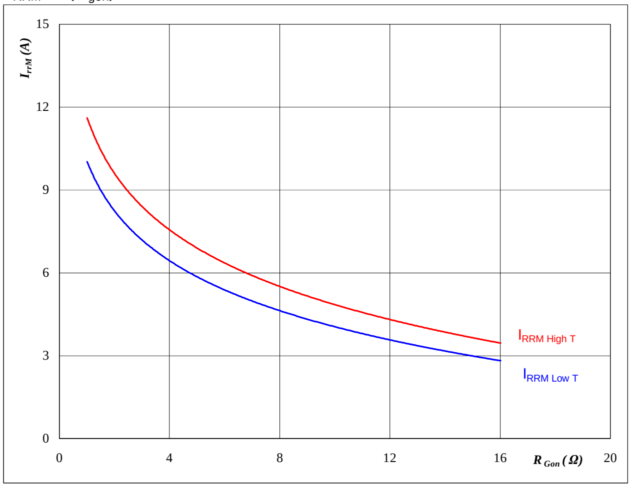
At

T_j =	25/125	°C
V_{CE} =	700	V
V_{GE} =	16	V
R_{gon} =	4	Ω

Figure 16 D1, D3, D5 FWD

Typical reverse recovery current as a function of IGBT turn on gate resistor

$I_{RRM} = f(R_{gon})$



At

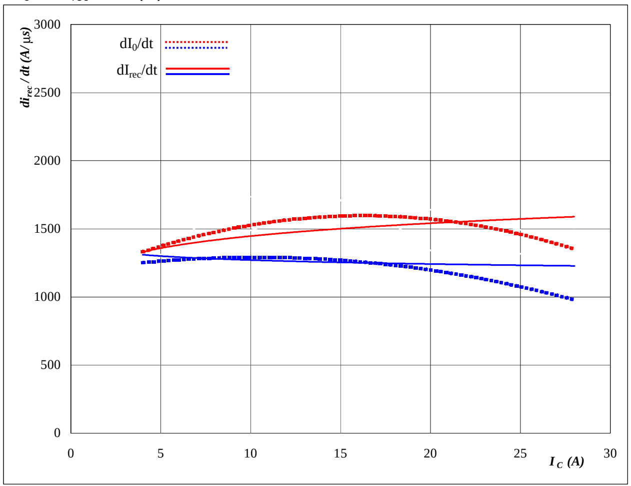
T_j =	25/125	°C
V_R =	700	V
I_F =	16	A
V_{GS} =	16	V

T1, T3, T5 / D1, D3, D5

Figure 17 D1, D3, D5 FWD

Typical rate of fall of forward and reverse recovery current as a function of collector current

$dI_0/dt, dI_{rec}/dt = f(I_C)$



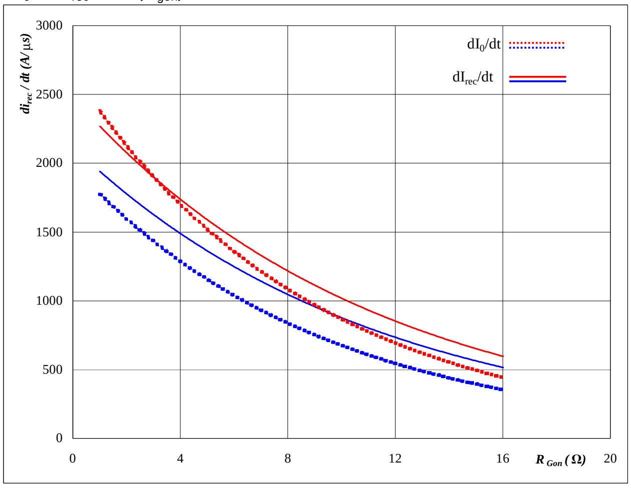
At

T_j =	25/125	°C
V_{CE} =	700	V
V_{GE} =	16	V
R_{gon} =	4	Ω

Figure 18 D1, D3, D5 FWD

Typical rate of fall of forward and reverse recovery current as a function of IGBT turn on gate resistor

$dI_0/dt, dI_{rec}/dt = f(R_{gon})$



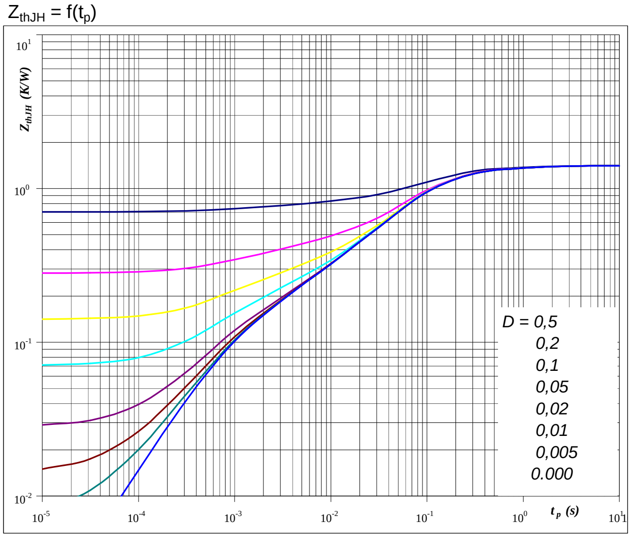
At

Tj =	25/125	°C
VR =	700	V
IF =	16	A
VGS =	16	V

Figure 19 T1, T3, T5 MOSFET

IGBT/MOSFET transient thermal impedance as a function of pulse width

$Z_{thJH} = f(t_p)$



At

D =	t_p / T	
R_{thJH} =	1,41	K/W

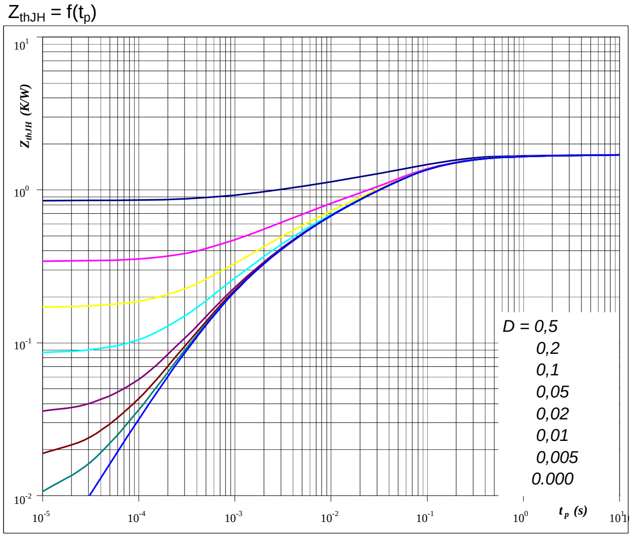
IGBT thermal model values

R (C/W)	Tau (s)
1,24E-01	1,00E+00
3,91E-01	1,66E-01
6,76E-01	6,11E-02
1,21E-01	5,50E-03
9,55E-02	8,02E-04

Figure 20 D1, D3, D5 FWD

FWD transient thermal impedance as a function of pulse width

$Z_{thJH} = f(t_p)$



At

D =	t_p / T	
R_{thJH} =	1,70	K/W

FWD thermal model values

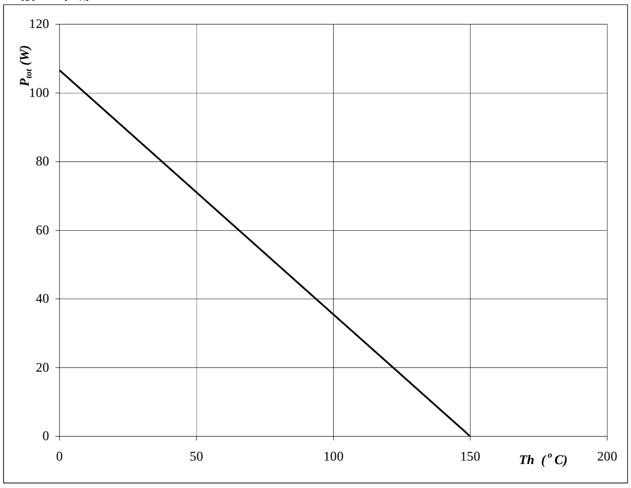
R (C/W)	Tau (s)
4,56E-02	3,21E+00
1,65E-01	3,88E-01
7,86E-01	6,52E-02
3,27E-01	1,11E-02
2,54E-01	2,71E-03
1,20E-01	6,15E-04

T1, T3, T5 / D1, D3, D5

Figure 21 T1, T3, T5 MOSFET

Power dissipation as a function of heatsink temperature

$P_{tot} = f(T_h)$



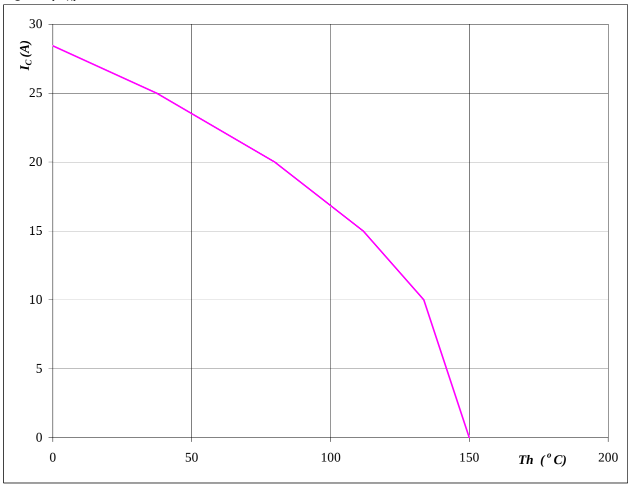
At

T_j = 150 ºC

Figure 22 T1, T3, T5 MOSFET

Collector/Drain current as a function of heatsink temperature

$I_C = f(T_h)$



At

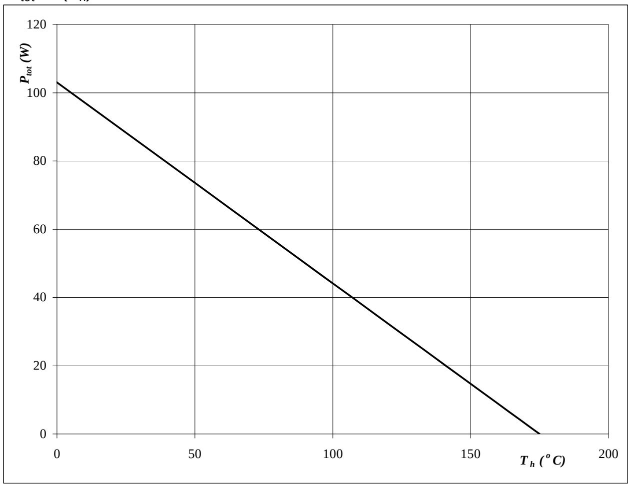
T_j = 150 ºC

V_{GS} = 20 V

Figure 23 D1, D3, D5 FWD

Power dissipation as a function of heatsink temperature

$P_{tot} = f(T_h)$



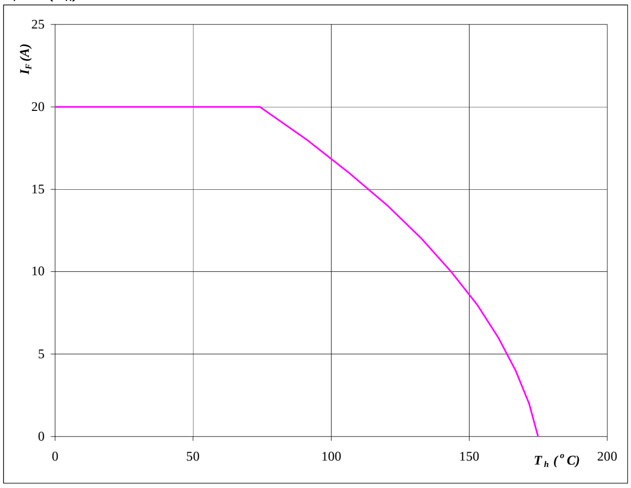
At

T_j = 175 ºC

Figure 24 D1, D3, D5 FWD

Forward current as a function of heatsink temperature

$I_F = f(T_h)$



At

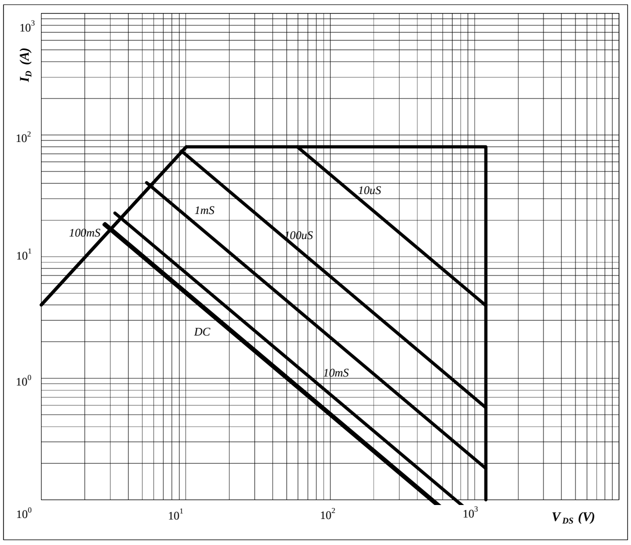
T_j = 175 ºC

T1, T3, T5 / D1, D3, D5

Figure 25 T1, T3, T5 MOSFET

Safe operating area as a function of drain-source voltage

$I_D = f(V_{DS})$



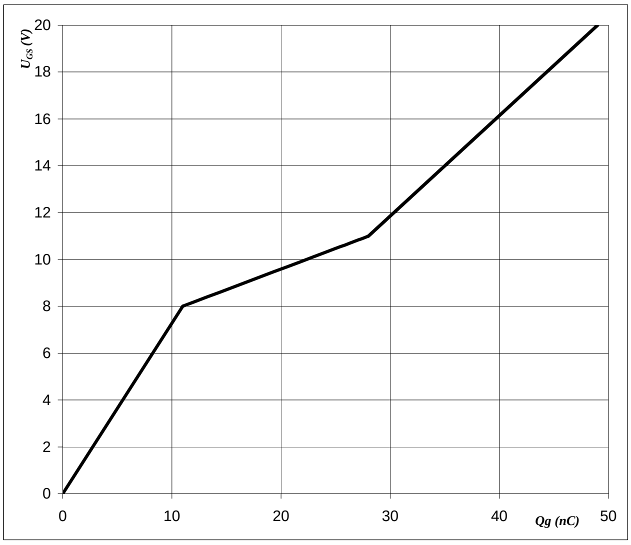
At

D =	single pulse	
T_h =	80	ºC
V_{GS} =	16	V
T_j =	T_{jmax}	ºC

Figure 26 T1, T3, T5 MOSFET

Gate voltage vs Gate charge

$V_{GS} = f(Qg)$



At

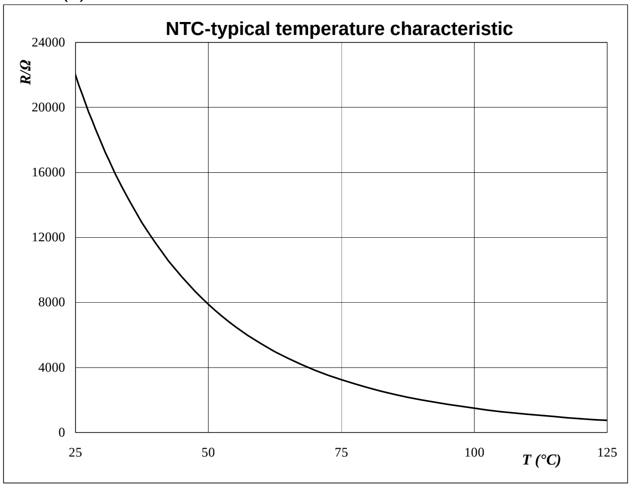
I_{DS} =	20	A
V_{DS}=	800	V
I_{GS}=	10	mA
T_j =	25	ºC

Thermistor

Figure 1 Thermistor

Typical NTC characteristic
as a function of temperature

$R_T = f(T)$



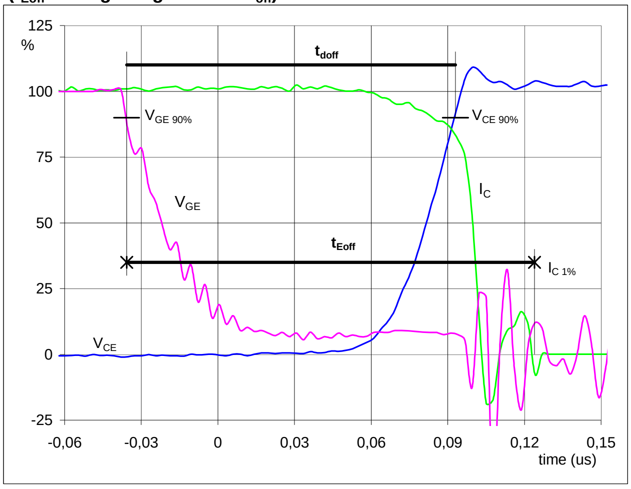
Switching Definitions BOOST

General conditions

T_j	=	125 °C
R_{gon}	=	4 Ω
R_{goff}	=	4 Ω

Figure 1 T1, T3, T5 MOSFET

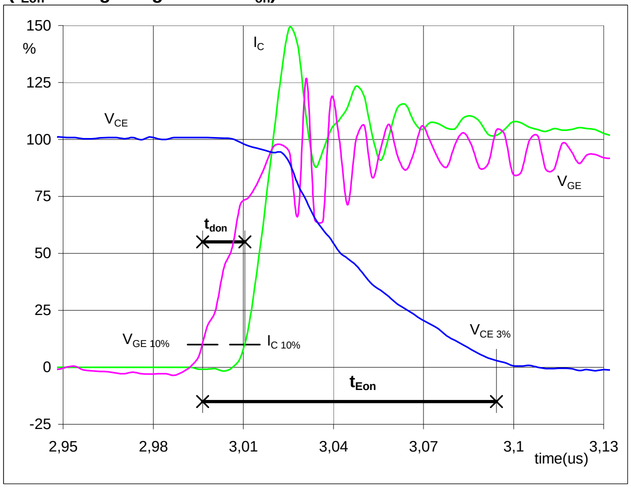
Turn-off Switching Waveforms & definition of t_{doff}, t_{Eoff}
(t_{Eoff} = integrating time for E_{off})



V_{GE} (0%) =	0	V
V_{GE} (100%) =	16	V
V_C (100%) =	700	V
I_C (100%) =	16	A
t_{doff} =	0,13	μs
t_{Eoff} =	0,16	μs

Figure 2 T1, T3, T5 MOSFET

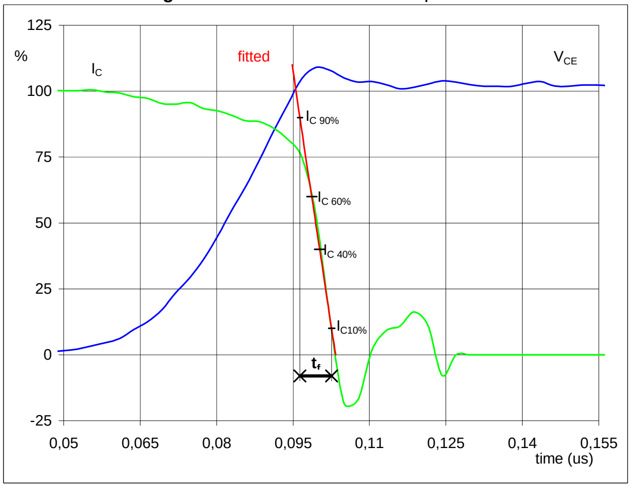
Turn-on Switching Waveforms & definition of t_{don}, t_{Eon}
(t_{Eon} = integrating time for E_{on})



V_{GE} (0%) =	0	V
V_{GE} (100%) =	16	V
V_C (100%) =	700	V
I_C (100%) =	16	A
t_{don} =	0,01	μs
t_{Eon} =	0,10	μs

Figure 3 T1, T3, T5 MOSFET

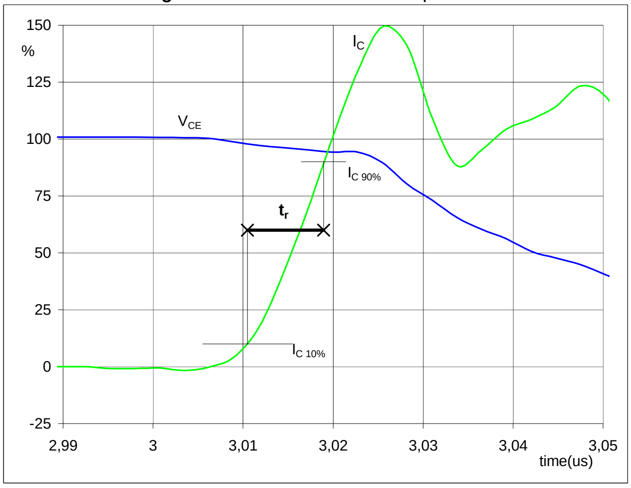
Turn-off Switching Waveforms & definition of t_f



V_C (100%) =	700	V
I_C (100%) =	16	A
t_f =	0,01	μs

Figure 4 T1, T3, T5 MOSFET

Turn-on Switching Waveforms & definition of t_r

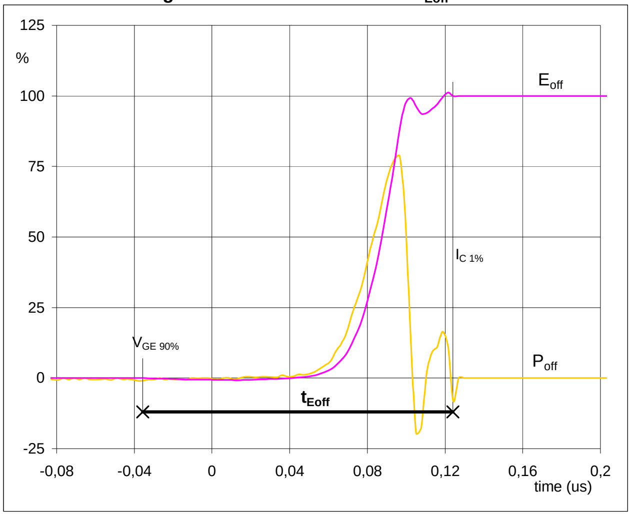


V_C (100%) =	700	V
I_C (100%) =	16	A
t_r =	0,01	μs

Switching Definitions BOOST

Figure 5 T1, T3, T5 MOSFET

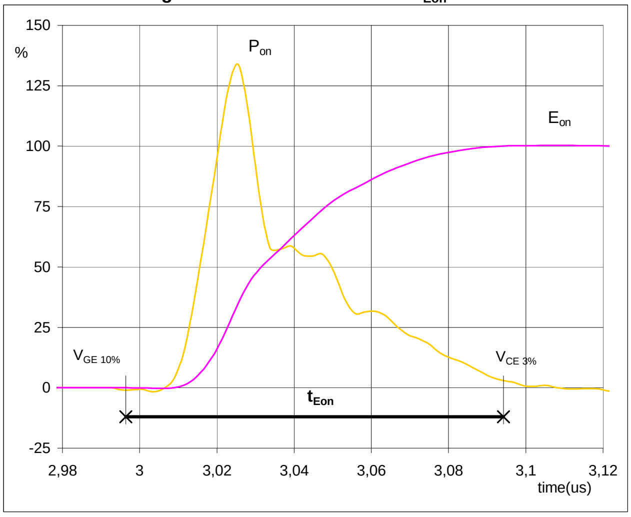
Turn-off Switching Waveforms & definition of t_{Eoff}



P_{off} (100%) =	11,25	kW
E_{off} (100%) =	0,19	mJ
t_{Eoff} =	0,16	μs

Figure 6 T1, T3, T5 MOSFET

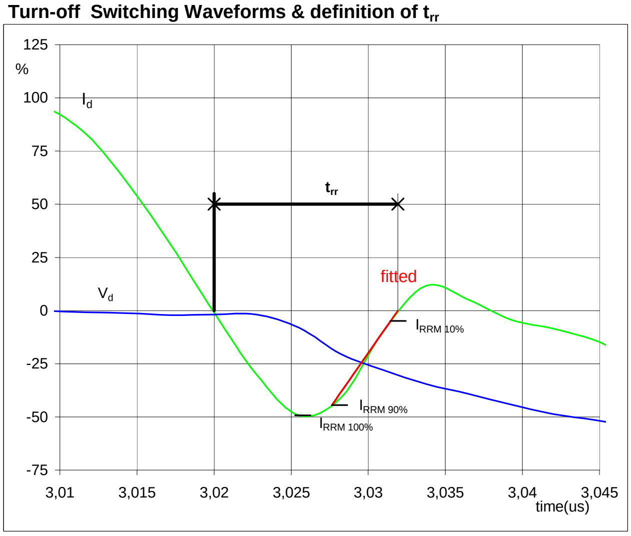
Turn-on Switching Waveforms & definition of t_{Eon}



P_{on} (100%) =	11,25	kW
E_{on} (100%) =	0,43	mJ
t_{Eon} =	0,10	μs

Figure 7 D1, D3, D5 FWD

Turn-off Switching Waveforms & definition of t_{rr}

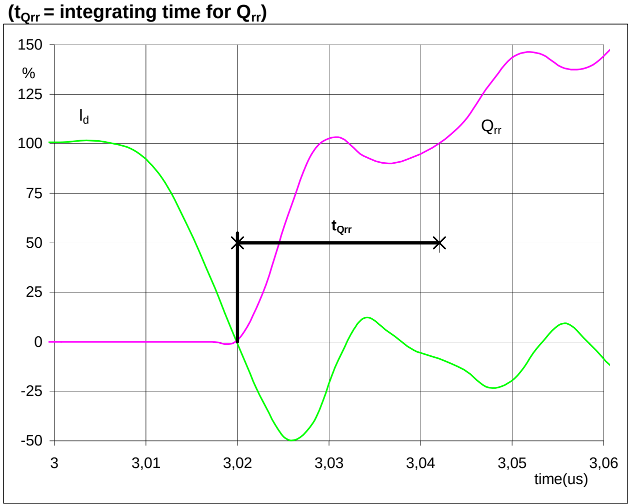


V_d (100%) =	700	V
I_d (100%) =	16	A
I_{RRM} (100%) =	-8	A
t_{rr} =	0,01	μs

Switching Definitions BOOST

Figure 8 D1, D3, D5 FWD

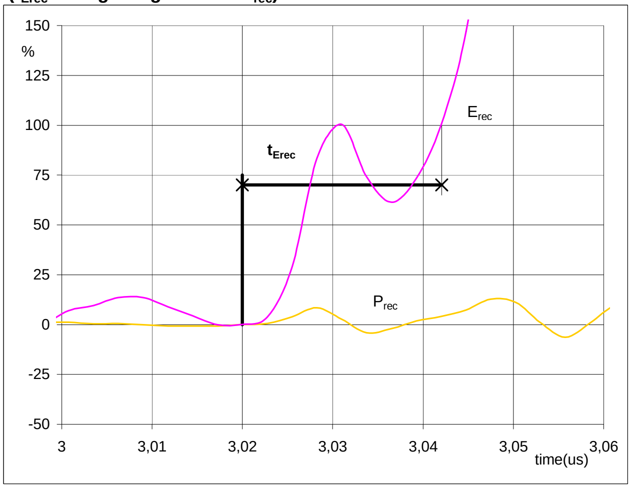
Turn-on Switching Waveforms & definition of t_{Qrr}
(t_{Qrr} = integrating time for Q_{rr})



I_d (100%) =	16	A
Q_{rr} (100%) =	0,12	μC
t_{Qrr} =	0,02	μs

Figure 10 D1, D3, D5 FWD

Turn-on Switching Waveforms & definition of t_{Erec}
(t_{Erec}= integrating time for E_{rec})

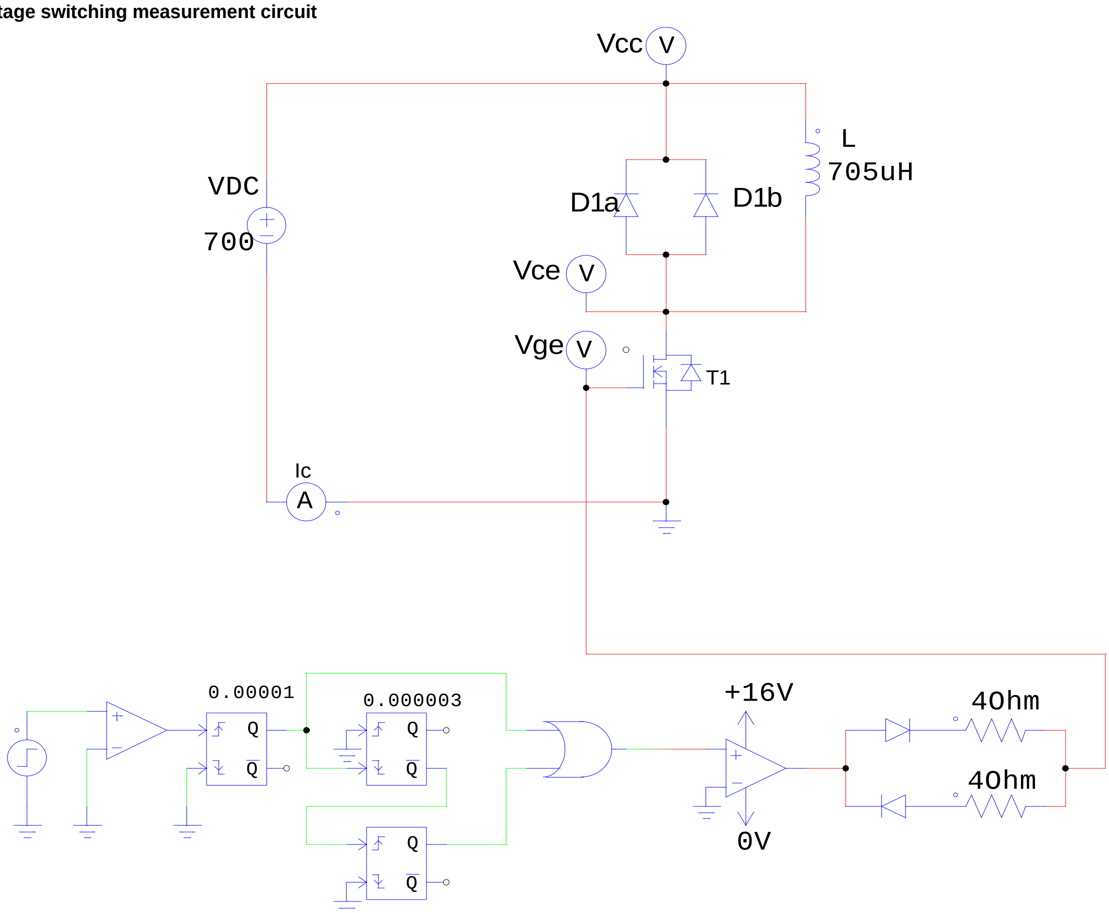


P_{rec} (100%) =	11,25	kW
E_{rec} (100%) =	0,04	mJ
t_{Erec} =	0,02	μs

Measurement circuit

Figure 11

BOOST stage switching measurement circuit



Ordering Code and Marking - Outline - Pinout

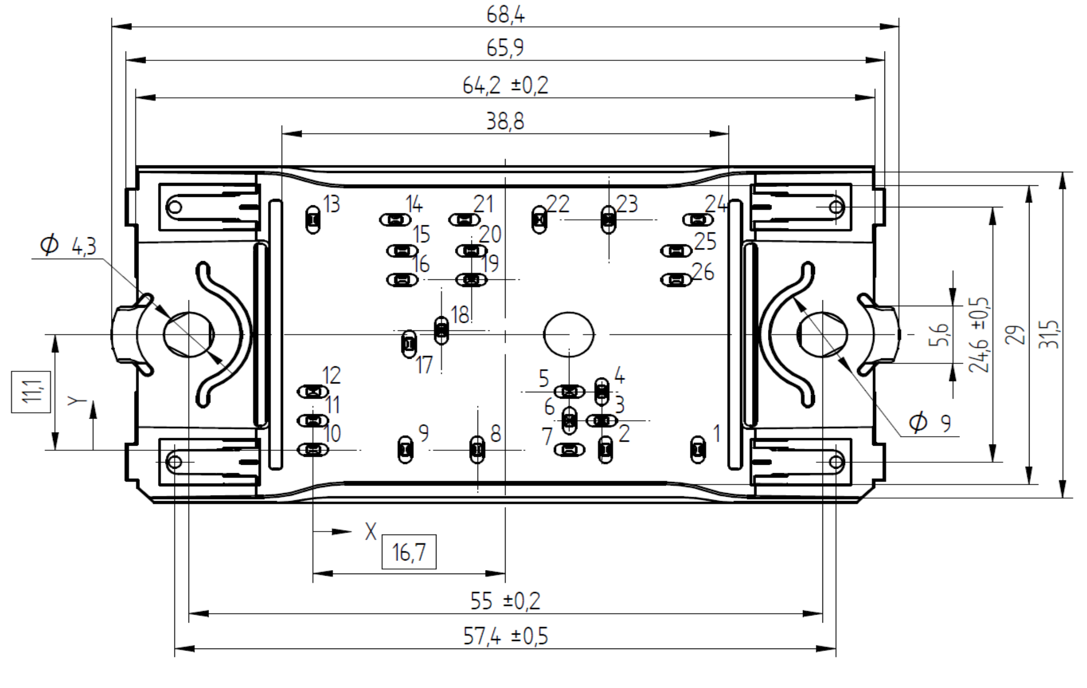
Ordering Code & Marking

Version	Ordering Code	in DataMatrix as	in packaging barcode as
w/o thermal paste 12mm housing Press-fit pin	10-PZ123BA080MR-M909L28Y	M909L28Y	M909L28Y

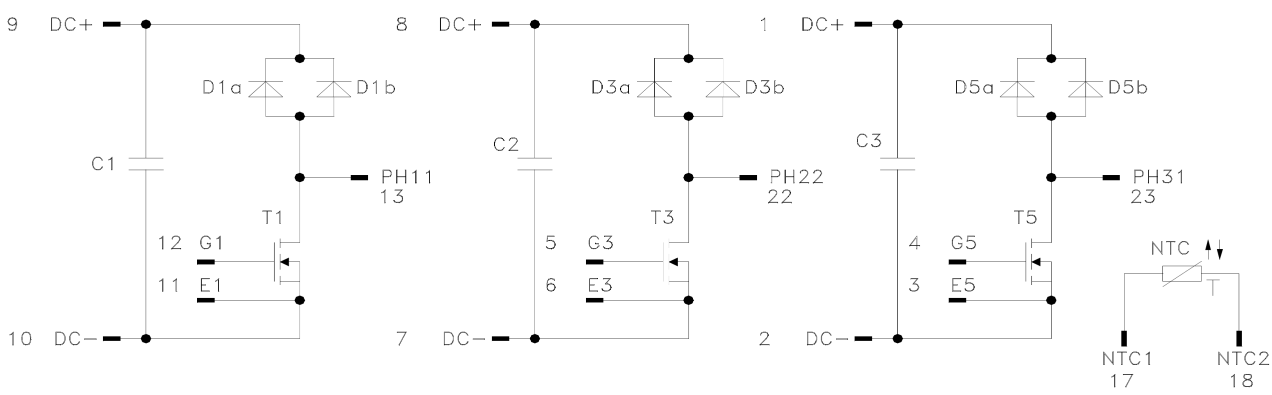
Outline

Pin table

Pin	X	Y
1	33,4	0
2	25,4	0
3	25,05	2,8
4	25,05	5,6
5	22,25	5,6
6	22,25	2,8
7	22,25	0
8	14,25	0
9	8	0
10	0	0
11	0	2,8
12	0	5,6
13	0	22,2
14	7,15	22,2
15	7,75	19,2
16	7,75	16,4
17	8,35	10,2
18	11,15	11,5
19	13,75	16,4
20	13,75	19,2
21	13,15	22,2
22	19,65	22,2
23	25,65	22,2
24	33,4	22,2
25	31,55	19,2
26	31,55	16,4



Pinout



Pin 15, 16, 19, 20, 25, 26 not connented

DISCLAIMER

The information given in this datasheet describes the type of component and does not represent assured characteristics. For tested values please contact Vincotech.Vincotech reserves the right to make changes without further notice to any products herein to improve reliability, function or design. Vincotech does not assume any liability arising out of the application or use of any product or circuit described herein; neither does it convey any license under its patent rights, nor the rights of others.

LIFE SUPPORT POLICY

Vincotech products are not authorised for use as critical components in life support devices or systems without the express written approval of Vincotech.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in labelling can be reasonably expected to result in significant injury to the user.

2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.