#### DATA SHEET



# NEM090603M-28

# N-CHANNEL SILICON POWER LDMOS FET FOR 75 W UHF-BAND SINGLE-END POWER AMPLIFIER

#### **DESCRIPTION**

The NEM090603M-28 is an N-channel enhancement-mode lateral diffused MOS FET designed for driver or final stage in 0.8 to 1.0 GHz PA, such as, analog/digital TV-transmitter and GSM/EDGE/D-AMPS/PDC cellular base station amplifiers. Dies are manufactured using our NEWMOS technology (our WSi gate lateral MOS FET), and its nitride surface passivation and triple layer aluminum silicon metallization offer a high degree of reliability.

#### **FEATURES**

High 1 dB compression output power : Po (1 dB) = 75 W TYP. (VDS = 28 V, IDset = 550 mA, f = 960 MHz)
 High linear gain : GL = 17.5 dB TYP. (VDS = 28 V, IDset = 550 mA, f = 960 MHz)
 High drain efficiency : pd = 54% TYP. (VDS = 28 V, IDset = 550 mA, f = 960 MHz)

Low intermodulation distortion
 IM<sub>3</sub> = -31 dBc TYP. (V<sub>DS</sub> = 28 V, I<sub>Dset</sub> = 550 mA, f = 960/960.1 MHz,

 $P_{out} = 45 \text{ dBm } (2 \text{ tones})$ )

- · Excellent thermal stability
- · Low cost hollow plastic packages
- · Integrated ESD protection
- Excellent stability against HCI (Hot Carrier Injection)

#### **APPLICATIONS**

- Digital cellular base station PA: GSM/EDGE/D-AMPS/PDC/N-CDMA etc.
- UHF-band TV-transmitter PA

#### **ORDERING INFORMATION**

Part Number	Order Number	Package	Supplying Form
NEM090603M-28	NEM090603M-28-A	3M (T-91M) (Pb-Free)	ESD protective tray

Remark To order evaluation samples, contact your nearby sales office.

The unit sample quantity is 1 pcs.

Caution Observe precautions when handling because these devices are sensitive to electrostatic discharge.

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# ABSOLUTE MAXIMUM RATINGS (Ta = +25°C, unless otherwise specified)

Parameter	Symbol	Ratings	Unit
Drain to Source Voltage	VDS	65	V
Gate to Source Voltage	V <sub>GS</sub>	±7	V
Drain Current	lο	6	Α
Total Device Dissipation	PD Note 1	146	W
Input Power	Pin Note 2	3.0	W
Channel Temperature	Tch	200	°C
Storage Temperature	T <sub>stg</sub>	−65 to +150	°C

Notes 1.  $Tc = 25^{\circ}C$ 

**2.**  $f = 960 \text{ MHz}, V_{DS} = 28 \text{ V}$ 

#### RECOMMENDED OPERATING RANGE

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Drain to Source Voltage	VDS		28	30	V
Gate to Source Voltage	V <sub>GS</sub>	2.6	3.0	3.6	V
Average Input Power <sup>Note</sup>	Pin (ave.)	-	28	32	dBm

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Note f = 960 MHz

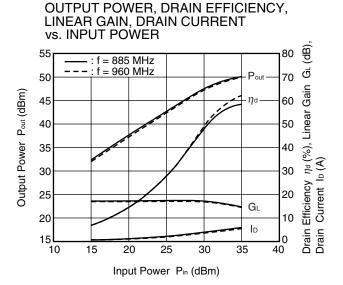
# **ELECTRICAL CHARACTERISTICS (TA = +25°C)**

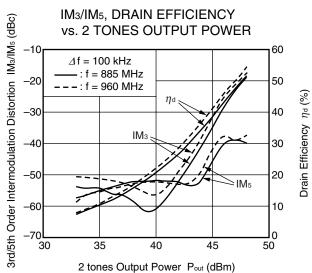
Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Gate to Source Leak Current	Igss	V <sub>GSS</sub> = 5 V		-	1	μΑ
Saturated Drain Current	IDSS	V <sub>DSS</sub> = 65 V	-	-	1	mA
Gate Threshold Voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	1.7	2.2	2.8	V
Transconductance	gm	V <sub>DS</sub> = 28 V, I <sub>Dset</sub> = 550 mA	-	2.7	-	S
Drain to Source Breakdown Voltage	BV <sub>DSS</sub>	loss = 10 $\mu$ A	65	75	-	٧
Thermal Resistance	Rth (ch-c)	Channel to Case	-	1.0	1.2	°C/W
Gain 1 dB Compression Output Power	Po (1 dB)	f = 960 MHz, V <sub>DS</sub> = 28 V,	-	48.8	-	dBm
Linear Gain	GL Note 1	I <sub>Dset</sub> = 550 mA	16	17.5	-	dB
Output Power	Pout Note 2		47.5	48.3	-	dBm
Drain Efficiency	$\eta_{ extsf{d}}$		48	54	-	%
Power Added Efficiency	$\eta_{ m add}$			53		%
3rd Order Intermodulation Distortion	IMз	f = 960/960.1 MHz, V <sub>DS</sub> = 28 V, I <sub>Dset</sub> = 550 mA, 2 tones P <sub>out</sub> = 45 dBm	_	-31	_	dBc

Notes 1. Pin = 21 dBm

**2.** Pin = 32 dBm

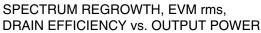
# TYPICAL CHARACTERISTICS (TA = +25°C, VDS = 28 V, IDset = 550 mA, unless otherwise specified)

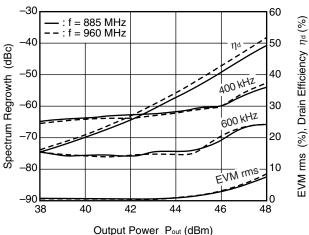




**Remark** The graphs indicate nominal characteristics.

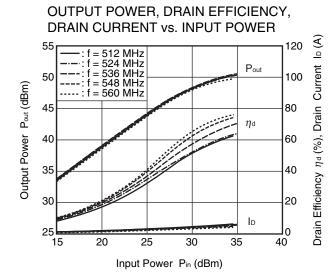
# TYPICAL CHARACTERISTICS (TA = +25°C, VDS = 28 V, IDset = 550 mA, EDGE Modulation Spectrum Performance, unless otherwise specified)





Remark The graph indicates nominal characteristics.

#### <R> TYPICAL CHARACTERISTICS (TA = +25°C, unless otherwise specified)



IM3/IM5, DRAIN EFFICIENCY IM3/IM5 (dBc) vs. 2 TONES OUTPUT POWER 60 ⊿f = 6 MHz loq = 500 mA 50 : f = 518 MHz -: f = 536 MHz 3rd/5th Order Intermodulation Distortion lφ (%) 40 -20 Drain Efficiency -30-40 ΙМз -50 10 -60 0 27 30 24 48 2 tones Output Power Pout (dBm)

Remark The graphs indicate nominal characteristics.

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#### <R> S-PARAMETERS

S-parameters/Noise parameters are provided on our web site in a form (S2P) that enables direct import to a microwave circuit simulator without keyboard input.

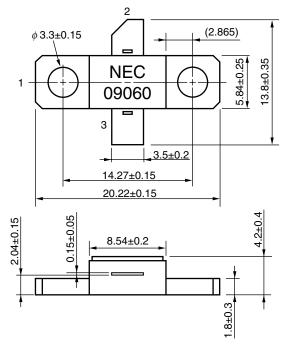
Click here to download S-parameters.

 $[\mathsf{RF} \ \mathsf{and} \ \mathsf{Microwave}] \to [\mathsf{Device} \ \mathsf{Parameters}]$ 

URL http://www.ncsd.necel.com/microwave/index.html

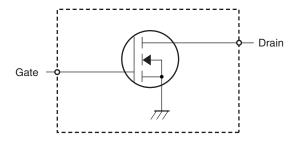
# <R> PACKAGE DIMENSIONS

3M (T-91M) (UNIT: mm)



### **PIN CONNECTIONS**

- 1. Source
- 2. Drain
- 3. Gate



Remark (): Reference value



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#### RECOMMENDED MOUNTING CONDITIONS FOR CORRECT USE

- (1) Fix to a heatsink or mount surface completely with screws at the two holes of the flange.
- (2) The recommended torque strength of the screws is 53 N·cm typical using M3 type screws.
- (3) The recommended flatness of the mount surface is less than  $\pm 10~\mu m$  (roughness of surface is  $\nabla \nabla \nabla$ ).

#### RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions	Condition Symbol	
Partial Heating	Peak temperature (terminal temperature) Soldering time (per terminal of device) Maximum chlorine content of rosin flux (% mass)	: 350°C or below : 3 seconds or less : 0.2%(Wt.) or below	HS350-P3

Caution Do not use different soldering methods together (except for partial heating).

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M8E 02.11-1

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