

3.3 V, HCMOS/ TTL, J LEADED, PLASTIC MOLDED SURFACE MOUNT CRYSTAL CLOCK OSCILLATOR

ASML SERIES

Pb in high temperature solder
(exempt per RoHS 2002/95/EC Annex (7)).

RoHS
Compliant



14.0 x 8.95 x 4.7 mm

FEATURES:

- Industry standard J-Leaded terminals.
- Plastic molded SMD, Reflow capable.
- Low current consumption.
- Tristate Enable/Disable.
- HCMOS output.
- 3.3Vdc operation.
- Extended temperature -40°C to +85°C option.

APPLICATIONS:

- Provide clock signals for microprocessors and digital circuits.
- Communication equipment, AV and OA equipment

STANDARD SPECIFICATIONS:

PARAMETERS

ABRACON P/N	ASML Series
Frequency range:	1 ~ 106.25 MHz
Operating temperature:	0° C to + 70° C (see options)
Storage temperature:	- 55° C to + 125° C
Overall frequency stability:	± 100 ppm max. (see options)
Supply voltage (Vdd):	3.3Vdc ± 10%
Input current:	15 mA max. for F ≤ 20 MHz 35 mA max. for F ≤ 70 MHz 60 mA max. for F ≤ 106.25 MHz
Symmetry:	40/60 % max. at 1/2Vdd CMOS (see option)
Rise and fall time (Tr/Tf):	10 ns max.
Output load:	10 TTL or 15 pFmax.
Output voltage:	VOH = 0.9*Vdd min. VOL = 0.1*Vdd max.
Tristate function:	"1" (VIH ≥ 2.2 V) or open: Oscillation "0" (VIL < 0.8 V) : Hi Z
Stat-up time:	10ms max.
Aging (First year):	±5ppm/year max.
Period jitter (1 sigma):	25ps max.

OPTIONS AND PART IDENTIFICATION

(Left blank if standard)

ASML - Frequency - □ - □ - □ - □

Operating Temp.	
D	-10°C to +60°C
E	-20°C to +70°C
F	-30°C to +70°C
N	-30°C to +85°C
L	-40°C to +85°C

Overall Frequency	
R	±25ppm
K	±30ppm
C	±50ppm

Packaging	
Blank	Bulk
T	Tape and Reel (1000pcs/reel)

Symmetry	
S	45/55% @ 1/2Vdd
S1	45/55% @ 1.4Vdd



