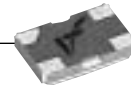


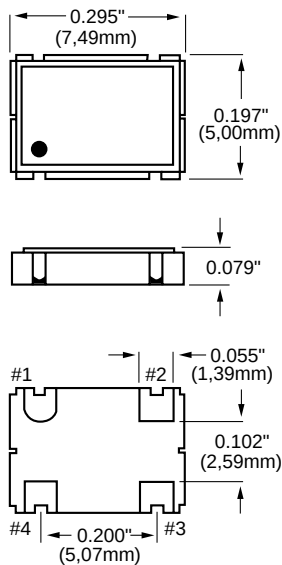
VF1



VF1 Series Miniature Ceramic SMD Tristate Oscillator HCMOS–TTL Compatible

FEATURES

- Miniature Ceramic Package
- Wide Frequency Range
- Industrial Temperature Available



All dimensions are typical unless otherwise specified.

Creating a Part Number

VF1 - - FREQ.

FREQUENCY STABILITY	
Code	Specification
S	±20 ppm
A	±25 ppm
B	±50 ppm
	±100 ppm (std.)

OPERATIONAL TEMP. RANGE	
Code	Specification
	-10°C to +70°C (std.)
1	-40°C to +85°C

STAND BY	
Code	Specification
	Std.
E	Stand by

Example: VF1SH-1-50.0MHz: Frequency Stability ±20ppm, Symmetry ±5%, Operating Temperature -40°C to +85°C, Frequency 50MHz.

Parameter	Symb	Condition	Min	Typ	Max	Unit	Note
Input Break Down Voltage	V _{cc}		-0.5		7.0	V	
Storage Temp.	T _s		-55		+125	°C	
Frequency Range	F		1.8		170	MHz	
Frequency Stability	ΔF/F	Overall	-100		100	ppm	1
Input Voltage	V _{cc}		4.75	5.00	5.25	V	
Input Current	I _{cc}	15pF load			20 35	mA	to 25MHz to 125MHz
Load	10–TTL gates or 15pF typical						
Duty Cycle		@50%V _{cc}	45	50	55	%	
Rise /Fall Time	Tr /Tf	10% to 90%		5	10	ns	
Logic "1" Level	V _{oh}	Loaded, overall	.9V _{cc}			V	
Logic "0" Level	V _{ol}	Loaded, overall			.1V _{cc}	V	
Enable Input Disable Input			2.2V (min) @0.4mA 0.8V (max) @0.4mA				
Start-up Time	T _s			3	10	ms	
Enable/ Disable Time					100	ns	
Operating Temperature Range	-10°C to +70°C (-40°C to +85°C available)						
Mechanical Shock	Per MIL-STD-202, Method 213, Cond. E						
Thermal Shock	Per MIL-STD-883, Method 1011, Cond. A						
Vibration	Per MIL-STD-883, Method 2007, Cond. A						
Soldering Conditions	260°C, for 10s, Max; 230°C, for 90s, Max.						
Hermetic Seal	Leak rate less than 5 x 10 ⁻⁸ atm.cc/s of helium						
Pin Out	Pin #1–Tristate Control Pin #2–Case, GND Pin #3–Output Pin #4–V _{cc}						

Notes:

1. ±50ppm, ±25ppm and ±20ppm stability available up to 50MHz for -10°C to 70°C operating temperature range.
2. For Standby* options only

* See Glossary

All specifications are subject to change without notice.