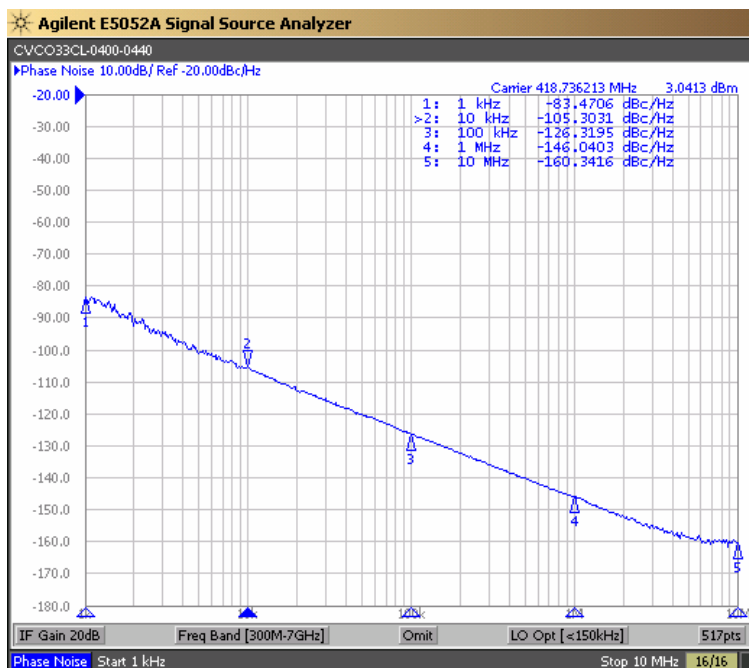


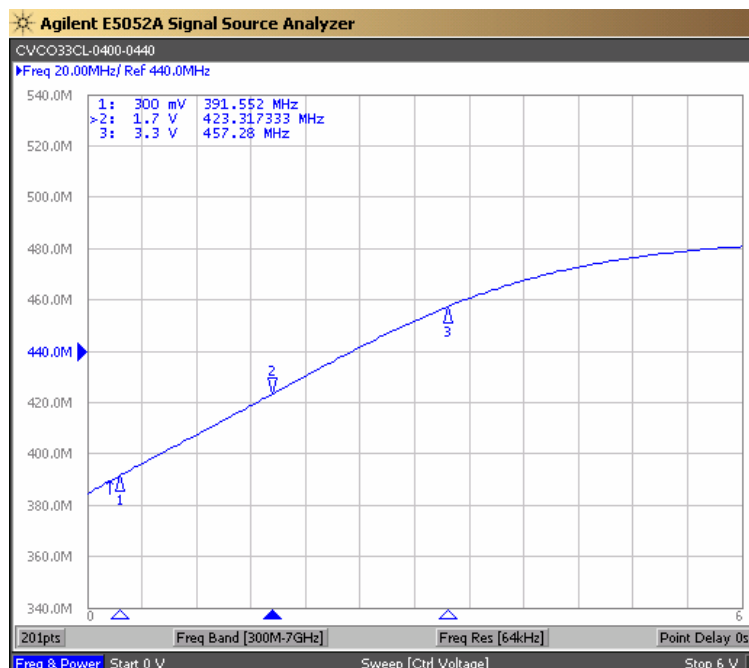


PERFORMANCE SPECIFICATION	MIN	TYP	MAX	UNITS
Lower Frequency:			400	MHz
Upper Frequency:	440			MHz
Tuning Voltage:	0.3		3.3	VDC
Supply Voltage:	3.135	3.3	3.465	VDC
Output Power:	+1.0	+3.0	+5.0	dBm
Supply Current:		16	25	mA
Harmonic Suppression (2 nd Harmonic):		-15	-12	dBc
Pushing:		0.5	1.0	MHz/V
Pulling, all Phases:		0.5	1.0	MHz pk-pk
Tuning Sensitivity:		20		MHz/V
Phase Noise @ 10kHz offset:		-106	-103	dBc/Hz
Phase Noise @ 100kHz offset:		-128	-125	dBc/Hz
Load Impedance:		50		Ω
Input Capacitance:			100	pF
Operating Temperature Range:	-40		+85	°C
Storage Temperature Range:	-45		+90	°C

Phase Noise (1 Hz BW, Typical)

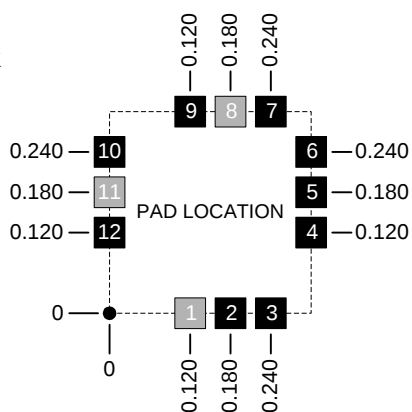
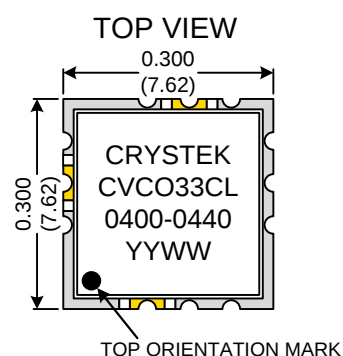
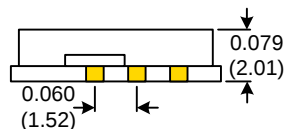
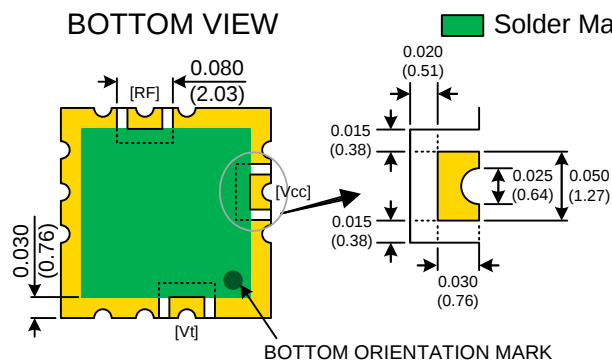


Tuning Curve (Typical)





 Gold Finish
 Solder Mask



Pad	Connection
1	Vt
8	RF-OUTPUT
11	Vcc
Others	GROUND

- Unless otherwise specified,
Dimensions are in: IN
(mm)
- Pad Location
Dimensions are in: Inches

Technical drawing of a rectangular plate with five square holes and a row of 15 circular holes. The drawing includes the following dimensions and features:

- Top Circular Holes:** A row of 15 holes with a diameter of $0.059 \varnothing$ TYP and a center-to-center spacing of $(1.50 \varnothing)$ TYP.
- Bottom Circular Holes:** A row of 15 holes with a diameter of 0.157 ± 0.004 and a center-to-center spacing of (4.00 ± 0.10) .
- Square Holes:** Five square holes, each with a side length of 0.472 (12.00).
- Plate Thickness:** The thickness of the plate is 0.630 ± 0.012 (16.00 ± 0.30).
- Direction of Feed:** An arrow on the right indicates the "DIRECTION OF FEED".

Drawing not to scale

Crystek Part Number:	CVCO33CL-0400-0440	Release Date:	16-Mar-09
Revision Level:	D	Responsible:	C. Vales

Specification is subject to change without notice

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12730 COMMONWEALTH DRIVE • FORT MYERS, FLORIDA 33913
PHONE: 239-561-3311 • 800-237-3061
FAX: 239-561-1025 • WWW.CRYSTEK.COM