# **Evaluation Board 3s SKYPER<sup>®</sup> 32PRO R**

**Technical Explanations** 

Revision 03

Status: evaluation board

This Technical Explanation is valid for the following parts:

part number	type	date code (YYWW)
L6100251	Board 3s SKYPER <sup>®</sup> 32PRO R	≥ 0740

Related documents:

title Technical Explanations SKYPER<sup>®</sup> 32PRO R

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### Content

Disclaimer	2
Application and Handling Instructions	3
Further application support	3
General Description	3
Dimensions	4
Component Placement Layout	5
PIN Array	
Signal IF_CMN_nHALT	7
Setting Dead Time	7
Setting Dynamic Short Circuit Protection	7
Collector Series Resistance	8
Adaptation Gate Resistors	8
Adaptation Decoupling Gate Resistors	8
Setting Soft Turn-Off	9
Temperature Signal	9
Over Temperature Protection Circuit (OTP)	9
Mounting Notes	
Schematics	11
Layouts	14
Parts List	15

### Disclaimer

In accordance with SEMIKRON's quality procedures, we hereby notify you that the Evaluation Board 3s SKYPER<sup>®</sup> 32PRO R should be considered as evaluation products only.

Evaluation products are experimental products and are therefore only intended for device evaluation. SEMIKRON does not represent or guarantee that a final version will be made available after device evaluation. Evaluation products are subject to the change service.

Although evaluation products have been manufactured using processes and procedures representative of final production, they have not been subjected to all of our normal quality audits and controls. Additionally, neither the product nor the manufacturing processes may have passed our internal qualification procedures.

### **IMPORTANT:**

The evaluation boards are supplied without warranty of any kind, expressed, implied of statutory, including but not limited to, any implied warranty of merchantability of fitness for a particular purpose. Credit or replacement for evaluation products that fail to function will not be given nor will a failure analysis be performed. There is no entitlement to technical support for evaluation products.

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The liability exclusions shall not apply in cases of liability independent of the question of blame or negligence, especially claims under the German Product Liability Act in cases of wilful intent, gross negligence, or injury to life, limb or health and on account of a breach of major contractual obligations. Compensation for failure to comply with major contractual obligations shall be limited, however, to typical, foreseeable damages, unless wilful intent or gross negligence applies, or on account of liability for injury to life, limb or health. Any change in the burden of proof to the detriment of the undersigned customer shall not be associated with the aforesaid rulings

### FOR SAFETY REASONS THE CUSTOMER IS NOT ALLOWED TO SELL EVALUATION PRODUCTS TO ANY END CUSTOMER OR ANY OTHER THIRD PARTY.

If the customer fails not to sell the evaluation products to any end customer or any other third party, then the customer shall indemnify SEMIKRON against all claims by the concerned end customer or third party in respect of any loss, damage or injury arising from the aforesaid reason.

#### Please note:

All values in this technical explanation are typical values. Typical values are the average values expected in large quantities and are provided for information purposes only. These values can and do vary in different applications. All operating parameters should be validated by user's technical experts for each application.

### **Application and Handling Instructions**

- Please provide for static discharge protection during handling. As long as the hybrid driver is not completely assembled, the input terminals have to be short-circuited. Persons working with devices have to wear a grounded bracelet. Any synthetic floor coverings must not be statically chargeable. Even during transportation the input terminals have to be short-circuited using, for example, conductive rubber. Worktables have to be grounded. The same safety requirements apply to MOSFET- and IGBT-modules.
- Any parasitic inductances within the DC-link have to be minimised. Over-voltages may be absorbed by C- or RCDsnubber networks between main terminals for PLUS and MINUS of the power module.
- When first operating a newly developed circuit, SEMIKRON recommends to apply low collector voltage and load current in the beginning and to increase these values gradually, observing the turn-off behaviour of the free-wheeling diode and the turn-off voltage spikes generated across the IGBT. An oscillographic control will be necessary. Additionally, the case temperature of the module has to be monitored. When the circuit works correctly under rated operation conditions, short-circuit testing may be done, starting again with low collector voltage.
- It is important to feed any errors back to the control circuit and to switch off the device immediately in failure events.
   Repeated turn-on of the IGBT into a short circuit with a high frequency may destroy the device.
- The inputs of the hybrid driver are sensitive to over-voltage. Voltages higher than V<sub>S</sub> +0,3V or below -0,3V may destroy
  these inputs. Therefore, control signal over-voltages exceeding the above values have to be avoided.
- The connecting leads between hybrid driver and the power module should be as short as possible (max. 20cm), the driver leads should be twisted.

#### Further application support

Latest information is available at <u>http://www.semikron.com</u>. For design support please read the SEMIKRON Application Manual Power Modules available at <u>http://www.semikron.com</u>.

#### **General Description**

The Board 3s SKYPER<sup>®</sup> 32PRO R is an evaluation board for the IGBT module SEMiX<sup>®</sup> 3s (spring contact version). The board can be customized allowing adaptation and optimization to the used SEMiX<sup>®</sup> Module. The switching characteristic of the IGBT can be influenced through user settings, e.g. changing turn-on and turn-off speed by variation of  $R_{Gon}$  and  $R_{Goff}$ . Furthermore, it is possible to adjust the monitoring level and blanking time for the DSCP (see

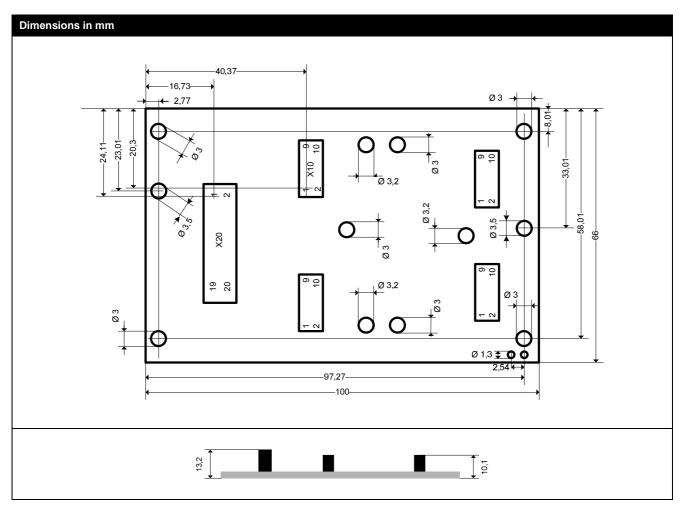
#### Please note:

This technical explanation is based on the Technical Explanations for SKYPER<sup>®</sup> 32PRO R. Please read the Technical Explanations SKYPER<sup>®</sup> 32 PRO R before using the Evaluation Board.

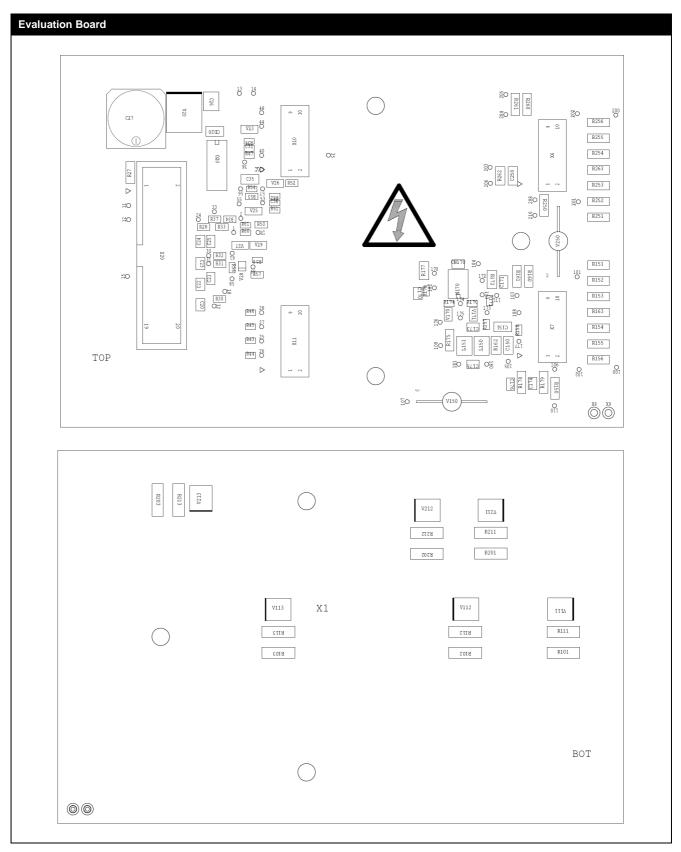


Technical Explanations SKYPER® 32PRO R).

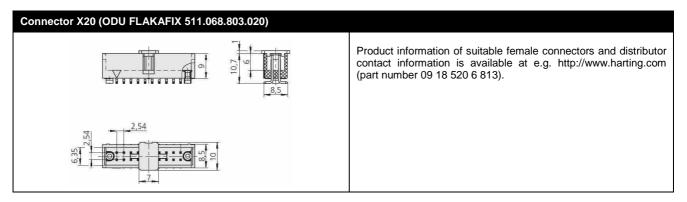
### Dimensions



#### **Component Placement Layout**



### **PIN Array**

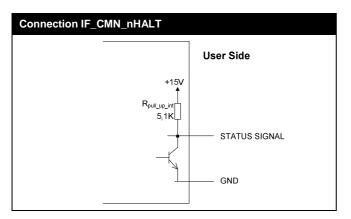


PIN	Signal	Function	Specification
X20:01	IF_PWR_15P	Drive power supply	Stabilised +15V ±4%
X20:02	IF_PWR_GND	GND for power supply	
X20:03	IF_PWR_15P	Drive power supply	Stabilised +15V ±4%
X20:04	IF_PWR_GND	GND for power supply	
X20:05	IF_PWR_15P	Drive power supply	Stabilised +15V ±4%
X20:06	IF_PWR_GND	GND for power supply	
X20:07	reserved		
X20:08	IF_PWR_GND	GND for power supply	
X20:09	IF_CMN_nHALT	Driver core status signal (bidirectional signal with dominant recessive behaviour)	Digital 15V logic; LOW (dominant) = driver disabled; HIGH (recessive) = ready to operate
X20:10	reserved		
X20:11	reserved		
X20:12	IF_CMN_GND	GND for signal IF_CMN_nHALT	
X20:13	reserved		
X20:14	reserved		
X20:15	IF_HB_TOP	Switching signal input (TOP switch)	Digital 15 V logic; 10 kOhm impedance; LOW = TOP switch off; HIGH = TOP switch on
X20:16	IF_HB_BOT	Switching signal input (BOTTOM switch)	Digital 15 V logic; 10 kOhm impedance; LOW = BOT switch off; HIGH = BOT switch on
X20:17	reserved		
X20:18	IF_HB_GND	GND for signals IF_HB_TOP & F_HB_BOT	
X20:19	reserved		
X20:20	reserved		

### Signal IF\_CMN\_nHALT

The Halt Logic Signals PRIM\_HALT\_IN and PRIM\_HALT\_OUT of the driver core are coupled to one bidirectional signal (IF\_CMN\_nHALT) with dominant recessive behaviour. IF\_CMN\_nHALT shows the driver core status. When IF\_CMN\_nHALT is HIGH (recessive), the driver core is ready to operate. When IF\_CMN\_nHALT is LOW (dominant), the driver core is disabled / not ready to operate because of e.g. detected failure or driver core system start.

A controller can hold with the IF\_CMN\_nHALT signal the driver core in a safe state (e.g. during a start up of a system or gathered failure signal of other hardware) or generate a coeval release of paralleled driver. Furthermore, paralleled drivers can send and receive IF\_CMN\_nHALT signals among each other by using a single-wire bus.



### **Setting Dead Time**

Designation	Pattern Name	Setting
R43 (connected to GND)	0603	PRIM_CFG_TDT2_IN Factory setting: 0Ω
R44 (connected to GND)	0603	PRIM_CFG_SELECT_IN Factory setting: not equipped
R45 (connected to GND)	0603	PRIM_CFG_TDT3_IN Factory setting: 0Ω
R46 (connected to GND)	0603	PRIM_CFG_TDT1_IN Factory setting: not equipped

### **Setting Dynamic Short Circuit Protection**

Designation	Pattern Name	Setting	
Designation	Fallelli Naille	Setting	
R162	1206	R <sub>CE</sub> Factory setting: not equipped	TOP
C150	1206	C <sub>CE</sub> Factory setting: not equipped	TOP
R262	1206	R <sub>CE</sub> Factory setting: not equipped	вот
C250	1206	C <sub>CE</sub> Factory setting: not equipped	BOT

### **Collector Series Resistance**

Designation	Pattern Name	Setting	
R150	MiniMELF	R <sub>VCE</sub> * Factory setting: not equipped	TOP
R250	MiniMELF	R <sub>VCE</sub> * Factory setting: not equipped	BOT

### Adaptation Gate Resistors

Designation	Pattern Name	Setting	
R151, R152, R153 (parallel connected)	MiniMELF	R <sub>Gon</sub> Factory setting: not equipped	TOP
R154, R155, R156 (parallel connected)	MiniMELF	R <sub>Goff</sub> Factory setting: not equipped	TOP
R251, R252, R253 (parallel connected)	MiniMELF	R <sub>Gon</sub> Factory setting: not equipped	BOT
R254, R255, R256 (parallel connected)	MiniMELF	R <sub>Goff</sub> Factory setting: not equipped	BOT

### **Adaptation Decoupling Gate Resistors**

For details to the decoupling gate resistors and recommended values, see Modules Explanations and Data Sheets SEMiX<sup>®</sup>.

Designation	Pattern Name	Setting	
R101	MELF	R <sub>G1</sub> Factory setting: not equipped	TOF
R102	MELF	R <sub>G2</sub> Factory setting: not equipped	TOF
R103	MELF	R <sub>G3</sub> Factory setting: not equipped	TOF
R201	MELF	R <sub>G1</sub> Factory setting: not equipped	BOT
R202	MELF	R <sub>G2</sub> Factory setting: not equipped	BOT
R203	MELF	R <sub>G3</sub> Factory setting: not equipped	BOT

### Setting Soft Turn-Off

Designation	Pattern Name	Cotting	
Designation	Pallem Name	Setting	
R160, R161	MiniMELF	R <sub>Goff_SC</sub>	TOP
(parallel connected)		Factory setting: not equipped	
R260, R261		R <sub>Goff SC</sub>	DOT
(parallel connected)	MiniMELF	Factory setting: not equipped	BOT

### **Temperature Signal**

The temperature sensor inside the SEMiX<sup>®</sup> module is directly connected to contacting points T1 and T2. For details to the temperature sensor, see Modules Explanations SEMiX<sup>®</sup>.

### Safety Warnings:

The contacting points T1 and T2 are not electrical isolated. Due to high voltage that may be present at the contacting points T1 and T2, some care must be taken in order to avoid accident. There is no cover or potential isolation that protect the high voltage sections / wires from accidental human contact.

### Please note:

If the contacting points T1 and T2 are used for evaluation of the temperature sensor, the Over Temperature Protection Ciruit must be disabled by taking out the resistors R175, R178 and R179.

#### **Over Temperature Protection Circuit (OTP)**

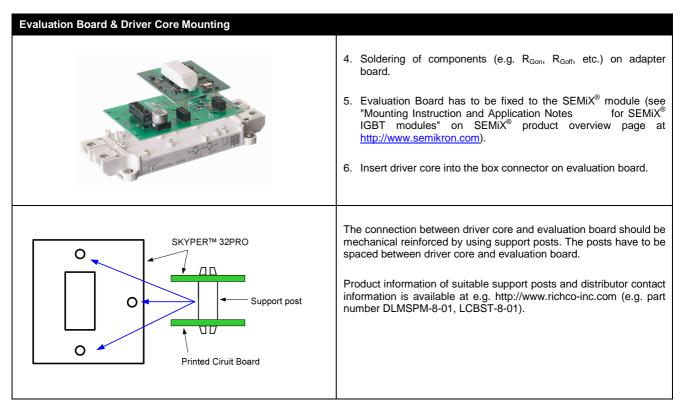
The external error input SEC\_TOP\_ERR\_IN on the secondary side (high potential) of the driver core is used for an over temperature protection circuit to place the gate driver into halt mode.

#### **Dimensioning OTP**

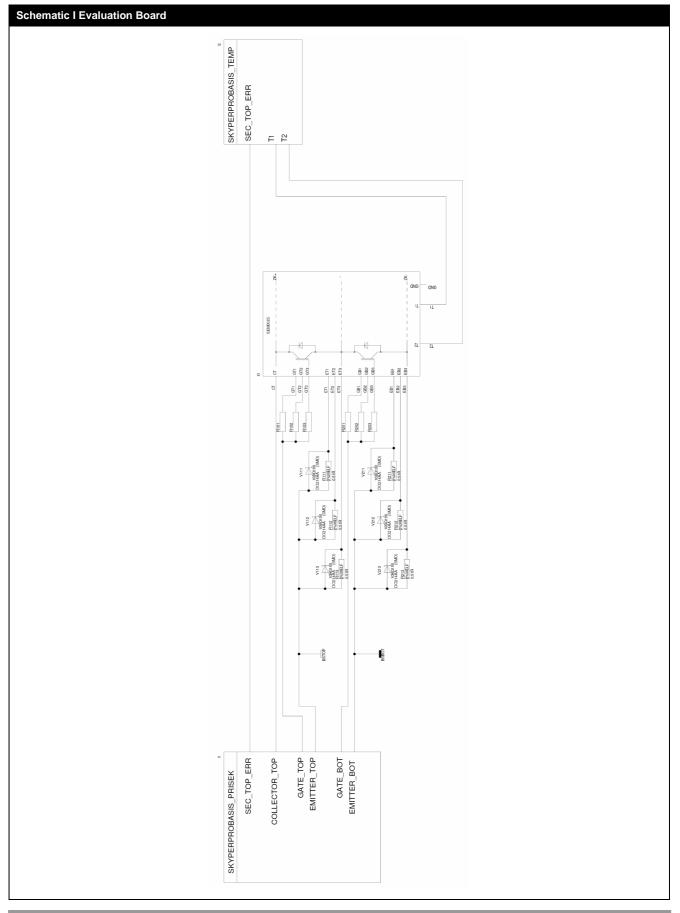
- 1. Define an over temperature trip level according to the application.
- Calculate the nominal ohmic resistance value of the temperature sensor at the defined trip level (see "Modules – Explanations - SEMiX<sup>®</sup>" on SEMiX<sup>®</sup> product overview page at <u>http://www.semikron.com</u>).
- 3. The trip level on the adapter board is set with R172 by using the calculated resistance value.
- Factory setting R172: not equipped
- If no resistor is used, a failure signal is generated.

### **Mounting Notes**

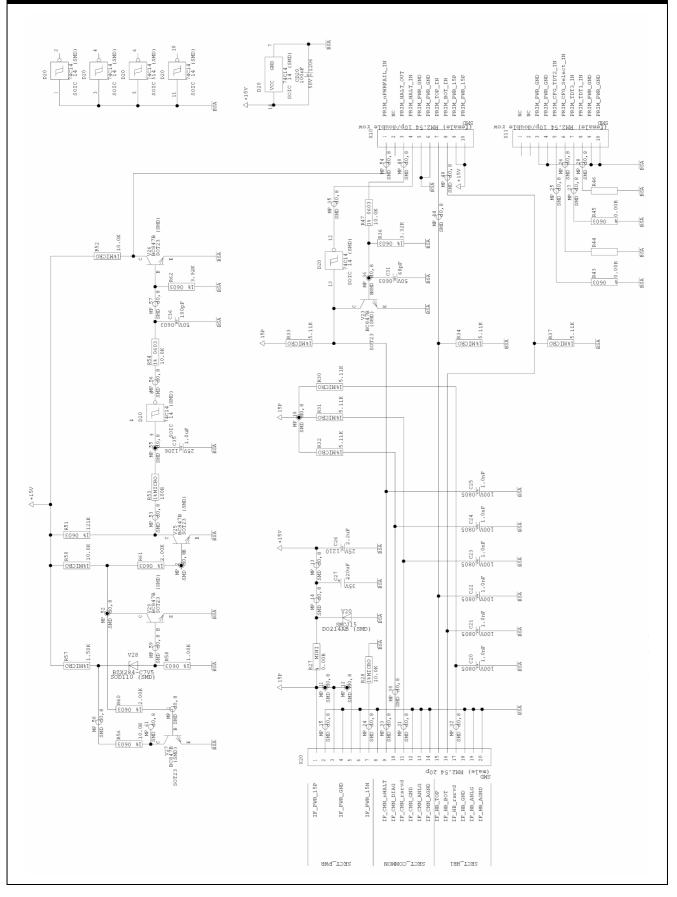
The electrical connections between evaluation board and SEMi $X^{\text{®}}$  are realised via spring contacts integrated in SEMi $X^{\text{®}}$  power modules and via landing pads on the bottom side of the evaluation board.



### Schematics



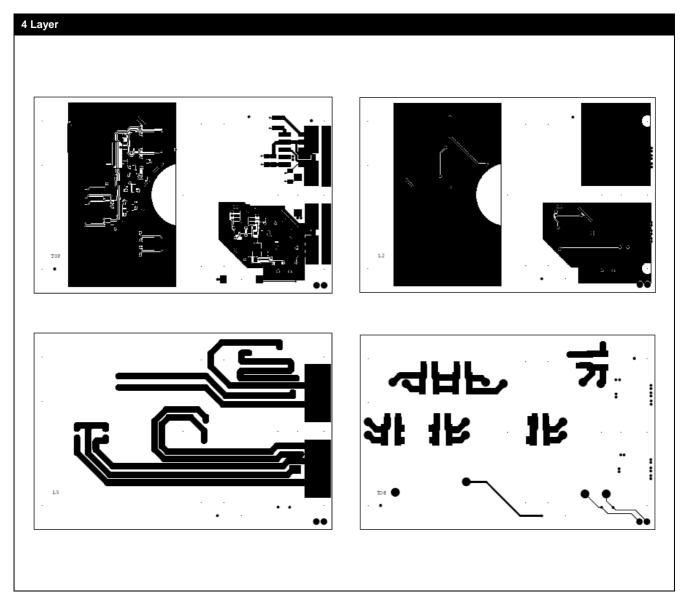
### Schematic II Evaluation Board



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#### Schematic III Evaluation Board +15VTOF TH 1.3/2.2 R157 090 15.0K BSTOR 6X C174 . 100r +15VTO R179 MINI MP 180 SMD 00,8 < IO 5080 c173 00 100nF +15VTO 50V]\* 100nF R171 8090 81 15.0K MP 181 MINI SMD 0,8 T <IO) 9021 #13.01K 50V 30805 TH 1.3/2.2 N170 N170 MP 171 SMD 00,8 7 MP 182 SMD 0, 8 LM2904 SOIC 8 // . 100nI X8 MP 183 MP 178 R172 50V|3 0805 LM2904 BSTOR SOIC BSTO E0 R176 50V 20805 C170 R174 18 0603 30.1K MP 170 d0,8 MP 174 R170 SMD 00,8 1% 0603 30.1K MP 175 SMD 0,8 $\triangleright$ +15VTOF . BAV70W SOT323 V170 Sor323 V171 N170 BSTOP BSTOP 8 VCC GND (SMD) (SMD) LM2904 SOIC 8 (SMD) ININ R175 IC 8 (S CN170 100nF 50V 8 08 5.621 IO \_\_\_\_\_BEC\_TOP\_BRR -SVTOF SEC\_TOP\_ERR GATE\_TOP 0 0 0 EMITTER\_TOP EMITTER\_BOT GATE BOT BSTOP SBOT MP 107 SMD do.8 -7 R180 -40.6 10.04 00 40 173 ↓ 10.04 10.04 00 40 8 0 10.04 -20 0.04 00 40 0 10.04 MP 100 SMD 40.8 V250 BY203/20S MP 200 SMD do.8 10:0K 10.0K R163 MP 110 V150 SMD 40,8 P203/20S SMD BSTOP RSTOP INW % MP 210 SMD 40,8 1260 C250 MP 109 SMD 60,8 R250 R253 R256 R161 R262 R252 R254 R251 1255 C150 R261 R162 R156 R151 3154 R150 R152 R153 R155 MP 177 SMD 60,8 MP 204 SMD d0,8 MP 102 SMD do.8 • MP 201 SMD 40.8 MP 202 SMD 40.8 MP 105 MP 104 SMD 40.8 SMD 40.8 103 d0,8 MP 208 SMD d0,8 MP 205 SMD d0,8 MP\_105 SMD\_d0,8 MP 203 SMD d0,8 MP 206 SMD 40.8 MP\_101 SMD\_d0,8 MP 108 S SMD do.8 MP\_106 SMD\_d0,8 ę - ~ ~ ⊳ vn vo r~ ∞ o 9 - 0 0 vo u⊳ ∞ o (elisme) SMD . NOJ BM2.54 10p/double EM2.54 10p/double (female) SMD SEC\_TOP\_VCE\_CPG SEC\_TOP\_VCE\_CUT SEC\_TOP\_PVCE\_LOF SEC\_TOP\_PVCL\_SP SEC\_TOP\_PVCL\_SP SEC\_TOP\_PVCL\_SUD SEC\_TOP\_PVCL\_SUD SEC\_TOP\_PVCL\_SUD SEC\_TOP\_PVCL\_SUD SEC\_TOP\_PVCL\_SUD SEC\_BOT\_VCE\_CFG SEC\_BOT\_VCE\_CUT SEC\_BOT\_PWR\_15P SEC\_BOT\_PWR\_15P SEC\_BOT\_ERT\_NN SEC\_BOT\_ERT\_NN SEC\_BOT\_PWR\_6ND SEC\_BOT\_PWR\_6ND SEC\_BOT\_PWR\_6ND SEC\_BOT\_PWR\_8ND SEC\_BOT\_PWR\_8ND

### Layouts



### Parts List

### Parts List Evaluation Board

Count	Ref. Designator	Value	Pattern Name	Description
7	C170, C171, C173, C174, C175, C176, CN170	100nF	0805 (SMD)	Capacitor X7R
6	C20, C21, C22, C23, C24, C25	1nF	0805 (SMD)	Capacitor X7R
1	C26	2,2µF	1210 (SMD)	Capacitor X7R
1	C27	220uF/35V	SMD	Longlife-Elko
1	C31	68pF	0603 (SMD)	Capacitor NP0
2	C35, C151	1uF	1206 (SMD)	Capacitor X7R
1	C36	100pF	0603 (SMD)	Capacitor NP0
1	CD20	100nF	1206 (SMD)	Capacitor X7R
1	D20	74C14	SOIC 14 (SMD)	Logic-IC 74C
2	L150, L151	100uH	1210 (SMD)	Inductor
1	N170	LM2904	SOIC 8 (SMD)	Operational Amplifier
6	R111, R112, R113, R201, R211, R212, R 213	0,51Ohm	Melf (SMD)	2%
2	R157, R171	15,0KOhm	0603 (SMD)	1%
1	R158	10,0Ohm	0603 (SMD)	1%
2	R163, R263	10,0KOhm	MiniMelf (SMD)	1%
3	R170, R174, R176	30,1KOhm	0603 (SMD)	1%
1	R175	5,62KOhm	MiniMelf (SMD)	1%
1	R177	3,01KOhm	1206 (SMD)	1%
3	R27, R178, R179	0,00Ohm	MiniMelf (SMD)	
3	R28, R50, R52	10,0KOhm	MicroMelf (SMD)	1%
6	R30, R31, R32, R33, R34, R37	5,11KOhm	MicroMelf (SMD)	1%
1	R36	3,32KOhm	0603 (SMD)	1%
2	R43, R45	0,00Ohm	0603 (SMD)	
3	R47, R54, R56	10,0KOhm	0603 (SMD)	1%
1	R51	121KOhm	0603 (SMD)	1%
1	R53	100Ohm	MicroMelf (SMD)	1%
1	R57	1,50KOhm	MicroMelf (SMD)	1%
1	R58	1,00KOhm	0603 (SMD)	1%
2	R60, R61	2,00KOhm	0603 (SMD)	1%
1	R62	3,92KOhm	0603 (SMD)	1%
6	V111, V112, V113, V211, V212, V213	10BQ100	SMB (SMD)	Diode Schottky
2	V150, V250	BY203/20S	SMD	High Voltage Diode
2	V170, V171	BAV70W	SOT323 (SMD)	Double Diode
1	V20	SMCJ15	DO214AB (SMD)	Suppressor Diode
5	V23, V25, V26, V27, V29	BC847B	SOT23 (SMD)	NPN-Transistor
1	V28	BZX284-C7V5	SOD110 (SMD)	Zener-Diode
1	X20	20p.	SMD	Connector
4	X6, X7, X10, X11	RM2,54 10p.	SMD	Box Connector

TP: Test Point

Box Connector: SUYIN 254100FA010G200ZU

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