

DL246

The DL246 is a cost effective and compact integrated delay line. Using LTCC Technology, the DL246 was designed to optimize the performance of the Scintera family of RF Adaptive Linearization Integrated Circuits. The versatile design integrates 2 delay lines, the first a nominal 2nS delay and the second a nominal 4nS. These 2 circuits can be combined for an overall delay of 6nS.



Key Features:

- Versatile design yields 3 separate nominal delay paths (2nS, 4nS & 6nS)
- Compact 12mm x 12mm
- Surface Mount
- Optimized to work with the RFPAL family of products from Scintera
- Characterized over the operating frequency 500MHz to 3000MHz
- RoHS Compliant
- Part #DL246TR comes packaged on 7" Tape & Reel

Absolute Maximum Ratings:

Parameters	Symbol	Value	Units
Storage Temperature	Tstg	-65 to 150	ô
Maximum Operating Temperature	Tmax	-40 to 85	°C

Electrical Specifications: (at room temperature)

Testing Item	Test Constraints	Min	Nom	Max	Unit
Delay	Pin 3 to Pin 4; 500 MHz to 3000 MHz	1.6	2.0	2.6	nS
Insertion Loss	Pin 3 to Pin 4; 900 MHz		1.0	1.6	dB
Insertion Loss	Pin 3 to Pin 4; 2100 MHz		2.0	2.6	dB
Return Loss	Pin 3 to Pin 4; 500 MHz to 3000 MHz	-9			dB
Delay	Pin 1 to Pin 2; 500 MHz to 3000 MHz	3.3	4.0	4.6	nS
Insertion Loss	Pin 1 to Pin 2; 900 MHz	0.0	1.8	2.6	dB
Insertion Loss	Pin 1 to Pin 2; 2100 MHz		3.4	4.5	dB
Return Loss	Pin 1 to Pin 2; 500 MHz to 3000 MHz	-9			dB
Delay	Pin 2 to Pin 4; 500 MHz to 3000 MHz	4.0	6.0	7.2	nS
Insertion Loss	Pin 2 to Pin 4; 900 MHz		3.0	3.6	dB
Insertion Loss	Pin 2 to Pin 4; 2100 MHz		5.5	7.0	dB
Return Loss	Pin 2 to Pin 4; 500 MHz to 3000 MHz	-9			dB

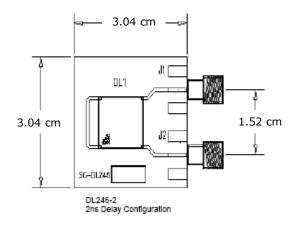
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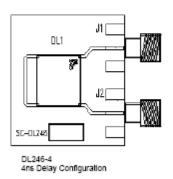
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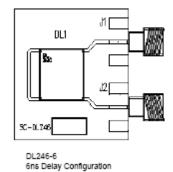
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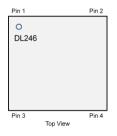


Mounting Orientations: (note pin 1 location)

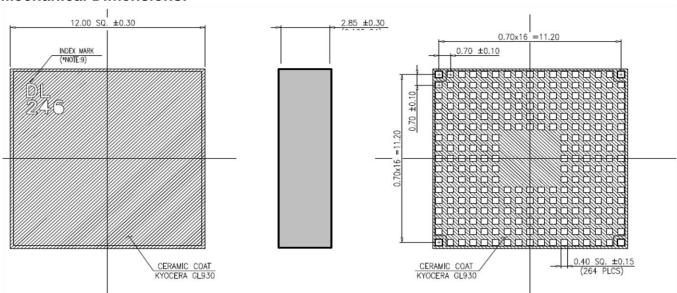








Mechanical Dimensions:



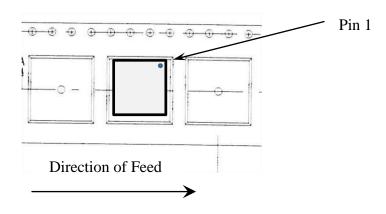
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Tape & Reel Information:

24mm



Ordering Information

Part Number	DL246	
	DL246TR	

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