

## Digital Attenuator, 5-Bit, 31 dB 400 - 2500 MHz

Rev. V6

#### **Features**

- 5 Bits, 1 dB Steps
- **Excellent Accuracy**
- Single Positive Control (+3 V to +5 V)
- QSOP-16 (SSOP-16) Package

### **Description**

M/A-COM's MAATSS0002 is a 1 dB step GaAs MMIC digital attenuator with 31 dB attenuation range in a low cost QSOP-16 (SSOP-16) package. It requires external DC blocking capacitors on the RF ports, positive supply voltage and five individual bit control voltages.

The MAATSS0002 is particularly suited where high attenuation accuracy, low insertion loss and low intermodulation products are required. applications include base stations, wireless data, and wireless local loop gain level control circuits.

The MAATSS0002 is fabricated using M/A-COM's GaAs 1.0 micron process. The process features full chip passivation for increased performance and reliability.

## Ordering Information<sup>1</sup>

Part Number	Package
MAATSS0002	Bulk Packaging
MAATSS0002TR	Tape and Reel (1K Reel)
MAATSS0002SMB	Sample Test Board

<sup>1.</sup> Reference Application Note M513 for reel size information.

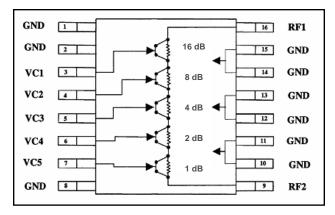
## **Absolute Maximum Ratings<sup>2</sup>**

Commitment to produce in volume is not guaranteed.

Parameter	Absolute Maximum		
Input Power	+34 dBm		
Voltage	+7 V		
Operating Temperature	-40°C to +85°C		
Storage Temperature	-65°C to +125°C		

<sup>2.</sup> Exceeding any one or combination of these limits may cause permanent damage to this device.

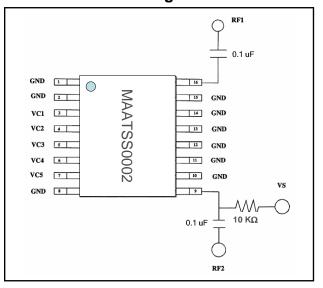
#### **Functional Schematic**



### Pin Configuration

Pin No.	Function	Pin No.	Function	
1	GND	9	RF2	
2	GND	10	GND	
3	VC1	11	GND	
4	VC2	12	GND	
5	VC3	13	GND	
6	VC4	14	GND	
7	VC5	15	GND	
8	GND	16	RF1	

### **Recommended Configuration**



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## Electrical Specifications: $T_A = 25$ °C, $Z_0 = 50 \Omega$ , $V_S = +5 V$ , $V_C = 0 V / 5 V$

Parameter	Conditions	Frequency	Units	Min.	Тур.	Max.
Insertion Loss (reference state)	_	0.8 - 1.5 GHz 1.5 - 2.0 GHz 2.0 - 2.5 GHz	dB dB dB	_ _ _	1.8 2.2 2.7	1.9 2.3 2.9
Accuracy	_	0.8 - 1.5 GHz 1.5 - 2.0 GHz 2.0 - 2.5 GHz	± (0.3 dB + 4% of Attenuation) ± (0.3 dB + 3% of Attenuation) ± (0.4 dB + 3% of Attenuation)			tion)
Step Change	_	0.8 - 1.5 GHz 1.5 - 2.0 GHz 2.0 - 2.5 GHz	dB dB dB	0.4 0.4 0.4	1.0 1.0 1.0	1.4 1.6 1.8
Attenuation Range	_	0.8 - 2.5 GHz	dB	_	31	_
VSWR	_	0.8 - 2.5 GHz	ratio	_	_	2.0:1
Trise, Tfall	10% to 90% RF, 90% to 10% RF	_	μS	_	2	_
Ton, Toff	50% Control to 10% / 90% RF	_	μS	_	2	_
Transients	In Band	_	mV	_	120	_
P1dB	+3V +5V	0.8 - 2.5 GHz 0.8 - 2.5 GHz	dBm dBm	15 18	21 27	_
IP3	Two tones, Pin $\leq$ +5 dBm/tone +3 V +5 V	0.8 - 2.5 GHz 0.8 - 2.5 GHz	dBm dBm	34 43		
Control Current	Vc Vs		μA μA	_	_	40 200

## Truth Table <sup>3</sup>

Control Inputs					
VC5	VC4	VC3	VC2	VC1	Attenuation (dB)
1	1	1	1	1	Reference
0	1	1	1	1	1 dB
1	0	1	1	1	2 dB
1	1	0	1	1	4 dB
1	1	1	0	1	8 dB
1	1	1	1	0	16 dB

<sup>3.</sup>  $0 = 0.0 \text{ V} \pm 0.2 \text{ V}$  $1 = Vs = 5.0 V \pm 0.2 V$ 

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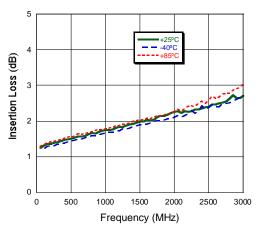


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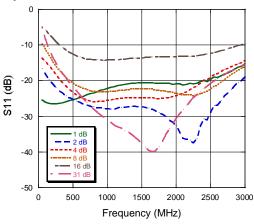
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## **Typical Performance Curves**

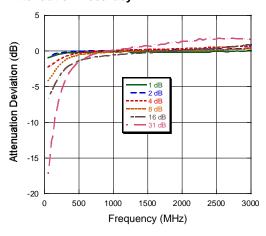
#### **Insertion Loss**



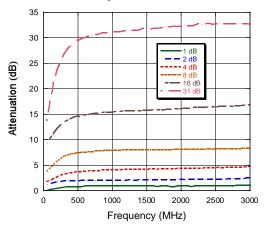
#### Input Return Loss



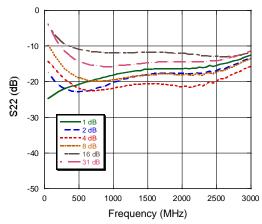
#### **Attenuation Accuracy**



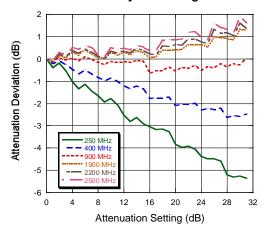
#### Attenuation at Major Bits



#### **Output Return Loss**



#### Attenuation Accuracy vs. Setting



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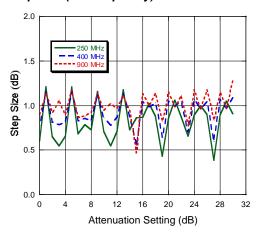


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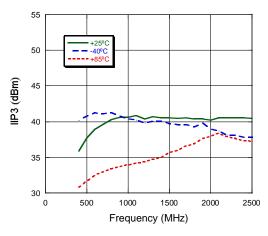
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## **Typical Performance Curves**

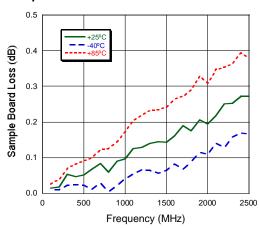
#### Step Size (low frequency)



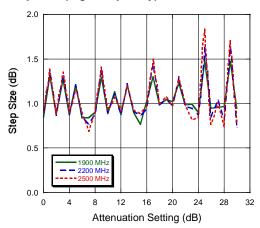
#### IIP3 at 3 V



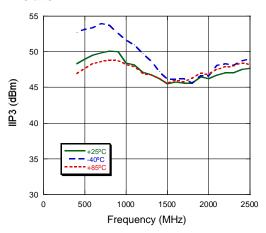
#### Sample Board Loss



#### Step Size (high frequency)



#### IIP3 at 5 V



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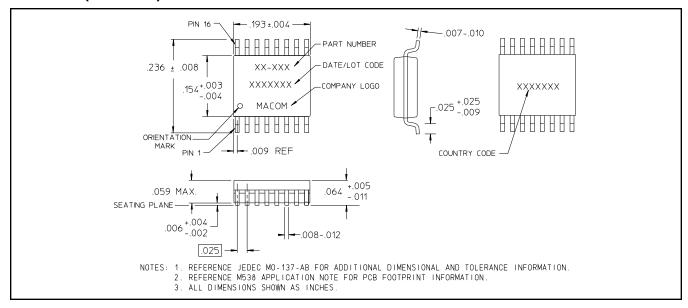
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### **QSOP-16 (SSOP-16)**



### **Operating Instructions**

The MAATSS0002 is designed to operate with 5 V logic levels. The difference between +3 V and +5 V operation is minimal for small signal performance. IIP3, however, is a strong function of voltage. +3 V is the minimum voltage at which the product will reliably operate.

The MAATSS0002 requires a parallel interface that allows the user to enter a 5 bit digital word. Each state increments the attenuation by 1.0 dB giving a total range of 31 dB.

The MAATSS0002 is not internally DC blocked. This means that the device requires DC blocking capacitors on the RF1 and RF2 ports. M/A-COM recommends 0.1 uF to allow for the entire frequency range to be utilized. Higher frequency applications can use smaller value capacitors as DC blocks.

For application information concerning this and other M/A-COM products, please visit our website at www.macom.com, where information including soldering profiles, reliability procedures, and Sparameter data can be found.

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