# MILITARY COTS DC-DC CONVERTER

9-70V Continuous Input	100V Transient Input	28V Output	5.8A Output	89% @ 2.9A / 88% @ 5.8A Efficiency

Operation: -55°C to +100°C

The MilQor series of Mil-COTS DC-DC converters brings SynQor's field proven high-efficiency synchronous rectification technology to the Military/Aerospace industry. SynQor's ruggedized encased packaging approach ensures survivability in demanding environments. Compatible with the industry standard format, these converters operate at a fixed frequency, and follow conservative component derating guidelines. They are designed and manufactured to comply with a wide range of military standards.

Mil-COTS
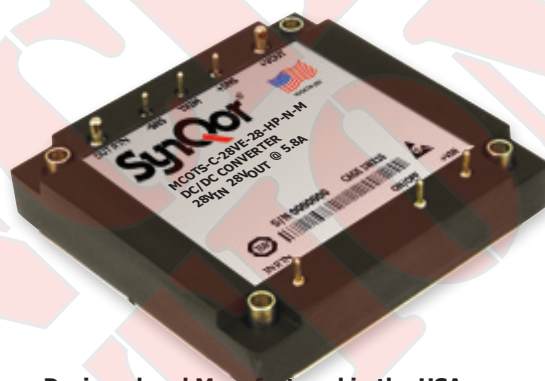
Designed and Manufactured in the USA

Safety Features

- 2250V, 30 MΩ input-to-output isolation
- Certified 60950-1 requirement for basic insulation (see Standards and Qualifications page)

Mechanical Features

- Industry standard half-brick pin-out configuration
- Size: 2.49" x 2.39" x 0.51"
             (63.1 x 60.6 x 13.0 mm)
- Total weight: 4.9 oz. (139 g)
- Flanged baseplate version available

Protection Features

- Input under-voltage lockout
- Output current limit and short circuit protection
- Active back bias limit
- Output over-voltage protection
- Thermal shutdown

Specification Compliance

MCOTS series converters (with an MCOTS filter) are designed to meet:
- MIL-HDBK-704 (A-F)
- RTCA/DO-160E Section 16
- MIL-STD-1275 (B,D)
- DEF-STAN 61-5 (Part 6)/(5 or 6)
- MIL-STD-461 (C, D, E)
- RTCA/DO-160E Section 22

Operational Features

- High efficiency, 88% at full rated load current
- Operating input voltage range: 9-70V
- Fixed frequency switching provides predictable EMI
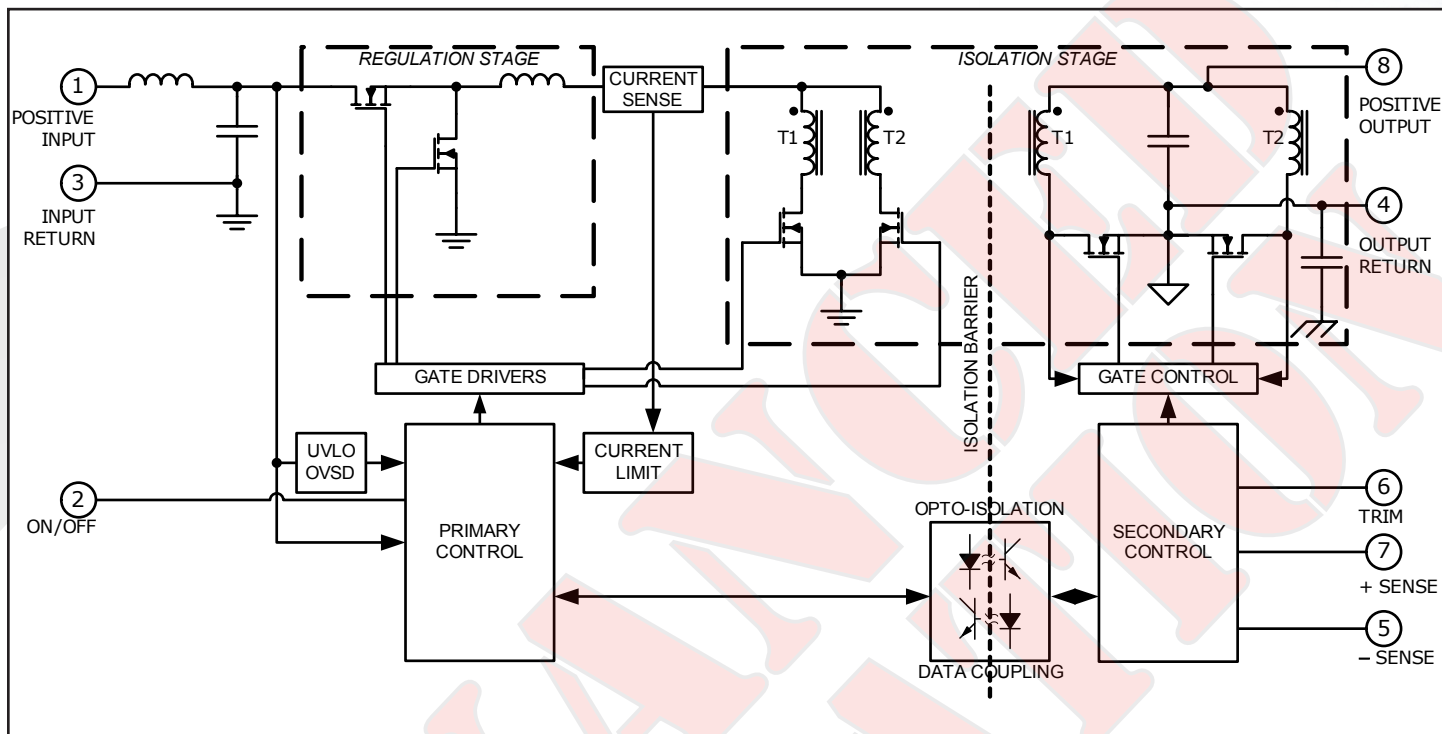- No minimum load requirement

Control Features

- On/Off control referenced to input return
- Remote sense for the output voltage
- Output voltage trim range of +10%, -20%
- Optional: Active current share for parallel applications
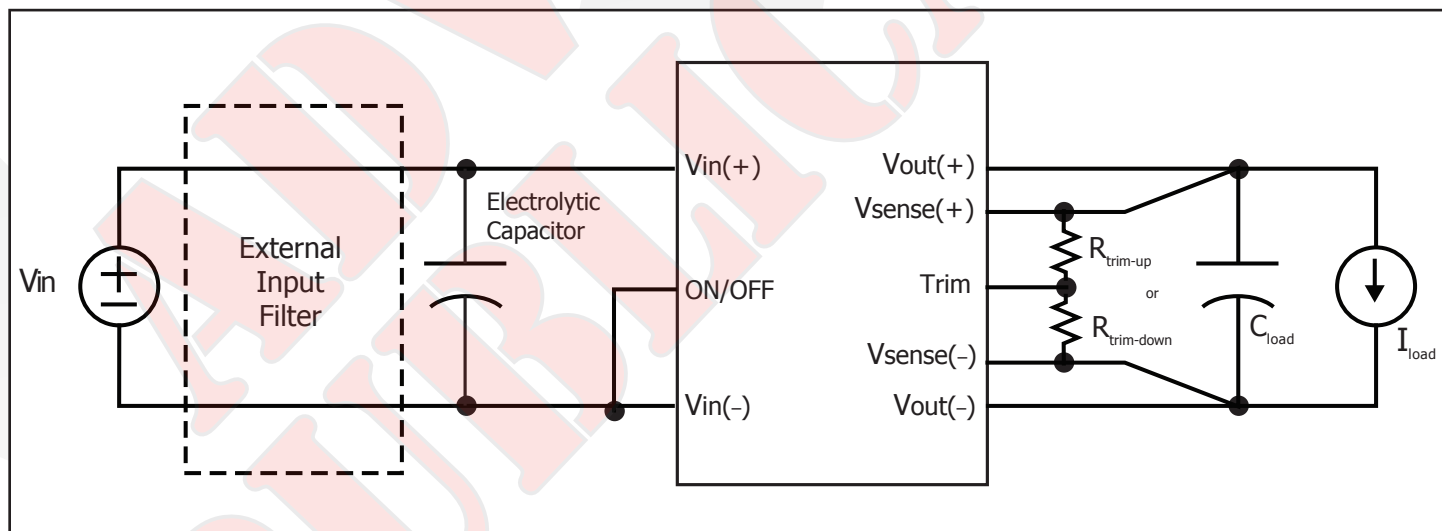
Screening/Qualification

- AS9100 and ISO 9001:2008 certified facility
- Qualification consistent with MIL-STD-883
- Available with S-Grade or M-Grade screening
- Temperature cycling per MIL-STD-883, Method 1010, Condition B, 10 cycles
- Burn-In at 100C baseplate temperature
- Final visual inspection per MIL-STD-2008
- Full component traceability

BLOCK DIAGRAM

REGULATION STAGE

ISOLATION STAGE

① POSITIVE INPUT

③ INPUT RETURN

CURRENT SENSE

T1 T2

T1 T2

⑧ POSITIVE OUTPUT

④ OUTPUT RETURN

ISOLATION BARRIER

GATE DRIVERS

GATE CONTROL

② ON/OFF

UVLO OVSD

PRIMARY CONTROL

CURRENT LIMIT

OPTO-ISOLATION

DATA COUPLING

SECONDARY CONTROL

⑥ TRIM

⑦ + SENSE

⑤ − SENSE

TYPICAL CONNECTION DIAGRAM

Vin

External Input Filter

Electrolytic Capacitor

Vin(+)

ON/OFF

Vin(−)

Vout(+)

Vsense(+)

Trim

Vsense(−)

Vout(−)

$R_{trim-up}$

or

$R_{trim-down}$

C_{load}

I_{load}

MCOTS-C-28VE-28-HP ELECTRICAL CHARACTERISTICS

Tb = 25 °C, Vin = 28dc unless otherwise noted; full operating temperature range is -55 °C to +100 °C baseplate temperature with appropriate power derating. Specifications subject to change without notice.

Parameter	Min.	Typ.	Max.	Units	Notes & Conditions
ABSOLUTE MAXIMUM RATINGS					
Input Voltage					
Non-Operating	-1		100	V	Continuous
Operating			70	V	Continuous
Operating Transient Protection			100	V	
Isolation Voltage					
Input to Output			2250	Vdc	
Input to Base-Plate			2250	Vdc	
Output to Base-Plate			2250	Vdc	
Operating Temperature	-55		100	°C	Baseplate temperature
Storage Temperature	-65		135	°C	
Voltage at ON/OFF input pin	-2		18	V	
INPUT CHARACTERISTICS					
Operating Input Voltage Range	9	28	70	V	100V transient for 1 s; see Note 3
Input Under-Voltage Lockout					
Turn-On Voltage Threshold	9.1	9.5	9.9	V	
Turn-Off Voltage Threshold	7.8	8.2	8.7	V	
Lockout Voltage Hysteresis		1.3		V	
Recommended External Input Capacitance		100		µF	Typical ESR 0.1-0.2 Ω
Input Filter Component Values (L\C)		0.5\13.2		µH\µF	Internal values; see Figure E
Maximum Input Current			21.5	A	VIN min; Full Load
No-Load Input Current		225	300	mA	
Disabled Input Current		4	6	mA	
Response to Input Transient		5		V	500V/ms
Input Terminal Ripple Current		635		mA	RMS
Recommended Input Fuse			30	A	Fast acting external fuse recommended
OUTPUT CHARACTERISTICS					
Output Voltage Set Point	27.72	28.00	28.28	V	
Output Voltage Regulation					
Over Line		±0.1	±0.3	%	
Over Load		±0.1	±0.3	%	
Over Temperature	-420		420	mV	
Total Output Voltage Range	27.30		28.70	V	Over sample, line, load, temperature & life
Output Voltage Ripple and Noise					20 MHz bandwidth; See Note 1
Peak-to-Peak		70	120	mV	Full Load
RMS		25	45	mV	Full Load
Operating Output Current Range	0		5.8	A	Subject to thermal derating
Output DC Current-Limit Inception	6.1	6.9	7.7	A	Output Voltage 10% Low
Output DC Current-Limit Shutdown Voltage		15.5		V	
Back-Drive Current Limit while Enabled		0.7		A	Negative current drawn from output
Back-Drive Current Limit while Disabled	0	0.4	50	mA	Negative current drawn from output
Maximum Output Capacitance			350	µF	Vout nominal at full load (resistive load)
Output Voltage during Load Current Transient					
Step Change in Output Current (0.1 A/µs)		1.5		V	50% to 75% to 50% IOUT max
Settling Time		1		ms	To within 1% VOUT nom
Output Voltage Trim Range	-20		10	%	Across Pins 8 & 4; Figure C; see Note 2
Output Voltage Remote Sense Range			10	%	Across Pins 8 & 4
Output Over-Voltage Protection	32.0	34.4	36.8	V	Over full temp range
EFFICIENCY					
100% Load		87.5		%	See Figure 1 for efficiency curve
50% Load		88.5		%	See Figure 1 for efficiency curve

Note 1: Output is terminated with 1 µF ceramic and 15 µF ceramic capacitors. For applications requiring reduced output voltage ripple and noise, consult SynQor applications support (e-mail: mqnbofae@synqor.com)

Note 2: Trim-up range is limited below 10% at low line and full load.

Note 3: Start-up guaranteed above 9.9 V, but will operate down to 9V.

MCOTS-C-28VE-28-HP ELECTRICAL CHARACTERISTICS

Tb = 25 °C, Vin = 28dc unless otherwise noted; full operating temperature range is -55 °C to +100 °C baseplate temperature with appropriate power derating. Specifications subject to change without notice.

Parameter	Min.	Typ.	Max.	Units	Notes & Conditions
DYNAMIC CHARACTERISTICS					
Turn-On Transient					
Turn-On Time		10		ms	Full load, Vout=90% nom.
Start-Up Inhibit Time	200	230	250	ms	See Figure F
Output Voltage Overshoot		0		%	Maximum Output Capacitance
ISOLATION CHARACTERISTICS					
Isolation Voltage (dielectric strength)					See Absolute Maximum Ratings
Isolation Resistance		30		MΩ	
Isolation Capacitance (input to output)		1000		pF	See Note 1
TEMPERATURE LIMITS FOR POWER DERATING CURVES					
Semiconductor Junction Temperature			125	°C	Package rated to 150 °C
Board Temperature			125	°C	UL rated max operating temp 130 °C
Transformer Temperature			125	°C	
Maximum Baseplate Temperature, Tb			100	°C	
FEATURE CHARACTERISTICS					
Switching Frequency	240	260	280	kHz	Isolation stage switching freq. is half this
ON/OFF Control					
Off-State Voltage	2.4		18	V	
On-State Voltage	-2		0.8	V	
ON/OFF Control					Application notes Figures A & B
Pull-Up Voltage		5		V	
Pull-Up Resistance		50		kΩ	
Over-Temperature Shutdown OTP Trip Point		125		°C	Average PCB Temperature
Over-Temperature Shutdown Restart Hysteresis		10		°C	
RELIABILITY CHARACTERISTICS					
Calculated MTBF per MIL-HDBK-217F		4.1		10^6 Hrs.	Ground Benign, 70°C Tb
Calculated MTBF per MIL-HDBK-217F		0.92		10^6 Hrs.	Ground Mobile, 70°C Tb

Note 1: Higher values of isolation capacitance can be added external to the module.

STANDARDS COMPLIANCE

Parameter	Notes & Conditions
STANDARDS COMPLIANCE	
UL 60950-1/R:2011-12	Basic Insulation
CAN/CSA-C22.2 No. 60950-1/:2011	
EN60950-1/A12:2011	

Note: An external input fuse must always be used to meet these safety requirements. Contact SynQor for official safety certificates on new releases or download from the SynQor website.
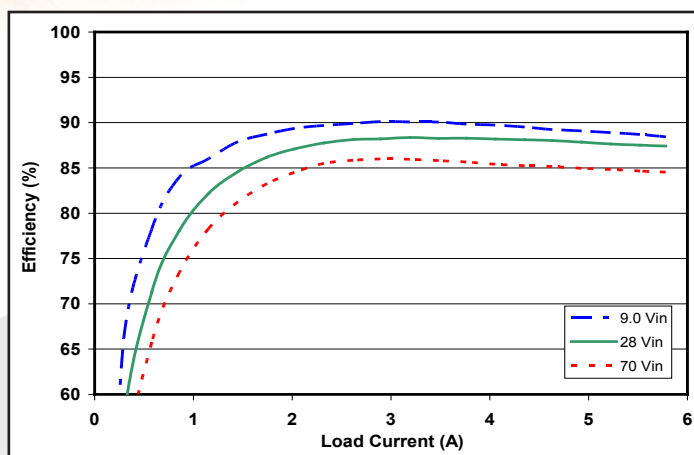
Figure 1: *Efficiency at nominal output voltage vs. load current for minimum, nominal, and maximum input voltage at 25°C.*
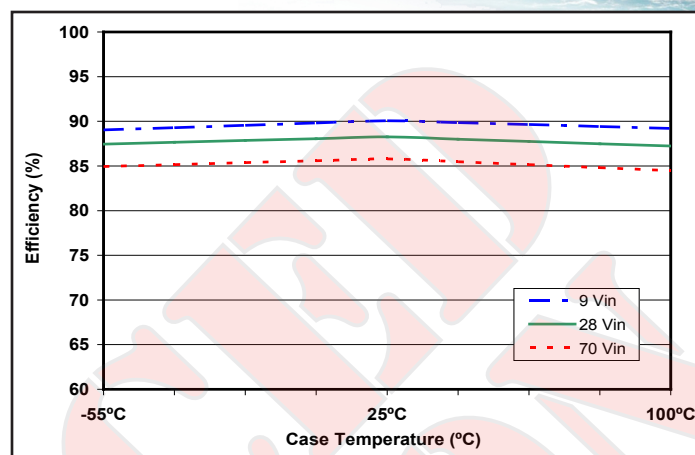

Figure 2: *Efficiency at nominal output voltage and 60% rated power vs. case temperature for minimum, nominal, and maximum input voltage.*
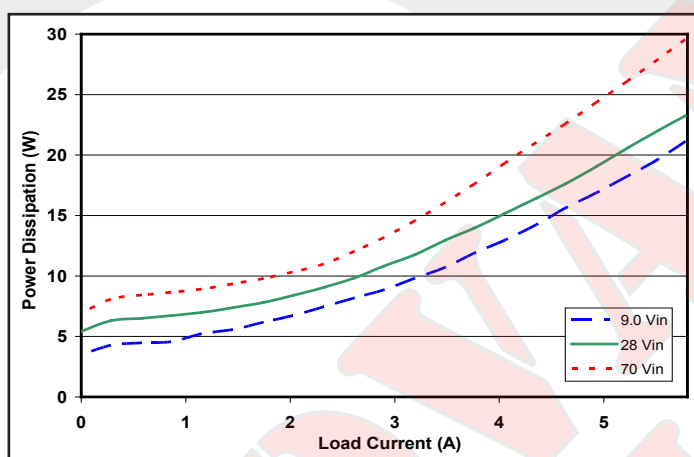

Figure 3: *Power dissipation at nominal output voltage vs. load current for minimum, nominal, and maximum input voltage at T_{CASE}=25°C.*
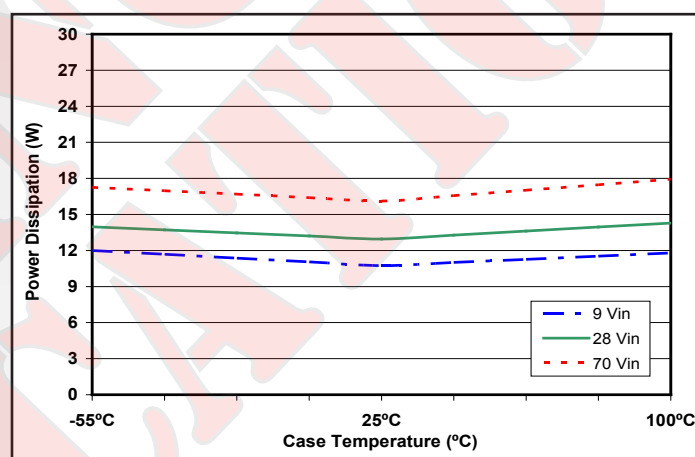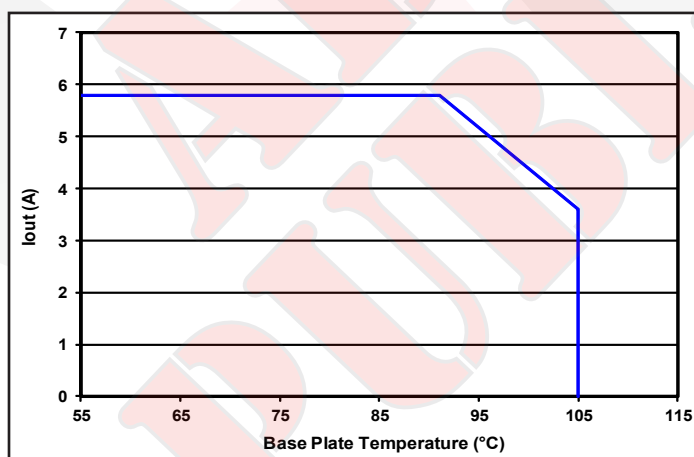

Figure 4: *Power dissipation at nominal output voltage and 60% rated power vs. case temperature for minimum, nominal, and maximum input voltage.*

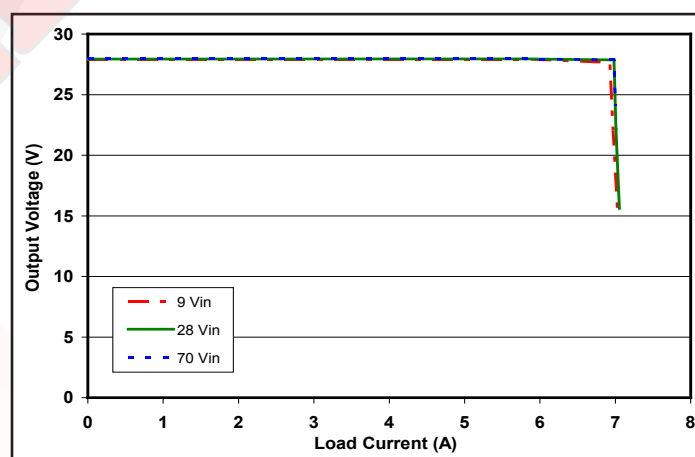**Figure 5:** *Maximum output current vs. base plate temperature (nominal input voltage).*

Figure 6: *Output voltage vs. load current showing typical current limit curves. See Current Limit section in the Application Notes.*
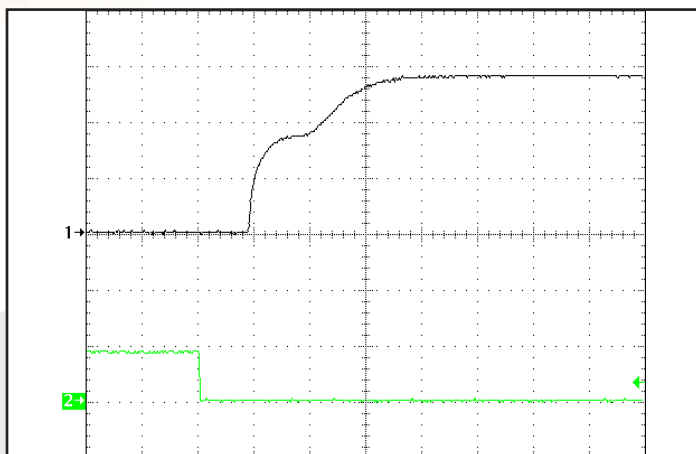
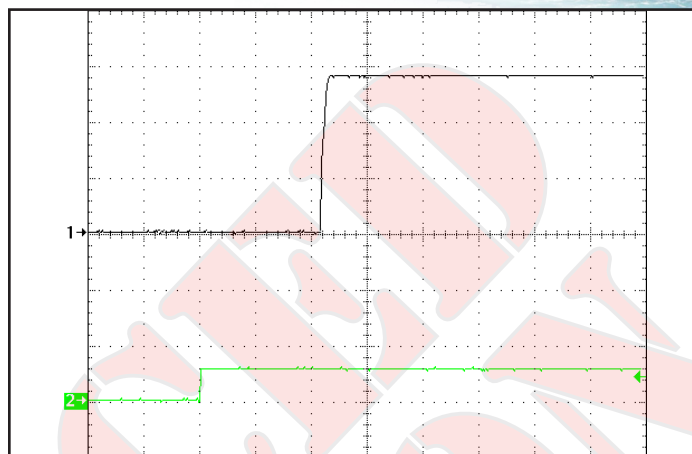**Figure 7:** *Typical startup waveform. Input voltage pre-applied, Ch 1: Vout (10V/div). Ch 2: ON|OFF (5V/div).*

Figure 8: *Turn-on transient at full resistive load and zero output capacitance initiated by Vin. ON/OFF Pin previously low. Ch 1: Vout (10V/div). Ch 2: Vin (50V/div).*
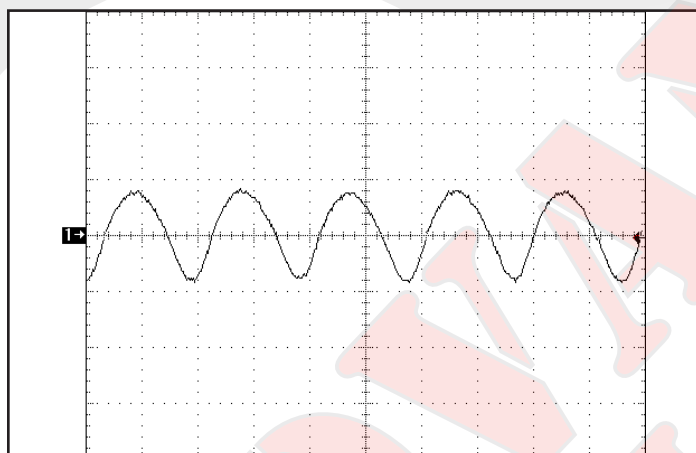

Figure 9: *Input terminal current ripple, i_C, at full rated output current and nominal input voltage with SynQor MCOTS filter module (1 A/div). Bandwidth: 20MHz. See Figure 17.*
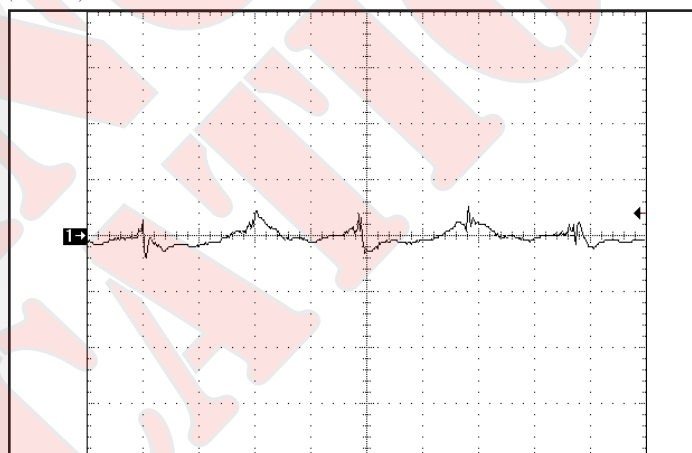

Figure 10: *Output voltage ripple, Vout, at nominal input voltage and rated load current (50 mV/div). Load capacitance: 1µF ceramic capacitor and 15µF tantalum capacitor. Bandwidth: 20 MHz. See Figure 17.*
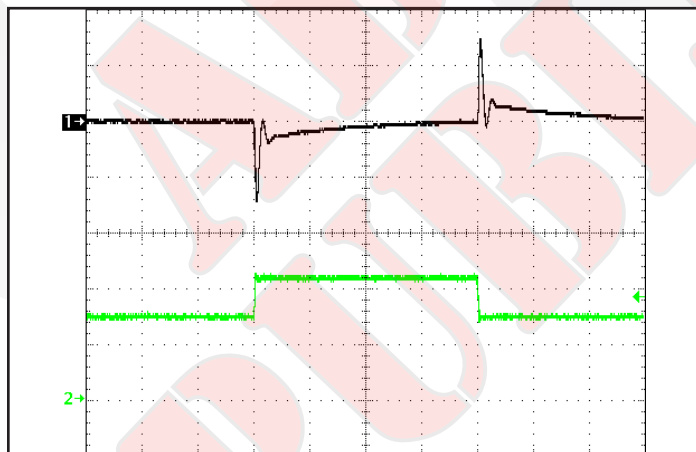

Figure 11: *Output voltage response to step-change in load current (50%-75%-50% of Iout(max); dI/dt = 0.1 A/µs). Load cap: 1 µF ceramic and 15 µF tantalum capacitors. Ch 1: Vout (1V/div), Ch 2: Iout (2A/div).*

Figure 12: *Output voltage response to step-change in input voltage (500V/ms), at full load current. Load cap: 100 µF electrolytic output capacitance. Ch 1: Vout (5V/div), Ch 2: Vin (50V/div).*
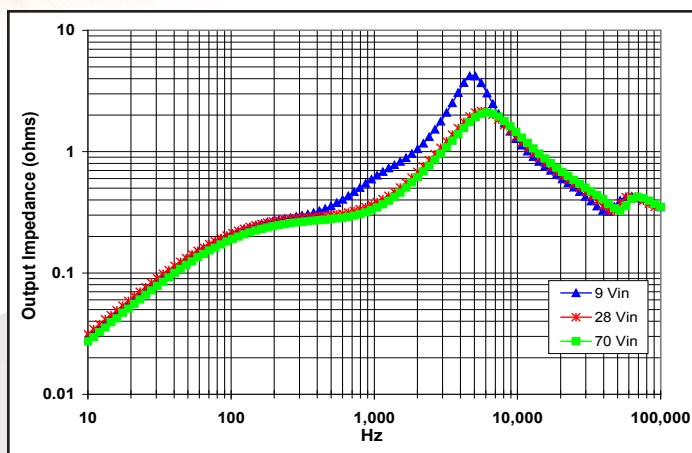
Figure 13: *Magnitude of incremental output impedance ($Z_{out} = v_{out}/i_{out}$) for minimum, nominal, and maximum input voltage at full rated power.*
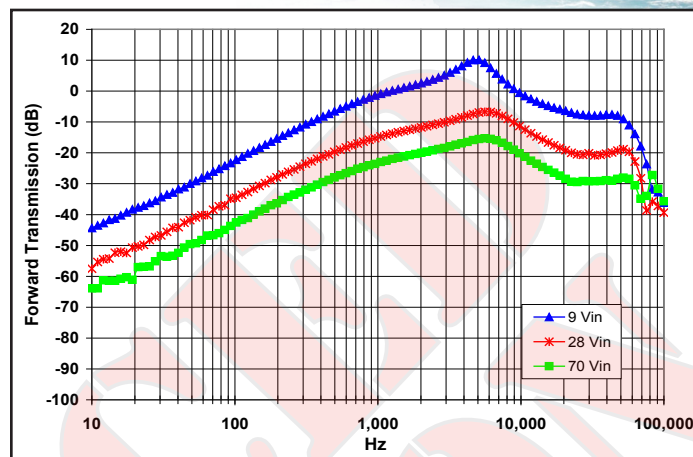

Figure 14: *Magnitude of incremental forward transmission ($FT = v_{out}/v_{in}$) for minimum, nominal, and maximum input voltage at full rated power.*
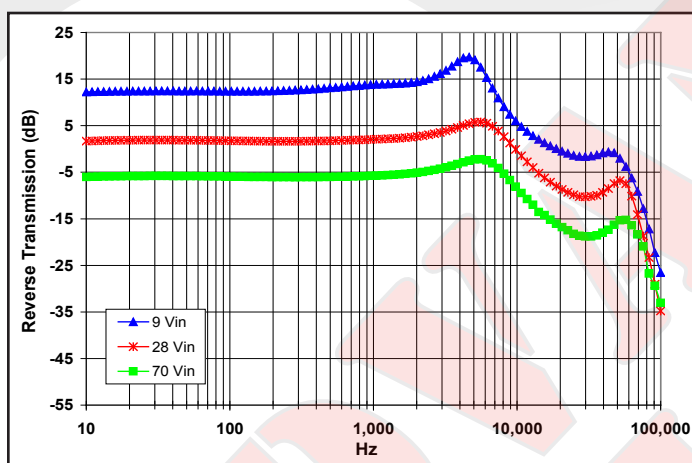

Figure 15: *Magnitude of incremental reverse transmission ($RT = i_{in}/i_{out}$) for minimum, nominal, and maximum input voltage at full rated power.*
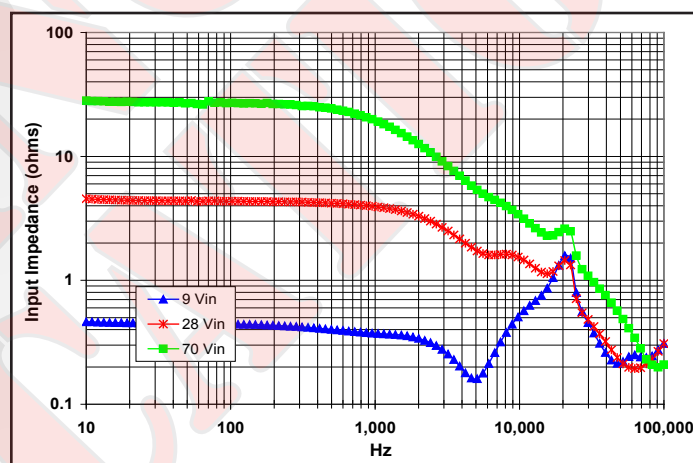
Figure 16: *Magnitude of incremental input impedance ($Z_{in} = v_{in}/i_{in}$) for minimum, nominal, and maximum input voltage at full rated power.*
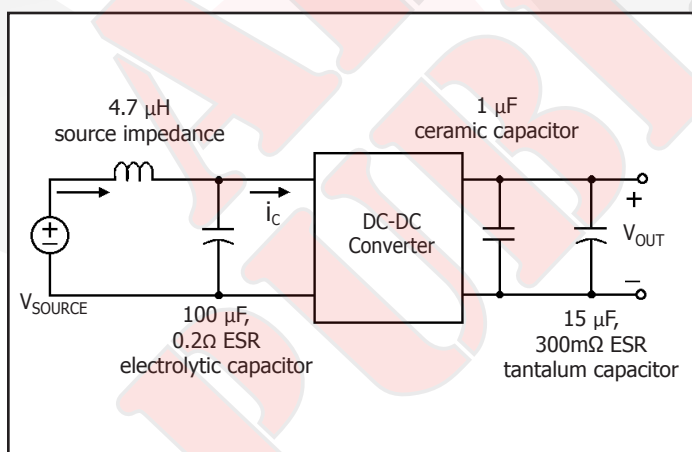

Figure 17: *Test set-up diagram showing measurement points for Input Terminal Ripple Current (Figure 9) and Output Voltage Ripple (Figure 10).*
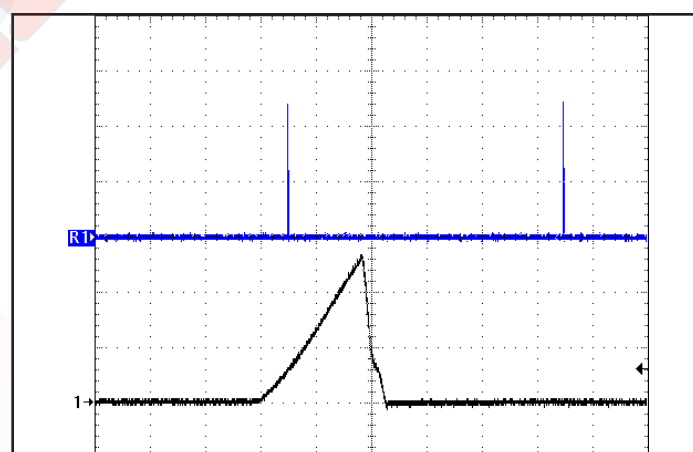

Figure 18: *Load current (5A/div) as a function of time (100us/div and 40ms/div) when the converter attempts to turn on into a 10 mΩ short circuit.*

BASIC OPERATION AND FEATURES

This Mil-COTS converter series uses a two-stage power conversion topology. The first stage is a buck-converter that keeps the output voltage constant over variations in line, load, and temperature. The second stage uses a transformer to provide the functions of input/output isolation and voltage step-up or step-down to achieve the output voltage required.

Both the first stage and the second stage switch at a fixed frequency for predictable EMI performance. Rectification of the transformer's output is accomplished with synchronous rectifiers. These devices, which are MOSFETs with a very low on-state resistance, dissipate far less energy than Schottky diodes. This is the primary reason that the converter has such high efficiency, even at very low output voltages and very high output currents.

These converter are offered totally encased to withstand harsh environments and thermally demanding applications. Dissipation throughout the converter is so low that it does not require a heatsink for operation in many applications; however, adding a heatsink provides improved thermal derating performance in extreme situations.

This series of converters use the industry standard footprint and pin-out configuration.

CONTROL FEATURES

REMOTE ON/OFF (Pin 2): The ON/OFF input, Pin 2, permits the user to control when the converter is on or off. This input is referenced to the return terminal of the input bus, Vin(-). The ON/OFF signal is active low (meaning that a low turns the converter on). Figure A details four possible circuits for driving the ON/OFF pin. Figure B is a detailed look of the internal ON/OFF circuitry.

REMOTE SENSE(±) (Pins 7 and 5): The SENSE(±) inputs correct for voltage drops along the conductors that connect the converter's output pins to the load.

Pin 7 should be connected to Vout(+) and Pin 5 should be connected to Vout(-) at the point on the board where regulation is desired. A remote connection at the load can adjust for a voltage drop only as large as that specified in this datasheet, that is

$$[Vout(+) - Vout(-)] - [Vsense(+) - Vsense(-)] \leq$$
$$\text{Sense Range \% x Vout}$$

Pins 7 and 5 must be connected for proper regulation of the output voltage. If these connections are not made, the converter will deliver an output voltage that is slightly higher than its specified value.

Note: the output over-voltage protection circuit senses the voltage across the output (pins 8 and 4) to determine when it should trigger, not the voltage across the converter's sense leads (pins 7 and 5). Therefore, the resistive drop on the board should be small enough so that output OVP does not trigger, even during load transients.
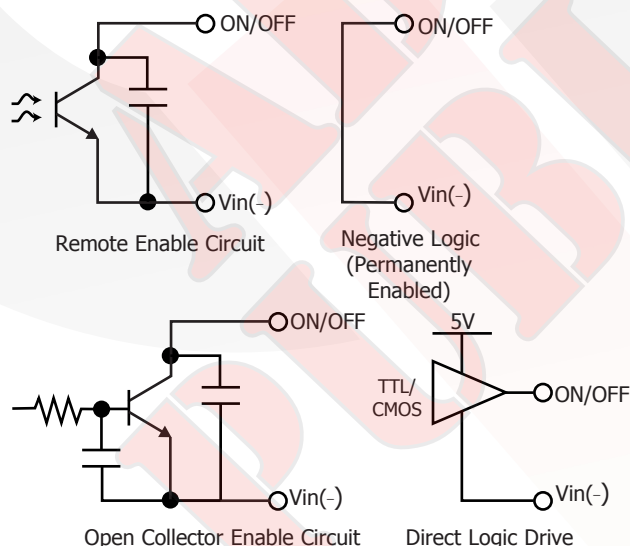

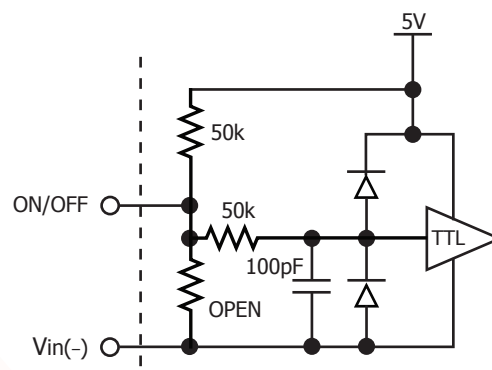*Figure A: Various circuits for driving the ON/OFF pin.*

Figure B: Internal ON/OFF pin circuitry

SynQor®
Technical Specification

OUTPUT VOLTAGE TRIM (Pin 6): The TRIM input permits the user to adjust the output voltage across the sense leads up or down according to the trim range specifications.

To decrease the output voltage, the user should connect a resistor between Pin 6 and Pin 5 (SENSE(-) input). For a desired decrease of the nominal output voltage, the value of the resistor should be

$$R_{trim-down} = \left(\frac{511}{\Delta\%}\right) - 10.22 \ (k\Omega)$$

where

$$\Delta\% = \left| \frac{Vnominal - Vdesired}{Vnominal} \right| \times 100\%$$

To increase the output voltage, the user should connect a resistor between Pin 6 and Pin 7 (SENSE(+) input). For a desired increase of the nominal output voltage, the value of the resistor should be

$$R_{trim-up} = \left(\frac{5.11 V_{OUT} \times (100 + \Delta\%)}{1.225\Delta\%} - \frac{511}{\Delta\%} - 10.22 \right) (k\Omega)$$

where

V_{out} = Nominal Output Voltage

Trim graphs show the relationship between the trim resistor value and Rtrim-up and Rtrim-down, showing the total range the output voltage can be trimmed up or down.

Note: the TRIM feature does not affect the voltage at which the output over-voltage protection circuit is triggered. Trimming the output voltage too high may cause the over-voltage protection circuit to engage, particularly during transients.
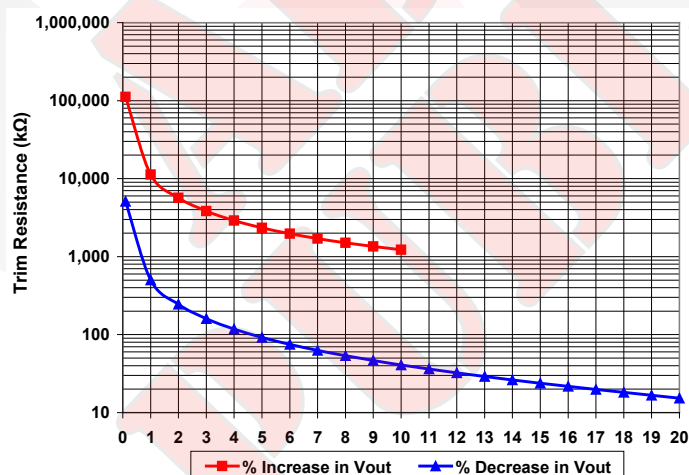

Figure C: Trim graph for trim-up, trim down.

It is not necessary for the user to add capacitance at the Trim pin. The node is internally bypassed to eliminate noise.

Total DC Variation of V$_{OUT}$: For the converter to meet its full specifications, the maximum variation of the dc value of V_{OUT}, due to both trimming and remote load voltage drops, should not be greater than that specified for the output voltage trim range.

PROTECTION FEATURES

Input Under-Voltage Lockout: The converter is designed to turn off when the input voltage is too low, helping avoid an input system instability problem, described in more detail in the application note titled "Input System Instability" on our website. The lockout circuitry is a comparator with dc hysteresis. When the input voltage is rising, it must exceed the typical Turn-On Voltage Threshold value (listed on the specifications page) before the converter will turn on. Once the converter is on, the input voltage must fall below the typical Turn-Off Voltage Threshold value before the converter will turn off.

Output Over-Voltage Limit: If the voltage across the output pins exceeds the Output Over-Voltage Protection threshold, the converter will immediately stop switching. This prevents damage to the load circuit due to 1) excessive series resistance in output current path from converter output pins to sense point, 2) a release of a short-circuit condition, or 3) a release of a current limit condition. Load capacitance determines exactly how high the output voltage will rise in response to these conditions. After 200 ms the converter will automatically restart.

Over-Temperature Shutdown: A temperature sensor on the converter senses the average temperature of the module. The thermal shutdown circuit is designed to turn the converter off when the temperature at the sensed location reaches the Over-Temperature Shutdown value. It will allow the converter to turn on again when the temperature of the sensed location falls by the amount of the Over-Temperature Shutdown Restart Hysteresis value.

Output Current Limit: The maximum current limit remains constant as the output voltage drops. However, once the impedance of the load across the output is small enough to make the output voltage drop below the specified Output DC Current-Limit Shutdown Voltage, the converter turns off.

The converter then enters a "hiccup" mode where it repeatedly turns on and off at a 5 Hz (nominal) frequency with 5% duty cycle until the short circuit condition is removed. This prevents excessive heating of the converter or the load board.

APPLICATION CONSIDERATIONS

Input System Instability: This condition can occur because any dc-dc converter appears incrementally as a negative resistance load. A detailed application note titled "Input System Instability" is available on the SynQor website which provides an understanding of why this instability arises, and shows the preferred solution for correcting it.

Application Circuits: Figure D provides a typical circuit diagram which details the input filtering and voltage trimming.
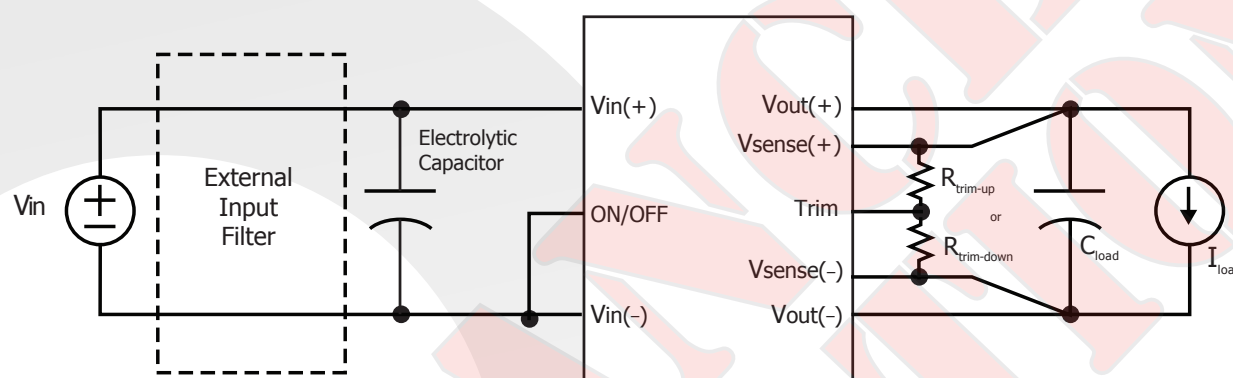
Figure D: Typical application circuit (negative logic unit, permanently enabled).

Input Filtering and External Capacitance: Figure E provides a diagram showing the internal input filter components. This filter dramatically reduces input terminal ripple current, which otherwise could exceed the rating of the converter's external electrolytic input capacitor. The recommended external input capacitance is specified in the Input Characteristics section on the Electrical Characteristics page. More detailed information is available in the application note titled "EMI Characteristics" on the SynQor website.
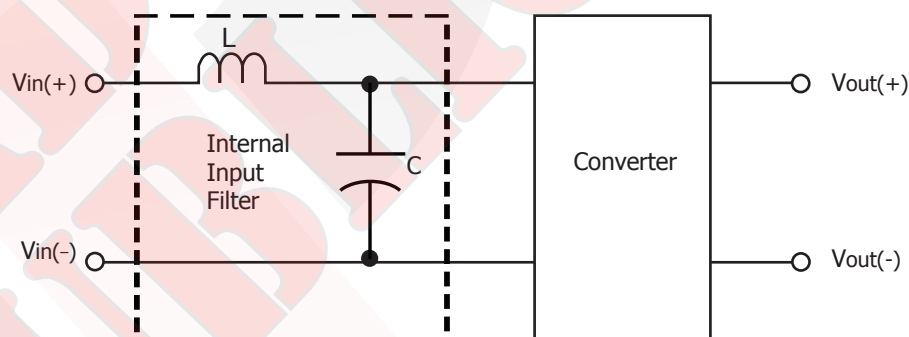
Figure E: Internal Input Filter Diagram

Startup Inhibit Period: The Startup Inhibit Period ensures that the converter will remain off for approximately 200 ms when it is shut down for any reason. When an output short is present, this generates a 5 Hz "hiccup" mode, which prevents the converter from overheating. In all, there are seven ways that the converter can be shut down, initiating a Startup Inhibit Period:

- Input Under-Voltage Lockout
- Input Over-Voltage Lockout
- Output Over-Voltage Protection
- Over Temperature Shutdown
- Current Limit
- Short Circuit Protection
- Turned off by the ON/OFF input

Figure F shows three turn-on scenarios, where a Startup Inhibit Period is initiated at t_0, t_1, and t_2:

Before time t_0, when the input voltage is below the UVL threshold, the unit is disabled by the Input Under-Voltage Lockout feature. When the input voltage rises above the UVL threshold, the Input Under-Voltage lockout is released, and a Startup Inhibit Period is initiated. At the end of this delay, the ON/OFF pin is evaluated, and since it is active, the unit turns on.

At time t_1, the unit is disabled by the ON/OFF pin, and it cannot be enabled again until the Startup Inhibit Period has elapsed.

When the ON/OFF pin goes high after t_2, the Startup Inhibit Period has elapsed, and the output turns on within the typical Turn-On Time.

Thermal Considerations: The maximum operating base-plate temperature, T_B, is 100 ºC. Refer to the thermal derating curve, Figure 5, to see the available output current at baseplate temperatures below 100 ºC.

A power derating curve can be calculated for any heatsink that is attached to the base-plate of the converter. It is only necessary to determine the thermal resistance, $R_{TH_{BA}}$, of the chosen heatsink between the base-plate and the ambient air for a given airflow rate. This information is usually available from the heatsink vendor. The following formula can then be used to determine the maximum power the converter can dissipate for a given thermal condition:

$$P_{diss}^{max} = \frac{T_B - T_A}{R_{TH_{BA}}}$$

This value of power dissipation can then be used in conjunction with the data shown in Figure 3 to determine the maximum load current (and power) that the converter can deliver in the given thermal condition.
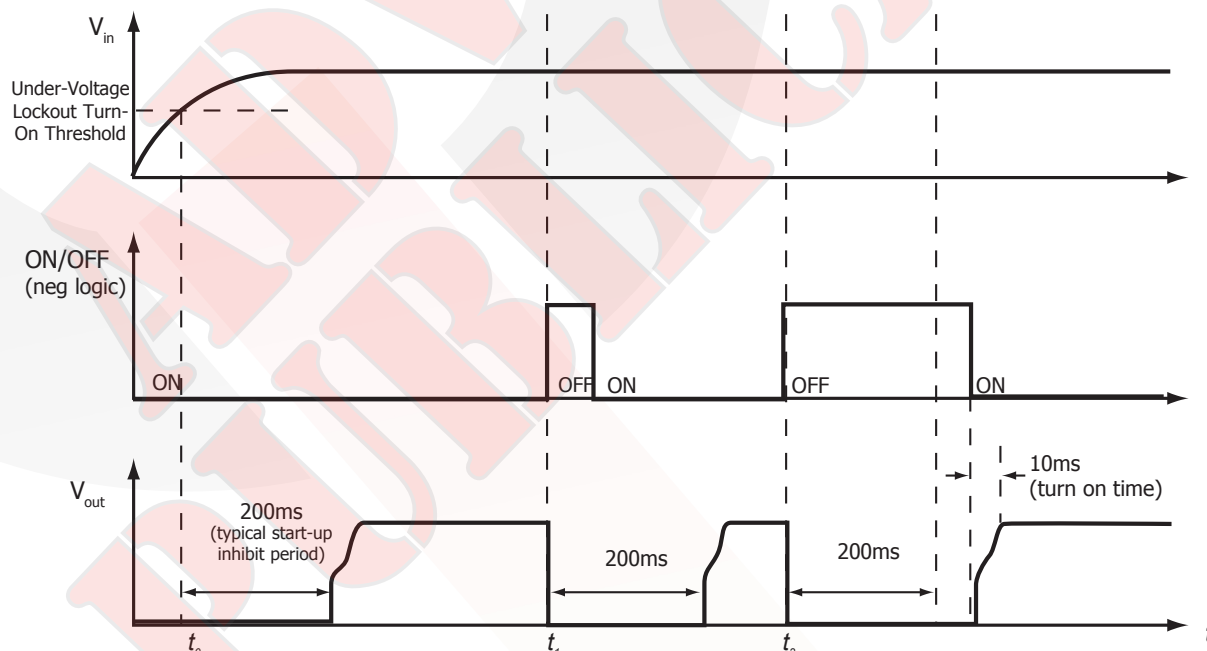

Figure F: *Startup Inhibit Period (turn-on time not to scale)*

FULL FEATURE APPLICATION NOTES

This section provides some baseic application information for the full-feature version of the MCOTS series converter.

All units in this product family include back-drive protection to simplify the user of multiple converters in a parallel or sequencing application. However, any voltage applied to the output of the converter should be kept below 120% of the rated output voltage of the converter.

In addition to back-drive protection, these units include the following features (pins):

Current Share (pin C): The active current share feature allows for N + 1 and parallel applications. To achieve load sharing, directly connect the I share pins of multiple units. The load current will share equally among the multiple units (±5% at full rated current). It is important that the Vin(-) pins of the sharing units be <u>directly</u> connected and NOT placed outside of an EMI filter or other impedance path. The voltage at the I Share pin will range from 0 to 5 volts (at full rated current), referenced to the primary-side ground, Vin(-).
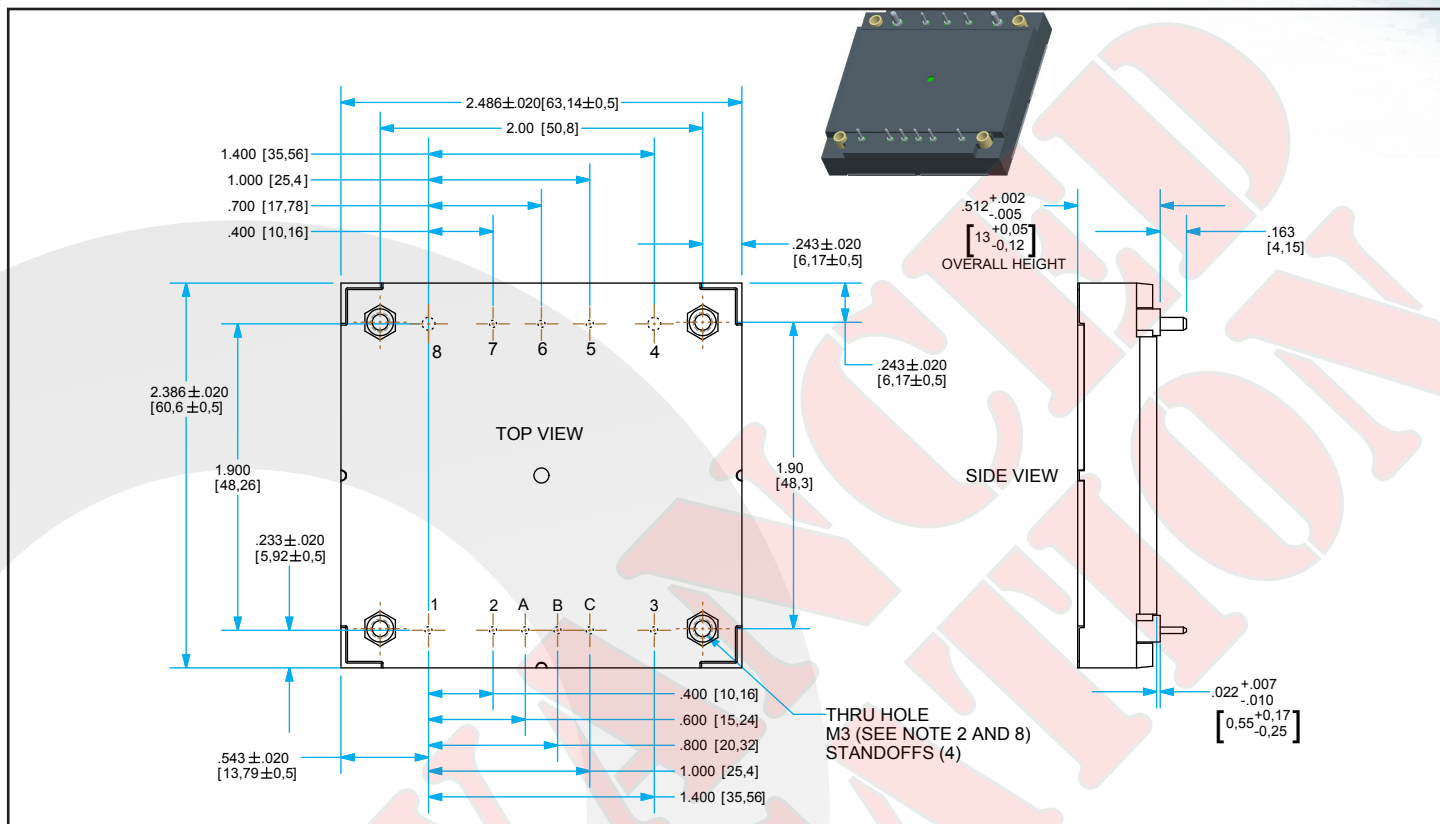
Start Sync (pin B): The Start Synchronization pin will allow a more consistent start-up sequence. To operate this feature, connect together the Start-Sync pins of multiple current-sharing units. This will permit immediate start-up with loads greater than the current limit of a single unit. Without this connection, any set of converters attempting to asynchronously start (or re-start) with a load greater than the current limit of a single unit will "hiccup". This "hiccup" mode will continue until one converter attempts a start at the same time as the minimum number of additional units necessary to sustain the load condition. For example, three 50 amp units starting into a 90 amp load would require two units to simultaneously attempt a start. The Start Sync connection synchronizes these starting attempts and provides a more consistent and reliable start-up sequqnce. For details about the "hiccup" mode ore repeated startup attempts, please see the "Startup Inhibit Period" note in the Technical Specification.

Clock Sync (pin A): The External Clock Synchronization pin provides the ability for the user to control the EMI signature and synchronize sensitive circuitry to quiet periods in the converter operation. With this option, the converter can be synchronized to an external clock signal whose frequency is greater than that of the free-running internal clock. However, substantially raising the converter's frequency will reduce its efficiency. Therefore, the recommended frequency range for the external clock synchronization signal applied to this pin is listed in the Technical Specification.

The following requirements should also be met:

- The external clock signal should be referenced to the negative input voltage, Vin(-).
- The high level of the signal should be between 3.5V and 5.0V.
- The low level should be between -0.5V and +1.2V.
- Do not apply a clock signal lower than the specified frequency.

Mechanical Drawing – Normal Heatsink Option

TOP VIEW

SIDE VIEW

2.486±.020 [63,14±0,5]

2.00 [50,8]

1.400 [35,56]

1.000 [25,4]

.700 [17,78]

.400 [10,16]

.243±.020 [6,17±0,5]

2.386±.020 [60,6±0,5]

1.900 [48,26]

.233±.020 [5,92±0,5]

.543±.020 [13,79±0,5]

.400 [10,16]

.600 [15,24]

.800 [20,32]

1.000 [25,4]

1.400 [35,56]

.243±.020 [6,17±0,5]

1.90 [48,3]

THRU HOLE
M3 (SEE NOTE 2 AND 8)
STANDOFFS (4)

.512 +.002 −.005

$\begin{bmatrix} 13 \,^{+0,05}_{-0,12} \end{bmatrix}$ OVERALL HEIGHT

.163 [4,15]

.022 +.007 −.010

$\begin{bmatrix} 0,55 \,^{+0,17}_{-0,25} \end{bmatrix}$

NOTES

1) Applied torque per screw should not exceed 6in-lb (0.7 Nm).
2) Baseplate flatness tolerance is 0.004" (.10mm) TIR for surface.
3) Pins 1-3, 5-7, A, B, and C are 0.040" (1.02mm) diameter, with 0.080" (2.03mm) diameter standoff shoulders.
4) Pins 4 and 8 are 0.080" (2.03 mm) diameter with 0.125" (3.18 mm) diameter standoff shoulders.
5) All Pins: Material – Copper Alloy; Finish – Matte Tin over Nickel plate
6) Undimensioned components are shown for visual reference only.
7) Weight: 4.9 oz. (139 g) typical
8) Threaded or Non-Threaded options available
9) All dimensions in inches (mm)
   Tolerances: x.xx +/-0.02 in. (x.x +/-0.5mm)
               x.xxx +/-0.010 in. (x.xx +/-0.25mm)
10) Workmanship: Meets or exceeds IPC-A610 Class II
11) Recommended pin length is 0.03" (0.76mm) greater than the PCB thickness.

PIN DESIGNATIONS

Pin	Name	Function
1	Vin(+)	Positive input voltage
2	ON/OFF	TTL input to turn converter on and off, referenced to Vin(−), with internal pull up.
A	CLOCK SYNC	Clock synchronization
B	START SYNC	Startup synchronization
C	I SHARE	Single wire parallel signal
3	IN RTN	Input Return
4	OUT RTN	Output Return
5	SENSE(−)	Negative remote sense[1]
6	TRIM	Output voltage trim[2]
7	SENSE(+)	Positive remote sense[3]
8	Vout(+)	Positive output voltage

Notes:
1) SENSE(−) should be connected to Vout(−) either remotely or at the converter.
2) Leave TRIM pin open for nominal output voltage.
3) SENSE(+) should be connected to Vout(+) either remotely or at the converter.
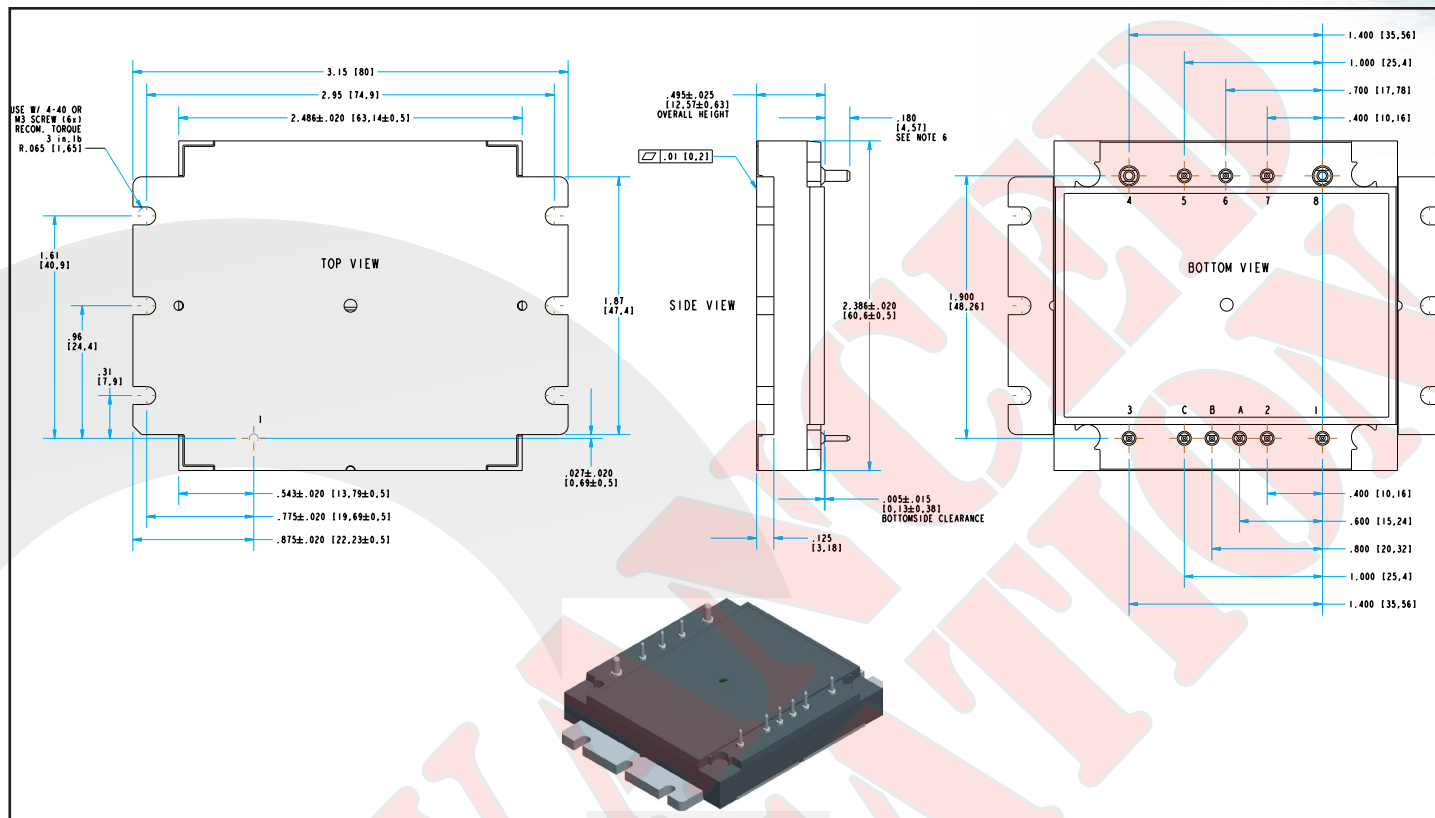4) Pins A, B, and C are populated on Full Feature version only.

SynQor®

Technical Specification

Mechanical Drawing – Flanged Heatsink Option

NOTES

1) Applied torque per screw should not exceed 5in-lb (3in-lb recommended).
2) Baseplate flatness tolerance is 0.01" (.25mm) TIR for surface.
3) Pins 1-3, 5-7, A, B, and C are 0.040" (1.02mm) diameter, with 0.080" (2.03mm) diameter standoff shoulders.
4) Pins 4 and 8 are 0.080" (2.03 mm) diameter with 0.125" (3.18 mm) diameter standoff shoulders.
5) All Pins: Material – Copper Alloy; Finish – Matte Tin over Nickel plate
6) Weight: 4.8 oz. (137 g) typical
7) Undimensioned components are shown for visual reference only.
8) All dimensions in inches (mm)
   Tolerances: x.xx +/-0.02 in. (x.x +/-0.5mm)
              x.xxx +/-0.010 in. (x.xx +/-0.25mm)
9) Workmanship: Meets or exceeds IPC-A610 Class II
10) Recommended pin length is 0.03" (0.76mm) greater than the PCB thickness.
11) A thermal interface material is required to assure proper heat transfer from the flanged baseplate to the cooling surface. Thermal grease, conductive pads, compounds, and other similar products are available from many heatsink manufacturers.

PIN DESIGNATIONS

Pin	Name	Function
1	Vin(+)	Positive input voltage
2	ON/OFF	TTL input to turn converter on and off, referenced to Vin(−), with internal pull up.
A	CLOCK SYNC	Clock synchronization
B	START SYNC	Startup synchronization
C	I SHARE	Single wire parallel signal
3	IN RTN	Input Return
4	OUT RTN	Output Return
5	SENSE(−)	Negative remote sense[1]
6	TRIM	Output voltage trim[2]
7	SENSE(+)	Positive remote sense[3]
8	Vout(+)	Positive output voltage

Notes:
1) SENSE(−) should be connected to Vout(−) either remotely or at the converter.
2) Leave TRIM pin open for nominal output voltage.
3) SENSE(+) should be connected to Vout(+) either remotely or at the converter.
4) Pins A, B, and C are populated on Full Feature version only.

SynQor®
Technical Specification

Mil-COTS Qualification

Test Name	Details	# Tested (# Failed)	Consistent with MIL-STD-883F Method	Consistent with MIL-STD-883F Method 5005
Life Testing	Visual, mechanical and electrical testing before, during and after 1000 hour burn-in @ full load	15 (0)	Method 1005.8	
Shock-Vibration	Visual, mechanical and electrical testing before, during and after shock and vibration tests	5 (0)		MIL-STD-202, Methods 201A & 213B
Humidity	+85°C, 95% RH, 1000 hours, 2 minutes on / 6 hours off	8 (0)	Method 1004.7	
Temperature Cycling	500 cycles of -55°C to +100°C (30 minute dwell at each temperature)	10 (0)	Method 1010.8	Condition A
Solderability	15 pins	15 (0)	Method 2003	
DMT	-65°C to +110°C across full line and load specifications in 5°C steps	7 (0)		
Altitude	70,000 feet (21 km), see Note	2 (0)		

Note: A conductive cooling design is generally needed for high altitude applications because of naturally poor convective cooling at rare atmospheres.

Mil-COTS DC-DC Converter and Filter Screening

Screening	Process Description	S-Grade	M-Grade
Baseplate Operating Temperature		-55°C to +100°C	-55°C to +100°C
Storage Temperature		-65°C to +135°C	-65°C to +135°C
Pre-Cap Inspection	IPC-610, Class III	•	•
Temperature Cycling	Method 1010, Condition B, 10 Cycles		•
Burn-In	100°C Baseplate	12 Hours	96 Hours
Final Electrical Test	100%	25°C	-55°C, +25°C, +100°C
Final Visual Inspection	MIL-STD-2008	•	•

MILCOTS MIL-STD-810G Qualification Testing

MIL-STD-810G Test	Method	Description
Fungus	508.6	Table 508.6-I
Altitude	500.5 - Procedure I	Storage: 70,000ft. / 2 Hr. duration
	500.5 - Procedure II	Operating; 70,000ft. / 2 Hr. duration; Ambient Temperature
Rapid Decompression	500.5 - Procedure III	Storage: 8,000ft. to 40,000ft.
Acceleration	513.6 - Procedure II	Operating - 15g's
Salt Fog	509.5	Storage
High Temperature	501.5 - Procedure I	Storage: 135°C / 3 hrs
	501.5 - Procedure II	Operating: 100°C / 3 hrs
Low Temperature	502.5 - Procedure I	Storage: -65C / 4 hrs
	502.5 - Procedure II	Operating: -55C / 3 hrs
Temperature Shock	503.5 - Procedure I - C	Storage: -65C to 135C; 12 cycles
Rain	506.5 - Procedure I	Wind Blown Rain
Immersion	512.5 - Procedure I	Non-Operating
Humidity	507.5 - Procedure II	Aggravated cycle @ 95% RH (Figure 507.5-7 aggravated temp - humidity cycle, 15 cycles)
Random Vibration	514.6 - Procedure I	10-2000 Hz, PSD level of 1.5 g2/Hz(54.6grms), duration = 1 hr/axis
Shock	516.6 - Procedure I	20g's peak, 11ms, Functional Shock (Operating no load) (saw tooth)
	516.6 - Procedure VI	Bench Handling Shock
Sinusoidal vibration	514.6 - Category 14	Rotary wing aircraft - helicopter, 4hrs/axis, 20g's (sine sweep from 10 - 500HZ)
Sand and Dust	510.5 - Procedure I	Blowing Dust
	510.5 - Procedure II	Blowing Sand

Ordering Information/ Part Numbering

Example MCOTS-C-28VE-28-HP-N-S
Not all combinations make valid part numbers, please contact SynQor for availability.

Family	Product	Input Voltage	Output Voltage	Package	Thermal Design	Screening Level	Options
MCOTS	**C:** Converter	**28VE:** 9-70V	**1R8:** 1.8V **3R3:** 3.3V **05:** 5.0V **7R5:** 7.5V **12:** 12V **15:** 15V **24:** 24V **28:** 28V **40:** 40V **48:** 48V	**HP:** Half Brick Peta	**N:** Normal Threaded **D:** Normal Non-Threaded **F:** Flanged	**S:** S-Grade **M:** M-Grade	**[ ]:** Standard Feature **F:** Full Feature

APPLICATION NOTES

A variety of application notes and technical white papers can be downloaded in PDF format from our website.

PATENTS

SynQor holds the following U.S. patents, one or more of which apply to each product listed in this document. Additional patent applications may be pending or filed in the future.

5,999,417	6,222,742	6,545,890	6,577,109	6,594,159	6,731,520
6,894,468	6,896,526	6,927,987	7,050,309	7,072,190	7,085,146
7,119,524	7,269,034	7,272,021	7,272,023	7,558,083	7,564,702
7,765,687	7,787,261	8,023,290	8,149,597	8,493,751	

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