Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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DATA SHEET

PHOTOCOUPLER PS2561D-1,PS2561DL-1 PS2561DL1-1,PS2561DL2-1

DIP PHOTOCOUPLER OPERATING AMBIENT TEMPERATURE 110°C -NEPOC Series-

DESCRIPTION

The PS2561D-1 is an optically coupled isolator containing a GaAs light emitting diode and an NPN silicon phototransistor.

The PS2561D-1 is in a plastic DIP (Dual In-line Package) and the PS2561DL-1 is lead bending type (Gull-wing) for surface mount.

The PS2561DL1-1 is lead bending type for long creepage distance.

The PS2561DL2-1 is lead bending type for long creepage distance (Gull-wing) for surface mount.

FEATURES

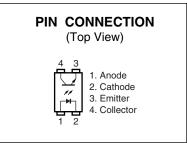
- Operating ambient temperature: 110°C
- High Isolation voltage (BV = 5 000 Vr.m.s.)
- High collector to emitter voltage (VCEO = 80 V)
- High current transfer ratio (CTR = 160% TYP.)
- High-speed switching (tr = 3 μ s TYP., tr = 5 μ s TYP.)
- Ordering number of taping product: PS2561DL-1-F3 : 2 000 pcs/reel

: PS2561DL2-1-E3: 1 000 pcs/reel

- Pb-Free product
- <R> Safety standards
 - UL approved: No. E72422
 - CSA approved: No. CA 101391 (CA5A, CAN/CSA-C22.2 60065, 60950)
 - BSI approved: No. 7112/7420
 - SEMKO approved: No. 903238
 - NEMKO approved: No. P09210868
 - DEMKO approved: No. 314999
 - FIMKO approved: No. FI 25119
 - DIN EN60747-5-2 (VDE0884 Part2) approved: No. 40008862 (Option)

APPLICATIONS

- Power supply
- Telephone/FAX.
- FA/OA equipment
- Programmable logic controller



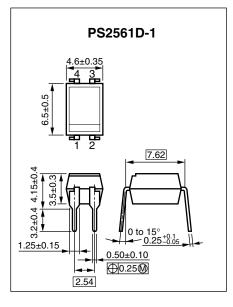
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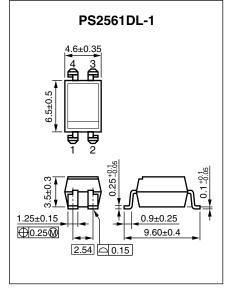
The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

PACKAGE DIMENSIONS (UNIT : mm)

DIP Type



<R> Lead Bending Type



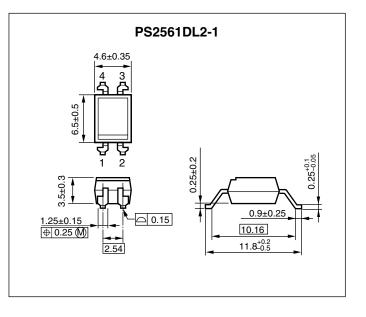
PHOTOCOUPLER CONSTRUCTION

PS2561DL1-1
4.6±0.35 4 3 G G G G G G G G

Long Creepage Distance (Gull-Wing)

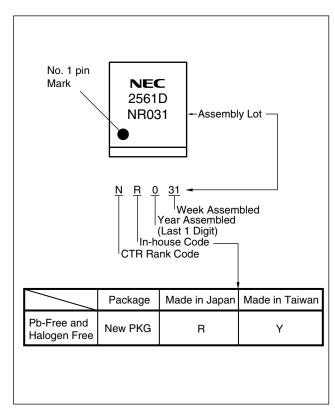
Long Creepage Distance

Г



Parameter	PS2561D-1, PS2561DL-1	PS2561DL1-1, PS2561DL2-1
Air Distance (MIN.)	7 mm	8 mm
Outer Creepage Distance (MIN.)	7 mm	8 mm
Inner Creepage Distance (MIN.)	4 mm	4 mm
Isolation Distance (MIN.)	0.4 mm	0.4 mm

<R> MARKING EXAMPLE



Part Number	Order Number	Solder Plating Specification	Packing Style	Safety Standard Approval	Application Part Number ^{*1}
PS2561D-1	PS2561D-1Y-A	Special version	Magazine case 100 pcs	Standard products	PS2561D-1
PS2561DL-1	PS2561DL-1Y-A	(Pb-Free and		(UL, CSA, BSI,	PS2561DL-1
PS2561DL1-1	PS2561DL1-1Y-A	Halogen Free)		NEMKO, DEMKO,	PS2561DL1-1
PS2561DL2-1	PS2561DL2-1Y-A			SEMKO, FIMKO	PS2561DL2-1
PS2561DL-1-F3	PS2561DL-1Y-F3-A		Embossed Tape 2 000 pcs/reel	approved)	PS2561DL-1
PS2561DL2-1-E3	PS2561DL2-1Y-E3-A		Embossed Tape 1 000 pcs/reel		PS2561DL2-1
PS2561D-1-V	PS2561D-1Y-V-A		Magazine case 100 pcs	DIN EN60747-5-2	PS2561D-1
PS2561DL-1-V	PS2561DL-1Y-V-A			(VDE0884 Part2)	PS2561DL-1
PS2561DL1-1-V	PS2561DL1-1Y-V-A			approved (Option)	PS2561DL1-1
PS2561DL2-1-V	PS2561DL2-1Y-V-A				PS2561DL2-1
PS2561DL-1-V-F3	PS2561DL-1Y-V-F3-A		Embossed Tape 2 000 pcs/reel		PS2561DL-1
PS2561DL2-1-V-E3	PS2561DL2-1Y-V-E3-A		Embossed Tape 1 000 pcs/reel		PS2561DL2-1

<R> ORDERING INFORMATION

*1 For the application of the Safety Standard, following part number should be used.

Parameter		Symbol	Ratings	Unit
Diode	Reverse Voltage	VR	6	V
	Forward Current (DC)	lf	40	mA
	Power Dissipation Derating	⊿Po/°C	1.5	mW/°C
	Power Dissipation	PD	150	mW
	Peak Forward Current ¹	IFP	1	А
Transistor	Collector to Emitter Voltage	VCEO	80	V
	Emitter to Collector Voltage	VECO	7	V
	Collector Current	lc	50	mA
	Power Dissipation Derating	⊿Pc/°C	1.5	mW/°C
	Power Dissipation	Pc	150	mW
Isolation Voltage ²		BV	5 000	Vr.m.s.
Operating A	Operating Ambient Temperature		–55 to +110	°C
Storage Temperature		Tstg	–55 to +150	°C

ABSOLUTE MAXIMUM RATINGS (TA = 25°C, unless otherwise specified)

*1 PW = 100 μ s, Duty Cycle = 1%

*2 AC voltage for 1 minute at $T_A = 25^{\circ}$ C, RH = 60% between input and output. Pins 1-2 shorted together, 3-4 shorted together.

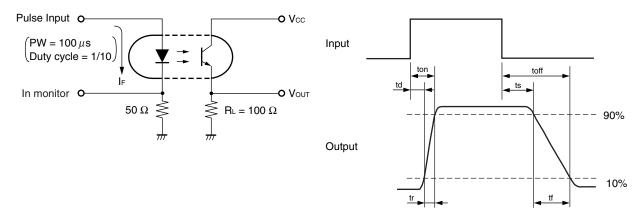
	Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Diode	Forward Voltage	VF	IF = 10 mA		1.2	1.4	V
	Reverse Current	IR	V _R = 5 V			5	μA
	Terminal Capacitance	Ct	V = 0 V, f = 1.0 MHz		10		pF
Transistor	Collector to Emitter Dark Current	Iceo	$V_{CE} = 48 \text{ V}, \text{ IF} = 0 \text{ mA}$			100	nA
Coupled	Current Transfer Ratio	CTR	$I_F = 5 \text{ mA}, V_{CE} = 5 \text{ V}$	50	160	400	%
	(Ic/IF) ^{*1}		IF = 1 mA, VCE = 5 V	10	80		
	Collector Saturation Voltage	VCE (sat)	I⊧ = 10 mA, Ic = 2 mA			0.3	V
	Isolation Resistance	Ri-o	VI-O = 1.0 kVDC	10 ¹¹			Ω
	Isolation Capacitance	Ci-o	V = 0 V, f = 1.0 MHz		0.5		pF
	Rise Time ^{⁺₂}	tr	V_{CC} = 10 V, Ic = 2 mA, RL = 100 Ω		3		μs
	Fall Time ^{'2}	tr			5		

ELECTRICAL CHARACTERISTICS (TA = 25°C)

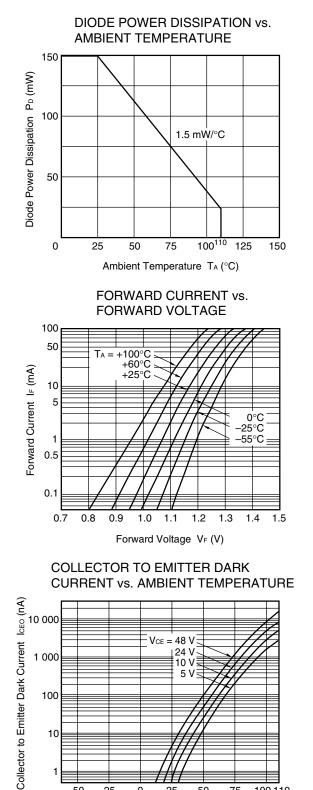
*1 CTR rank

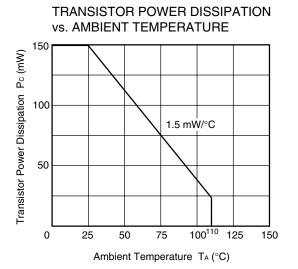
CTR Rank	CTR (%)	Conditions
н	80 to 160	IF = 5 mA, VCE = 5 V
п	16 and larger	$I_F = 1 \text{ mA}, V_{CE} = 5 \text{ V}$
0	100 to 200	IF = 5 mA, VCE = 5 V
Q	20 and larger	$I_F = 1 \text{ mA}, V_{CE} = 5 \text{ V}$
w	130 to 260	$I_F = 5 \text{ mA}, V_{CE} = 5 \text{ V}$
	26 and larger	$I_F = 1 \text{ mA}, V_{CE} = 5 \text{ V}$
1	200 to 400	$I_F = 5 \text{ mA}, V_{CE} = 5 \text{ V}$
L	40 and larger	$I_F = 1 \text{ mA}, V_{CE} = 5 \text{ V}$
N	50 to 400	$I_F = 5 \text{ mA}, V_{CE} = 5 \text{ V}$
	10 and larger	$I_F = 1 \text{ mA}, V_{CE} = 5 \text{ V}$

<R> *2 Test circuit for switching time

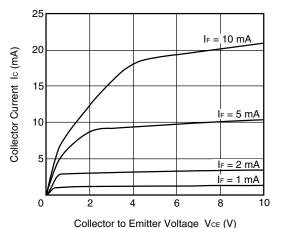


<R> TYPICAL CHARACTERISTICS (TA = 25°C, unless otherwise specified)

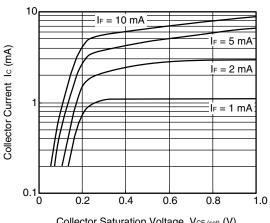




COLLECTOR CURRENT vs. COLLECTOR TO EMITTER VOLTAGE



COLLECTOR CURRENT vs. COLLECTOR SATURATION VOLTAGE



Collector Saturation Voltage VCE (sat) (V)



25 Ambient Temperature TA (°C)

50

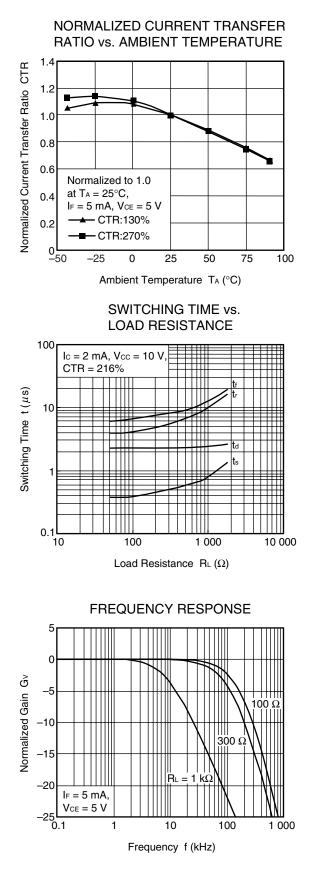
75

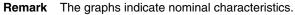
-25

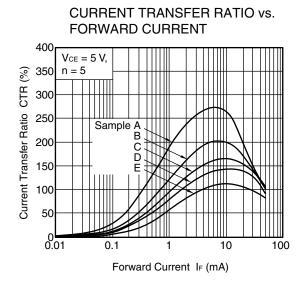
0

-50

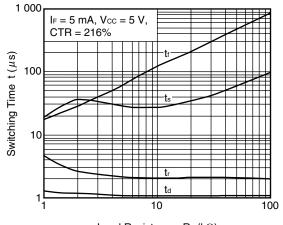
100 1 10





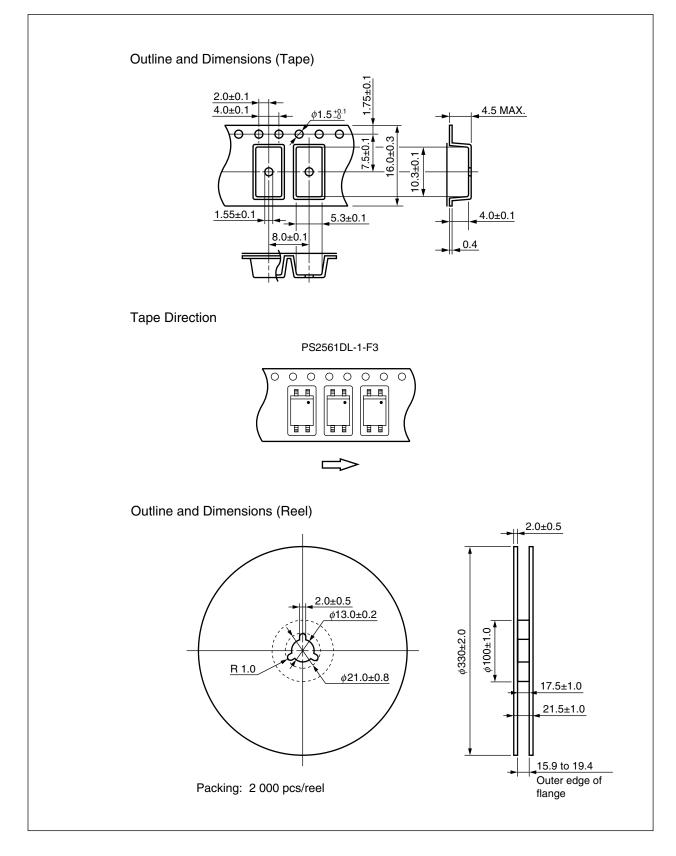


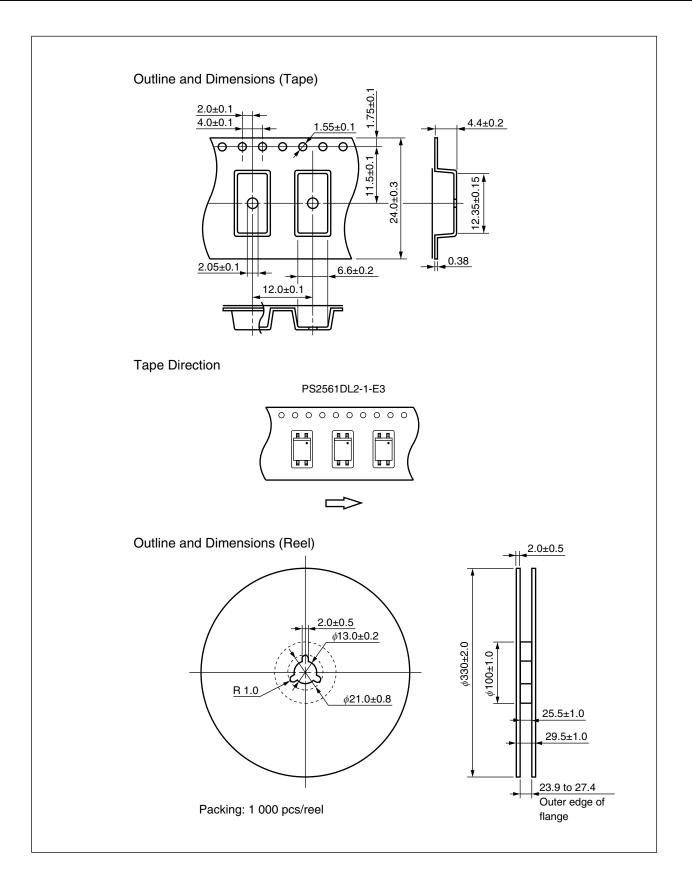
SWITCHING TIME vs. LOAD RESISTANCE



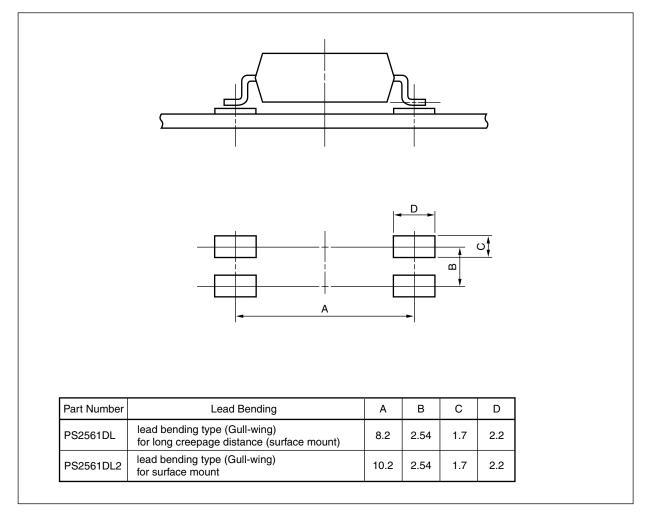
Load Resistance RL (kΩ)

TAPING SPECIFICATIONS (UNIT : mm)





RECOMMENDED MOUNT PAD DIMENSIONS (UNIT: mm)



NOTES ON HANDLING

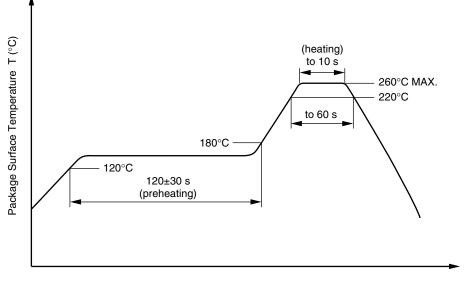
1. Recommended soldering conditions

(1) Infrared reflow soldering

- Peak reflow temperature
- Time of peak reflow temperature
- Time of temperature higher than 220°C
- Time to preheat temperature from 120 to 180°C
- Number of reflows
- Flux

260°C or below (package surface temperature) 10 seconds or less 60 seconds or less 120±30 s Three Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

Recommended Temperature Profile of Infrared Reflow



Time (s)

(2) Wave soldering

- Temperature 260°C or below (molten solder temperature)
- Time 10 seconds or less
- Preheating conditions 120°C or below (package surface temperature)
- Number of times One (Allowed to be dipped in solder including plastic mold portion.)
- Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

(3) Soldering by soldering iron

Peak temperature (lead part temperature)	350°C or below
 Time (each pins) 	3 seconds or less
• Flux	Rosin flux containing small amount of chlorine (The flux with a
	maximum chlorine content of 0.2 Wt% is recommended.)

- (a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead.
- (b) Please be sure that the temperature of the package would not be heated over 100°C.

(4) Cautions

Fluxes

Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output or between corrector-emitters at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

3. Measurement conditions of current transfer ratios (CTR), which differ according to photocoupler

Check the setting values before use, since the forward current conditions at CTR measurement differ according to product.

When using products other than at the specified forward current, the characteristics curves may differ from the standard curves due to CTR value variations or the like. Therefore, check the characteristics under the actual operating conditions and thoroughly take variations or the like into consideration before use.

USAGE CAUTIONS

- 1. Protect against static electricity when handling.
- 2. Avoid storage at a high temperature and high humidity.

<R> SPECIFICATION OF VDE MARKS LICENSE DOCUMENT (1/2) (PS2561D-1, PS2561DL-1)

Parameter	Symbol	Spec.	Unit
Climatic test class (IEC 60068-1/DIN EN 60068-1)		55/110/21	
Dielectric strength maximum operating isolation voltage Test voltage (partial discharge test, procedure a for type test and random test) $U_{pr} = 1.5 \times U_{IORM}, P_d < 5 pC$	Uiorm Upr	890 1 335	V _{peak} V _{peak}
Test voltage (partial discharge test, procedure b for all devices) U_{pr} = 1.875 × U _{IORM} , Pd < 5 pC	Upr	1 669	Vpeak
Highest permissible overvoltage	Utr	8 000	Vpeak
Degree of pollution (DIN EN 60664-1 VDE0110 Part 1)		2	
Comparative tracking index (IEC 60112/DIN EN 60112 (VDE 0303 Part 11))	CTI	175	
Material group (DIN EN 60664-1 VDE0110 Part 1)		lll a	
Storage temperature range	Tstg	-55 to +150	°C
Operating temperature range	TA	-55 to +110	°C
Isolation resistance, minimum value $V_{IO} = 500 \text{ V dc at } T_A = 25^{\circ}\text{C}$ $V_{IO} = 500 \text{ V dc at } T_A MAX. at least 100^{\circ}\text{C}$	Ris MIN. Ris MIN.	10 ¹² 10 ¹¹	Ω Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal derating curve) Package temperature Current (input current IF, Psi = 0) Power (output or total power dissipation) Isolation resistance	Tsi Isi Psi	175 400 700	°C mA mW
V _{IO} = 500 V dc at T _A = Tsi	Ris MIN.	10 [°]	Ω

<R> SPECIFICATION OF VDE MARKS LICENSE DOCUMENT (2/2) (PS2561DL1-1, PS2561DL2-1)

Parameter	Symbol	Spec.	Unit
Climatic test class (IEC 60068-1/DIN EN 60068-1)		55/110/21	
Dielectric strength maximum operating isolation voltage Test voltage (partial discharge test, procedure a for type test and random test) $U_{pr} = 1.5 \times U_{IORM}, P_d < 5 \text{ pC}$	Uiorm Upr	1 130 1 695	V _{peak} V _{peak}
Test voltage (partial discharge test, procedure b for all devices) U_{pr} = 1.875 \times U_{IORM}, P_{d} < 5 pC	Upr	2 119	V _{peak}
Highest permissible overvoltage	Utr	8 000	Vpeak
Degree of pollution (DIN EN 60664-1 VDE0110 Part 1)		2	
Comparative tracking index (IEC 60112/DIN EN 60112 (VDE 0303 Part 11))	CTI	175	
Material group (DIN EN 60664-1 VDE0110 Part 1)		III a	
Storage temperature range	Tstg	-55 to +150	°C
Operating temperature range	TA	-55 to +110	°C
Isolation resistance, minimum value $V_{IO} = 500 \text{ V dc at } T_A = 25^{\circ}\text{C}$ $V_{IO} = 500 \text{ V dc at } T_A \text{ MAX. at least } 100^{\circ}\text{C}$	Ris MIN. Ris MIN.	10 ¹² 10 ¹¹	Ω Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal derating curve) Package temperature Current (input current IF, Psi = 0) Power (output or total power dissipation) Isolation resistance	Tsi Isi Psi	175 400 700	°C mA mW
V _{IO} = 500 V dc at T _A = Tsi	Ris MIN.	10 [°]	Ω

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	 Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
	2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
	• Do not burn, destroy, cut, crush, or chemically dissolve the product.
	• Do not lick the product or in any way allow it to enter the mouth.