

ABA3116 1.2 GHz Balanced Low Noise **Linear Amplifier**

ADVANCED PRODUCT INFORMATION - Rev 0.0

FEATURES

- 15 dB Gain
- +5 V or +8 V Nominal Supply Voltage
- **High Linearity**
- Low Noise Figure: 2.7 dB (typ.)
- Characterized at +25 dBmV output power
- Wide Band operation 1.2 GHz
- -40 to +85 °C
- RoHS-Compliant Package

APPLICATIONS

- **Driver Amplifier**
- CATV Distribution / Drop Amplifiers
- Set Top Boxes
- Home Gateway

PRODUCT DESCRIPTION

The ABA3116 is a monolithic IC intended for use in applications requiring high linearity, such as Cellular Telephone Base Station Driver Amplifiers, CATV Fiber Receiver and Distribution Amplifiers, CATV Drop Amplifiers, CATV Set Top Boxes and Home Gateways.



Offered in a modified 16 lead surface mount SOIC package with heat slink slug that is RoHS-Compliant, it is well suited for use in amplifiers where small size, reduced component count, and high reliability are important.

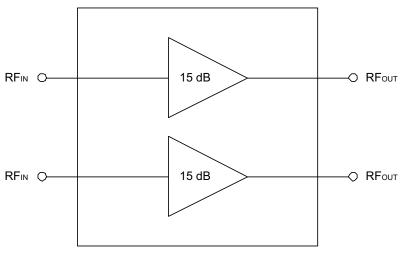


Figure 1: Block Diagram

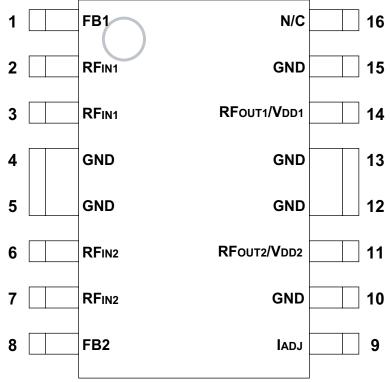


Figure 2: Pinout Diagram

Table 1: Pin Description

PIN	NAME	DESCRIPTION	PIN	NAME	DESCRIPTION
1	FB1	Feedback for Amplifier A1	9	ladj	Current Adjust
2	RF _{IN1}	RF Input of Amplifier A1	10	GND	Ground
3	RF _{IN1}	RF Input of Amplifier A1	11	RFOUT2/VDD2	RF Output and VDD of Amplifier A2
4	GND	Ground	12	GND	Ground
5	GND	Ground	13	GND	Ground
6	RF _{IN2}	RF Input of Amplifier A2	14	RFout1/VDD1	RF Output and VDD of Amplifier A1
7	RF _{IN2}	RF Input of Amplifier A2	15	GND	Ground
8	FB2	Feedback for Amplifier A2	16	N/C	No Connect

ELECTRICAL CHARACTERISTICS

Table 2: Absolute Minimum and Maximum Ratings

PARAMETER	MIN	MAX	UNIT
Analog Supply (pins 11, 14)	0	+12	VDC
RF Power at Inputs (pins 2, 3, 6, 7)	-	+10	dBm
Storage Temperature	-65	+150	°C
Soldering Temperature	-	260	°C
Soldering Time	-	5	sec

Stresses in excess of the absolute ratings may cause permanent damage. Functional operation is not implied under these conditions. Exposure to absolute ratings for extended periods of time may adversely affect reliability. *Notes*:

Table 3: Operating Ranges

PARAMETER	MIN	TYP	MAX	UNIT
RF Input / Output Frequency	50	-	1218	MHz
Analog Supply: VDD (pins 11, 14)	+4.5	+5	+10	VDC
Case Temperature: TA	-40	-	+85	°C

The device may be operated safely over these conditions; however, parametric performance is guaranteed only over the conditions defined in the electrical specifications.

⁽¹⁾ Pins 1, 2, 3, 6, 7, and 8 should be AC-coupled. No external DC bias should be applied.

⁽²⁾ Pin 9 should be AC-grounded. No external DC bias should be applied.

Table 4: Electrical Specifications (TA = +25 °C, V_{DD} = + 5 VDC, V_{LC} = +5 V, Test System = 75 Ω)

PARAMETER	MIN	TYP	MAX	UNIT	COMMENTS
Gain	14	15	17	dB	
Noise Figure	1 1 1 1	2.5 2.7 3.2 TBD	3.0 3.5 4.0 4.0	dB	50 - 350 MHz 350 - 550 MHz 550 - 860 MHz 860 - 1218 MHz
Input / Output Return Loss	10	18	-	dB	
CSO (1)	-	-72	-70	dBc	
CTB (1)	-	-75	-73	dBc	
2nd Order Output Intercept Point (OIP2) (2)	1	+60	1	dBm	
3rd Order Output Intercept Point (OIP3) (2)	1	+35	1	dBm	
Thermal Resistance	-	-	33	°C/W	
Current Consumption (3), (4)	120	150	170	mA	

Notes:

^{(1) 132} channels, +25 dBmV per channel (measured at the output), 6 MHz channel spacing.

⁽²⁾ Two tones: 397 MHz and 403 MHz, +4 dBm per tone.

⁽³⁾ Characterized with IADJ pin floating.

⁽⁴⁾ Adding a shunt resistor from the IADJ pin to ground will lower current but with some decrease in performance.

APPLICATION INFORMATION

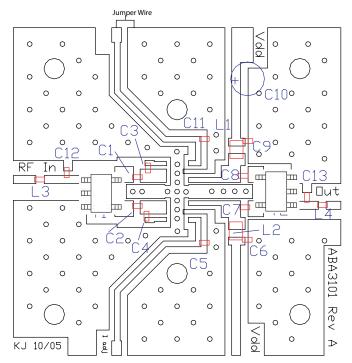
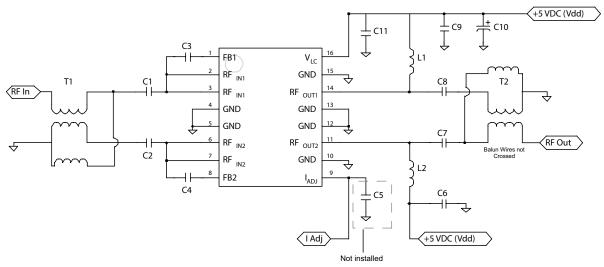


Figure 3: Evaluation Board Layout



Note:

Figure 4: 1.2 GHz Evaluation Board Schematic

^{*} Connecting a resistor from IADJ to ground lowers current; Normal operation is with IADJ floating.

Table 4: Evaluation Board Parts List

ITEM	DESCRIPTION	QTY	VENDOR	VENDOR PART NUMBER
C1-C4, C6, C9, C11	0.01uF CHIP CAP.	7	MURATA	GRM39X7R1103K25V
C7, C8	470 pF CHIP CAP.	2	MURATA	GRM39COG471J25V
C10	47 uF ELEC. CAP.	1	DIGI-KEY CORP	P5275-ND
C5	(not installed)			
L1, L2	390 nH CHIP INDUCTOR	2	COILCRAFT	1008CS-391XKBC
CONNECTORS (1)	75 Ω N MALE PANEL MOUNT	2	PASTERNACK ENTERPRISES	PE4504
T1, T2 ⁽²⁾ (BALUN)	1:1 Balun	2	IND. MACOM	MABA-009210-CT1760
	PCB	1	STAND. PRINTED CIRCUITS INC.	EBSTA-111899

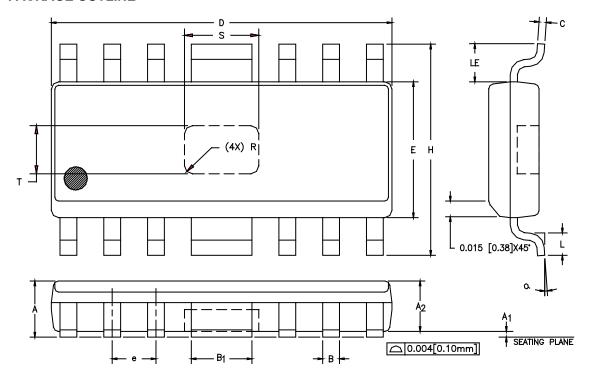
Notes:

^{(1) &}quot;N" Connector center pin should be approximately 80 mils in length.

⁽²⁾ Connector tabs must be reduced by 150 mils.

⁽³⁾ Device must be soldered on PC board.

PACKAGE OUTLINE



SYMBOL	INC	HES	MILLIMETERS		NOTE
_مر	MIN.	MAX.	MIN.	MAX.	.,
Α	0.058	0.068	1.47	1.73	
A ₁	0.000	0.004	0.00	0.10	7
A ₂	0.054	0.065	1.37	1.65	
В	0.013	0.020	0.33	0.50	
B ₁	0.062	0.070	1.58	1.78	
С	0.008	0.010	0.20	0.25	4
D	0.380	0.400	9.66	10.16	2
E	0.150	0.160	3.81	4.06	3
е	0.050 BSC		1.27 BSC		
Н	0.226	0.244	5.74	6.20	
L	0.016	0.040	0.41	1.02	
LE	0.030		0.76		
a	0,	8*	0,	8*	
S	0.070	0.100	1.78	2.54	6
Т	0.040	0.070	1.02	1.78	6
R	0.015 REF.		0.38 REF.		6

NOTES:

- 1. CONTROLLING DIMENSION: INCHES
- 2. DIMENSION "D" DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH, PROTRUSIONS AND GATE BURRS SHALL NOT EXCEED 0.006 [0.15mm] PER SIDE.
- 3. DIMENSION "E" DOES NOT INCLUDE INTER-LEAD FLASH OR PROTRUSIONS. INTER-LEAD FLASH AND PROTRUSIONS SHALL NOT EXCEED 0.010 [0.25mm] PER SIDE.
- 4. MAXIMUM LEAD TWIST/SKEW TO BE ±0.005 [0.13mm].
- 5. LEAD THICKNESS AFTER PLATING TO BE 0.013 [0.33mm] MAXIMUM.
- 6. DIMENSIONS "S", "T" AND "R" INDICATE EXPOSED SLUG AREA.
- 7. A1 STAND OFF IS MEASURED FROM BOTTOM OF HEAT SLUG TO THE SEATING PLANE.

Figure 5: Package Outline - 8 Pin SOIC with Heat Slug

ABA3116

ORDERING INFORMATION

ORDER NUMBER TEMPERATURE RANGE		PACKAGE DESCRIPTION	COMPONENT PACKAGING	
ABA3116	-40 to +85 °C	RoHS-Compliant Modified 16 Pin SOIC	3,500 piece Tape and Reel	
ABA3116	-40 to +85 °C	RoHS-Compliant Modified 16 Pin SOIC	Plastic tubes (50 pieces per tube)	



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