

FEATURES

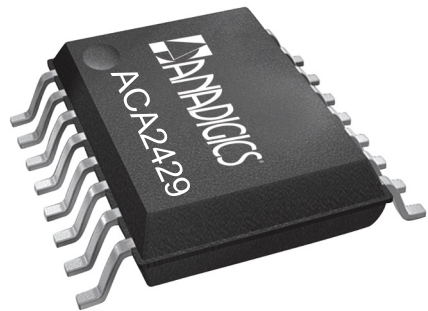
- 50 to 1218 MHz Frequency Range
- +59 dBmV output power
- 25 dB Gain @ 1.218 GHz
- 1 dB tilt over bandwidth
- Single +24 V Supply
- Operating Current = 430 mA
- GaN Amplifier Technology
- Halogen Free/RoHS Compliant

APPLICATIONS

- Advanced high power, high frequency HFC transmission systems
- Output power doubler for Deep Fiber Node in CATV distribution
- CATV digital edge QAM output driver

PRODUCT DESCRIPTION

The ACA2429 is a highly linear, high output power integrated RF amplifier designed for CATV head ends and HFC distribution systems. The IC consists of two parallel amplifiers that are optimized for exceptionally low distortion, high output power, and high crash point in a thermally enhanced surface mount package. A GaN output stage is incorporated to minimize the operating (bias) current, thus making this an excellent choice for environmentally friendly "green" initiatives.



**16 Pin Wide Body SOIC
with Heat Slug**

The ACA2429 provides high gain over the 50 to 1218 MHz CATV downstream band, and is cascaded between two transmission line baluns.

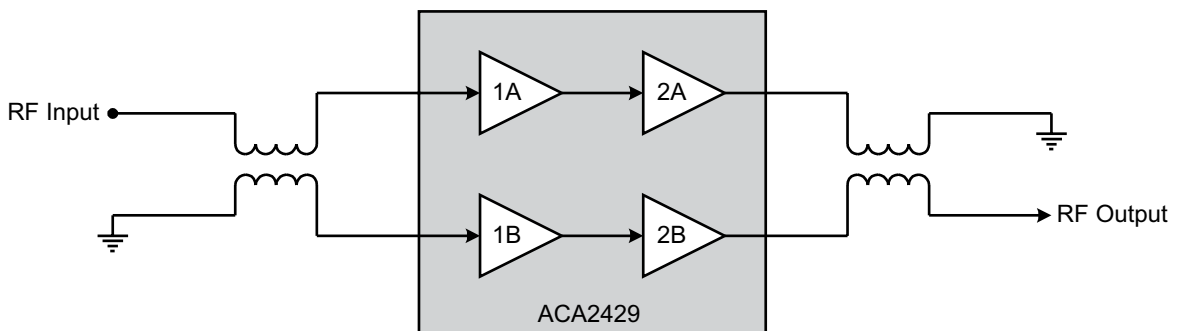


Figure 1: Block Diagram

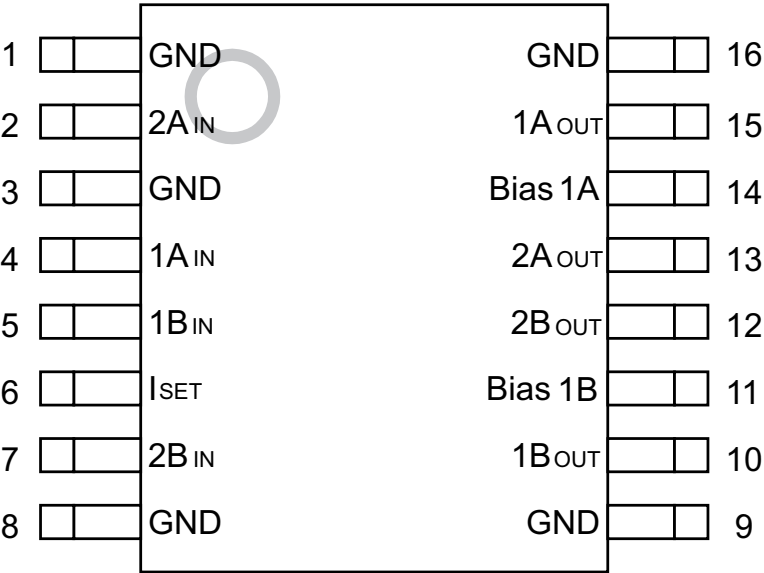


Figure 2: Pinout

Table 1: Pin Description

PIN	NAME	DESCRIPTION	PIN	NAME	DESCRIPTION
1	GND	Ground	9	GND	Ground
2	2A _{IN}	Amplifier 2A Input	10	1B _{OUT}	Amplifier 1B Output
3	GND	Ground	11	Bias 1B	Bias for 1B Amplifier
4	1A _{IN}	Amplifier 1A Input	12	2B _{OUT}	Amplifier 2B Output and Supply
5	1B _{IN}	Amplifier 1B Input	13	2A _{OUT}	Amplifier 2A Output and Supply
6	I _{SET}	Current Adjust	14	Bias 1A	Bias for 1A Amplifier
7	2B _{IN}	Amplifier 2B Input	15	1A _{OUT}	Amplifier 1A Output
8	GND	Ground	16	GND	Ground

ELECTRICAL CHARACTERISTICS

Table 2: Absolute Minimum and Maximum Ratings

PARAMETER	MIN	MAX	UNIT
Supply (pins 12, 13)	0	+28	V _{DC}
Current Adjust (pin 6)	-	+4	V _{DC}
RF Power at Inputs (pins 4, 5)	-	+75	dBmV
Case Operating Temperature Range (T _C)	-40	+110	°C
Storage Temperature	-65	+150	°C
Soldering Temperature	-	+260	°C
Soldering Time	-	5.0	Sec

Stresses in excess of the absolute ratings may cause permanent damage. Functional operation is not implied under these conditions. Exposure to absolute ratings for extended periods of time may adversely affect reliability.

Notes:

- (1) Pins 2, 4, 5 and 7 should be AC-coupled. No external DC bias should be applied.
- (2) Pin 6 should be AC-grounded and/or pulled to ground through a resistor for current control.
- (3) Pins 10, 11, 14 and 15 should have no other external bias applied.

Table 3: Operating Ranges

PARAMETER	MIN	TYP	MAX	UNIT
RF Input/Output Frequency (f)	50	-	1218	MHz
Supply Voltage (V _{DD})	-	+24	-	V _{DC}
Current Adjust (pin 15)	-	+1.5	-	V _{DC}
Case Temperature (T _C)	-40	-	+100	°C

The device may be operated safely over these conditions; however, parametric performance is guaranteed only over the conditions defined in the electrical specifications.

Table 4: Electrical Specifications (See Notes 1, 3, & 4)
(T_A = +25 °C, V_{DD} = +24 VDC, f = 50 to 1218 MHz, 75 Ω system)

PARAMETER	MIN	TYP	MAX	UNIT	COMMENTS
Gain (1218 MHz)	24.5	25.25	26	dB	f = 1218 MHz, See note 3
Cable Equivalent Slope	-	1	-	dB	See note 3
Gain Flatness to 1218 MHz	-	± 0.2	-	dB	See notes 2 & 3
Noise Figure	-	5	-	dB	
CTB	-	-78	-74	dBc	See note 1
CSO	-	-66	-64	dBc	See note 1
XMOD	-	-65	-60	dBc	See note 1
CIN	-	-60	-56	dBc	See note 1
Input Return Loss	-	-20	-18	dB	See note 3
Output Return Loss	-	-20	-18	dB	See note 3
Thermal resistance (θ _{JC})	-	2.7	3.3	°C/W	Junction to case slug
Supply Current	400	430	470	mA	

Notes:

- (1) Parts measured with 79 NTSC analog channels plus digital QAM channels to 1000 MHz, +58 dBmV output power and 15.5 dB tilt referenced to 1 GHz.
- (2) Peak deviation from a straight line between gain value at 50 MHz and 1218 MHz.
- (3) Measured in application circuit in Figure 3.
- (4) All specifications as measured on ANADIGICS evaluation board.

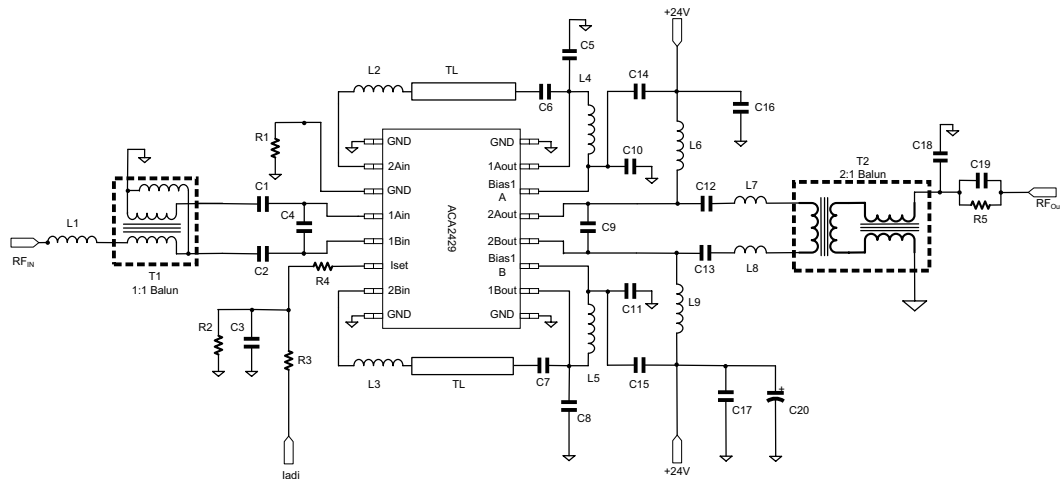
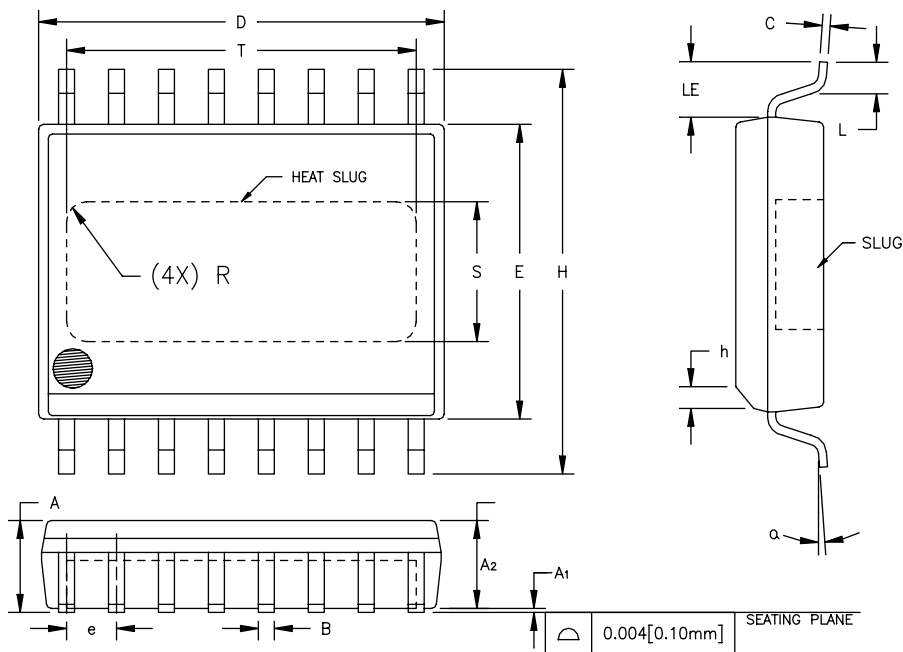


Figure 3: Application Circuit Diagram

Table 5: Component Descriptions

QTY	REFERENCE DESIGNATOR	DESCRIPTION	VALUE
5	C1,C2,C3,C16,C17	0402 CAP	0.01 uF
1	C4	0402 CAP	0.5 pF
2	C5,C8	0402 CAP	1.3 pF
2	C6,C7	0402 CAP	270 pF
1	C9	0402 CAP	0.1 pF
4	C10,C11,C14,C15	0402 CAP	4700 pF
2	C12,C13	0402 CAP	150 pF
1	C18	0402 CAP	0.2 pF
1	C19	0402 CAP	68 pF
1	C20	0402 CAP	47 uF
1	L1	0402 IND	1.0 nH
2	L2,L3	0402 IND	2.2 nH
2	L4,L5	Murata (LQH31CNR47M03L)	470 nH
2	L6,L9	Allied (LS03-R68J-RC)	680 nH
2	L7,L8	0402 IND	0 Ohm
1	R3	DNP	
1	R2	0402 Res	1.2k Ohm
2	R1,R4	0402 Res	0 Ohm
1	R5	0402 Res	8.25 Ohm
1	T1	1:1 Minntronix Balun (MRF-25001)	
1	T2	2:1 Minntronix Balun (MRF-27001)	
1	U1	ACA2429	
1	PCB	11G21A75_B10	

PACKAGE OUTLINE



S _W , B _{OL}	INCHES		MILLIMETERS		NOTE
	MIN.	MAX.	MIN.	MAX.	
A	0.087	0.098	2.21	2.49	
A ₁	0.000	0.004	0.00	0.10	6
A ₂	0.087	0.094	2.21	2.39	
B	0.013	0.019	0.33	0.48	
C	0.007	0.009	0.18	0.23	
D	0.398	0.412	10.11	10.46	2
E	0.290	0.300	7.37	7.62	3
e	0.050	BSC	1.27	BSC	4
H	0.394	0.418	10.01	10.62	
h	0.010	0.028	0.25	0.71	
L	0.024	0.040	0.61	1.02	
LE	0.052	—	1.32	—	
α	0°	8°	0°	8°	
S	0.120	0.140	3.05	3.56	5
T	0.330	0.350	8.38	8.89	5
R	REF.	0.015	REF.	0.38	5

NOTES:

1. CONTROLLING DIMENSION: INCHES
2. DIMENSION "D" DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH, PROTRUSIONS AND GATE BURRS SHALL NOT EXCEED 0.006 [0.15mm] PER SIDE.
3. DIMENSION "E" DOES NOT INCLUDE INTER-LEAD FLASH OR PROTRUSIONS. INTER-LEAD FLASH AND PROTRUSIONS SHALL NOT EXCEED 0.010 [0.25mm] PER SIDE.
4. MAXIMUM LEAD TWIST/SKEW TO BE ± 0.005 [0.13mm].
5. DIMENSIONS "S", "T" AND "R" INDICATE EXPOSED SLUG AREA.
6. STANDOFF HEIGHT (A₁) MEASURED FROM BOTTOM OF SLUG.

Figure 4: Wide Body SOIC-16 Package

ORDERING INFORMATION

ORDER NUMBER	TEMPERATURE RANGE	PACKAGE DESCRIPTION	COMPONENT PACKAGING
ACA2429P2	-40 °C to +100 °C	16 Pin Wide Body SOIC with Heat Slug	Tape and Reel, 1500 pieces per Reel

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