

Applications

- Wireless Infrastructure
- LTE / WCDMA / CDMA / GSM
- General Purpose Wireless

Product Features

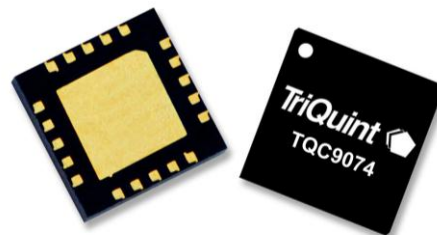
- Integrates Amp + VVA + Amp functionality
- 700-2800 MHz
- 22 dB Gain @ 2.6 GHz
- 3.0 dB Noise Figure @ max gain setting
- +27.1 dBm P1dB
- +42.0 dBm OIP3
- +5V Supply Voltage
- 235 mA Quiescent Current
- MTTF > 1000 Years

General Description

The TQC9074 is a high dynamic range analog controlled variable gain amplifier (VGA) which operates from 0.7 to 2.8 GHz. The VGA is able to achieve high performance with +42.0 dBm OIP3 and +27.1 dBm P1dB over a wide gain variation range while only consuming 235 mA current.

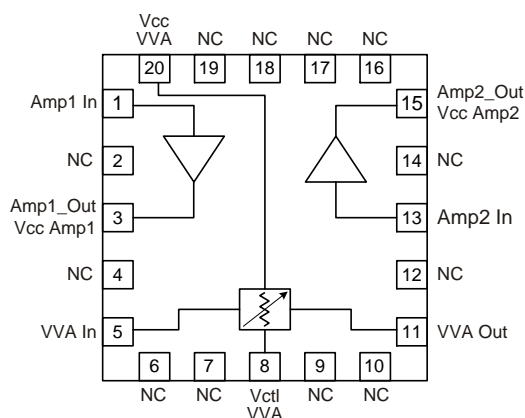
The TQC9074 integrates a high linearity, low noise amplifier for the first stage, followed by a broadband voltage variable attenuator, and then with a high performance ½W P1dB amplifier. The input and output of the various individual stages are accessible with external pins to allow for optimization of performance at any sub-band across the VGA's 0.7 to 2.8 GHz operating frequency range. The TQC9074 is packaged in a RoHS-compliant, compact 5x5mm surface-mount leadless package. Superior thermal design allows the product to have a minimum MTTF rating of 1000 years at a mounting temperature of +85° C.

The TQC9074 is targeted for use in wireless infrastructure where high linearity, medium power, and high efficiency are required.



20-pin 5x5mm leadless SMT package

Functional Block Diagram



Pin No.	Label
1	Amp1_In
2, 4, 6, 7, 9, 10, 12, 14, 16, 17, 18, 19	NC (No Connect)
3	Amp1_Out / Vcc Amp1
5	VVA_In
8	Vctrl_VVA
11	VVA_Out
13	Amp2_In
15	Amp2_Out / Vcc Amp2
20	Vcc_VVA
Backside Paddle	RF/DC Ground

Ordering Information

Part No.	Description
TQC9074	½W Variable Gain Amplifier
TQC9074-PCB2600	2600 MHz Evaluation Board

Standard T/R size = 2500 pcs on a 13" reel.

Absolute Maximum Ratings

Parameter	Rating
Storage Temperature	-65 to 150 °C
RF Input Power, CW, 50Ω, 25°C	+10 dBm
V _{DD} , Power Supply Voltage	+6 V

Operation of this device outside the parameter ranges given above may cause permanent damage.

Recommended Operating Conditions

Parameter	Min	Typ	Max	Units
Supply Voltage (V _{DD})	+4.75	+5	+5.25	V
T _{ch} (for >10 ⁶ hours MTTF)			+170	°C
Case Temperature	-40		+85	°C

Electrical specifications are measured at specified test conditions. Specifications are not guaranteed over all recommended operating conditions.

Electrical Specifications

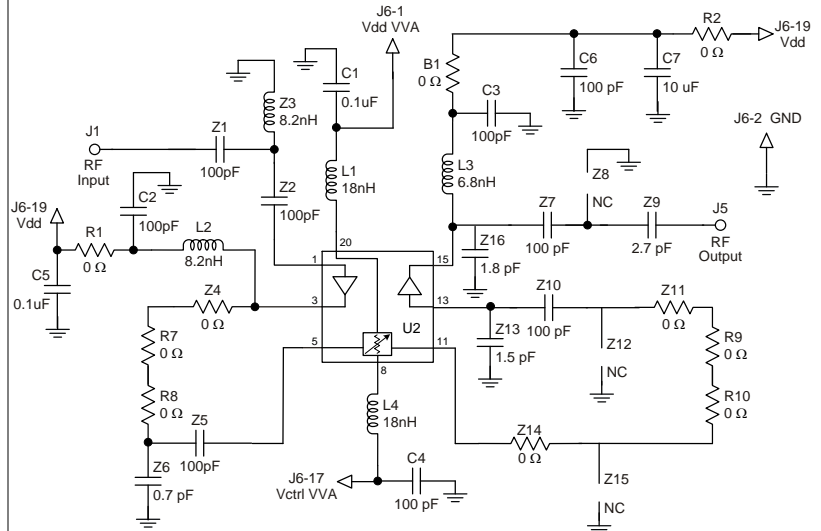
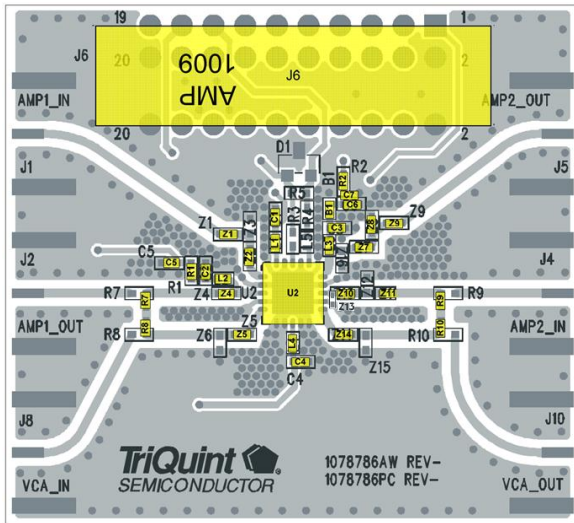
Test conditions: T_{LEAD}=+25°C, V_{AMP} = V_{DD} =+5V

Parameter	Conditions	Min	Typ	Max	Units
Operational Frequency Range		700		2800	MHz
Test Frequency			2600		MHz
Gain	V _{ctrl} = 0 V	TBD	22	TBD	dB
Gain	V _{ctrl} = 4.021 V	TBD	6.7	TBD	dB
Gain Variation Range	See Note 1	TBD	30		dB
Input Return Loss			-14		dB
Output Return Loss			-10		dB
Output P1dB			+27.1		dBm
Output IP3	See Note 2		+41.5		dBm
ACLR	At P _{out} = 12 dBm. See Note 3		-66		dBc
Noise Figure	At max gain level		3.0		dB
Voltage Control Range	0V = max gain level	0		5	V
Supply Voltage			+5		V
Amplifier Current (Pin 3 + 15)	See Notes 4 & 5	TBD	235	TBD	mA
VVA Current (Pin 8) @ minimum attenuation	See Note 6		3.6	6	mA
Thermal Resistance (R _{th})	Channel to case			40.3	°C /W

Notes:

1. The gain variation range is measured as the difference in gain with V_{ctrl} = 5 V and V_{ctrl} = 0 V.
2. OIP3 measured with two tones at an output power of +11 dBm / tone separated by 1 MHz. The suppression on the largest IM3 product is used to calculate the OIP3 using a 3:1 rule.
3. 3GPP WCDMA, 1±64DPCH, 3.84 MHz BW, PAR=7.5 dB at 0.01% Probability, single carrier.
4. Amp1 current (pin 3) is typically 85 mA.
5. Amp2 current (pin 15) is typically 150 mA.
6. VVA typical current is 3.6 mA for min attenuation and 1.6 mA for max attenuation.

TQC9074-PCB2600 Evaluation Board



Bill of Material – TQC9074-PCB2600

Reference Des.	Value	Description	Manuf.	Part No.
n/a	n/a	1078786PC Printed Circuit Board	TriQuint	N/A
U1	n/a	TQC9074 ½ W Variable Gain Amplifier	TriQuint	TQC9074
L1, L4	18 nH	Ind., 0402, 5%, 420 mA, 3.1 GHz SRF	various	Various
L2, Z3	8.2 nH	Ind., 0402, 5%, 4.4 GHz SRF	various	various
L3	6.8 nH	Ind., 0402, 5%, 4.8 GHz SRF	various	various
B1, R1, R2, R7, R8, R9, R10, Z4, Z11, Z14	0 Ohm	Res., 0402, 5%, 1/16W	various	various
C7	10 uF	CAP, 6032, 20%, 50V, Tantalum	various	various
C1, C5	0.1 uF	Cap., 0402, 10%, 10V, X5R	various	various
C2, C3, C4, C6, Z1, Z2, Z5,	100 pF	Cap., 0402, 5%, 50V, NPO/COG	various	various
Z9	2.7 pF	Cap., 0402, +/-0.25PF. 50V. NPO/COG	various	various
Z16	1.8 pF	Cap., 0402, +/-0.25PF. 50V. NPO/COG	various	various
Z13	1.5 pF	Cap., 0402, +/- 0.1 pF, 25V, NPO/COG	various	various
Z6	0.7 pF	Cap., 0402, +/- 0.075PF. 25V. NPO/COG	various	various
J1, J5	n/a	Conn.062 RF SMA F STRT Flang	various	various
J6-1, J6-2, J6-17, J6-19	n/a	Conn, Header, 4 pin, right angle (Note 1)	various	various

Typical Performance – TQC9074-PCB2600

Test conditions unless otherwise noted: $V_{DD} = +5V$, $I_{DD} = 235$ mA (typ.), $V_{DD} VVA = 0V$, Temp = +25°C

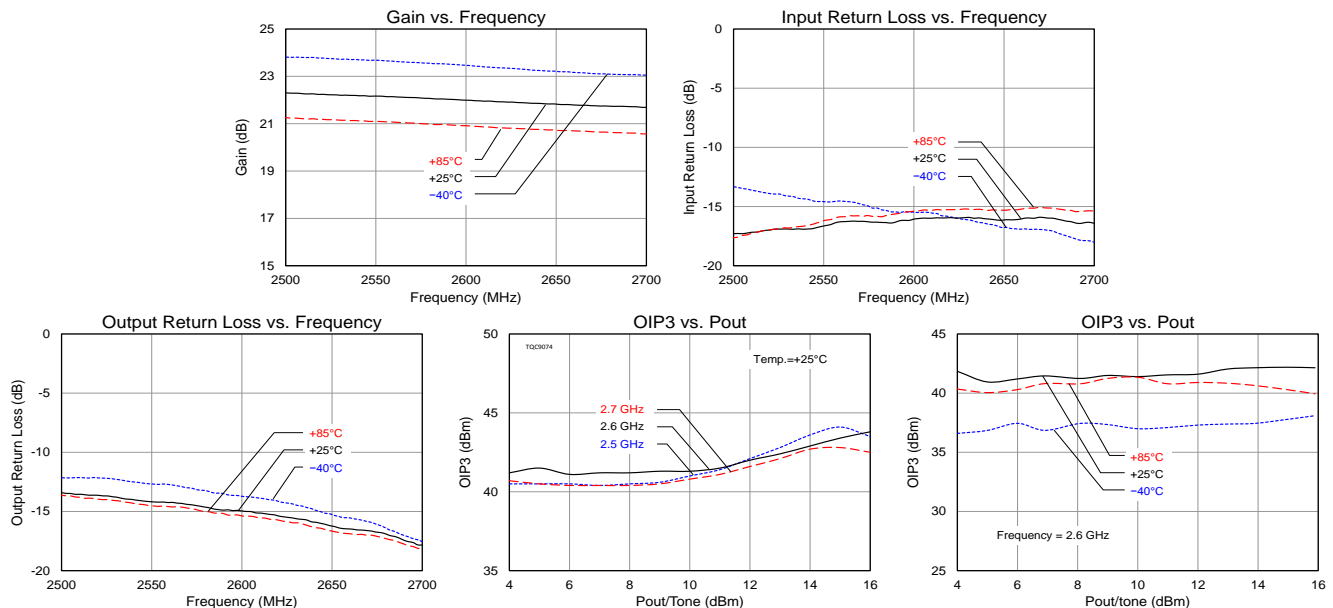
Parameter	Conditions	Typical Values			Units
Frequency		2500	2600	2700	MHz
Gain		22.4	22.1	21.8	dB
Input Return Loss		-17	-16	-16	dB
Output Return Loss		-13	-15	-17	dB
Output P1dB		+27.6	+27.5	+27.4	dBm
OIP3	Pout = +10 dBm/tone, $\Delta f = 1$ MHz	+41.0	+41.5	+40.8	dBm
Noise figure ⁽¹⁾		3.1	3.2	3.4	dB

Notes:

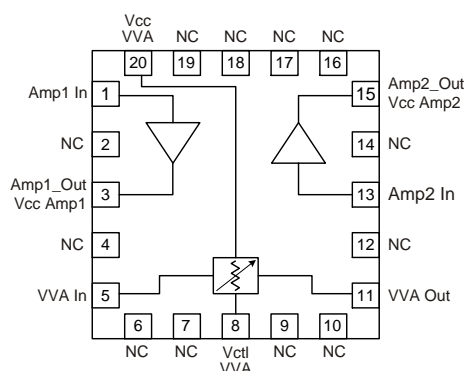
- Noise figure data shown in the table above is de-embedded from the eval board loss.

Performance Plots – TQC9074-PCB_RF

Test conditions unless otherwise noted: $V_{DD} = +5V$, $I_{DD} = 235$ mA (typ.), $V_{DD} VVA = 0V$, Temp = +25°C

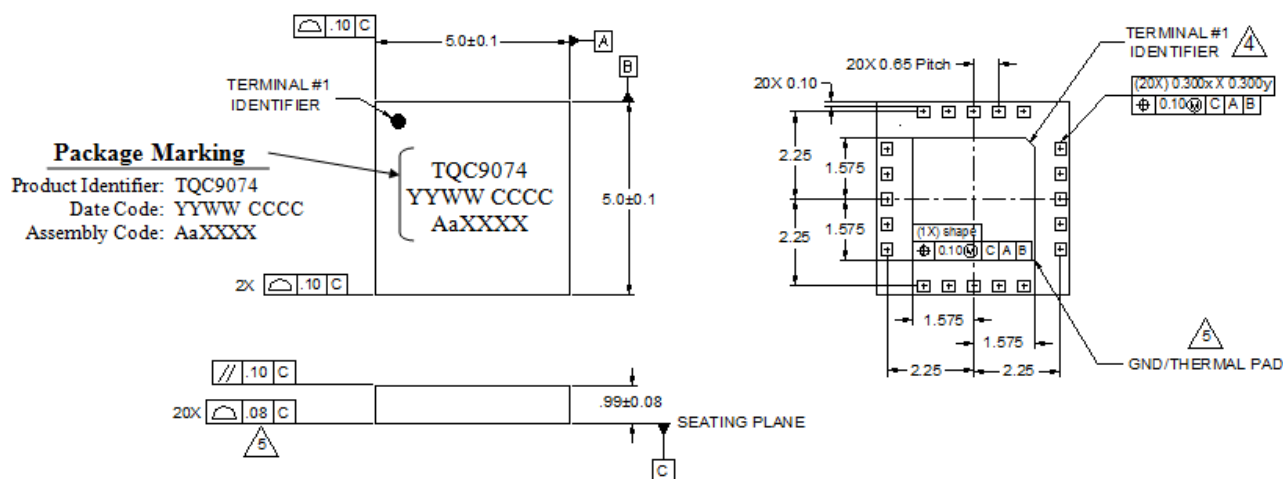


Pin Configuration and Description



Pin No.	Label	Description
1	Amp1_In	RF input (Amp1). Band-specific matching circuit required.
2, 4, 6, 7, 9, 10, 12, 14, 16, 17, 18, 19	NC (No Connect)	No electrical connection. Land pads should be provided for PCB mounting integrity.
3	Amp1_Out/Vcc_Amp1	RF output / DC supply (Amp1).
5	VVA_In	RF input (VVA).
8	Vctrl_VVA	Analog input for voltage controlled amplifier.
11	VVA_Out	RF output (VVA).
13	Amp2_In	RF input (Amp2). Band-specific matching circuit required.
15	Amp2_Out/Vcc_Amp2	RF output / DC supply (Amp2). Band-specific matching circuit required.
20	Vcc_VVA	DC supply for voltage controlled amplifier.
Backside Paddle	RF/DC Ground	RF/DC ground. Provide recommended via pattern (see page 8) and ensure good solder attach for best thermal and electrical performance.

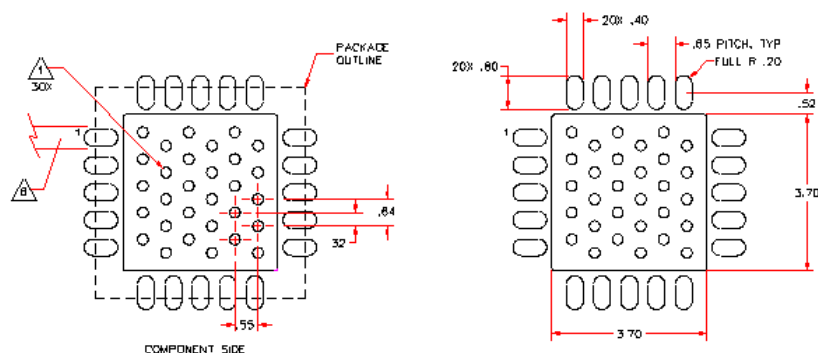
Package Marking and Dimensions



Notes:

1. All dimensions are in millimeters. Angles are in degrees.
2. Dimension and tolerance formats conform to ASME Y14.4M-1994.
3. The terminal #1 identifier and terminal numbering conform to JESD 95-1 SPP-012.

PCB Mounting Pattern



Notes:

1. All dimensions are in millimeters. Angles are in degrees.
2. Use 1 oz. copper minimum for top and bottom layer metal.
3. Vias are required under the backside paddle of this device for proper RF/DC grounding and thermal dissipation. We recommend a 0.35mm (#80/.0135") diameter bit for drilling via holes and a final plated thru diameter of 0.25 mm (0.10").
4. Ensure good package backside paddle solder attach for reliable operation and best electrical performance.

Product Compliance Information

ESD Sensitivity Ratings



Caution! ESD-Sensitive Device

ESD Rating: 1A
Value: Passes ≥ 250 V to < 500 V
Test: Human Body Model (HBM)
Standard: JEDEC Standard JS-001-2012

ESD Rating: C3
Value: Passes ≥ 1000 V
Test: Charged Device Model (CDM)
Standard: JEDEC Standard JESD22-C101F

MSL Rating

MSL Rating: Level 3
Test: $+260$ °C convection reflow
Standard: JEDEC standard IPC/JEDEC J-STD-020

Solderability

Compatible with both lead-free (260 °C max. reflow temp.) and tin/lead (245 °C max. reflow temp.) soldering processes.

Package lead plating: Electrolytic plated Au over Ni.

RoHS Compliance

This part is compliant with EU 2002/95/EC RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment).

This product also has the following attributes:

- Halogen Free (Chlorine, Bromine)
- Antimony Free
- TBBP-A ($C_{15}H_{12}Br_4O_2$) Free
- PFOS Free
- SVHC Free
- Lead Free

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