AWT6273

HELP3™ Cellular/WCDMA 3.4 V/29 dBm
Linear Power Amplifier Module
Data Sheet - Rev 2.1

FEATURES

- InGaP HBT Technology
- High Efficiency:
  - 43 % @ P_{OUT} = +29 dBm
  - 20 % @ P_{OUT} = +16 dBm
  - 8 % @ P_{OUT} = +8 dBm
- Low Quiescent Current: 7 mA
- Low Leakage Current in Shutdown Mode: <1 μA
- Internal Voltage Regulator Eliminates the Need for External Reference Voltage (No V_{REF} Required)
- Optimized for a 50 Ω System
- Low Profile Miniature Surface Mount Package
- RoHS Compliant Package, 250 °C MSL-3
- HSDPA Compliant (no backoff)

M20 Package
10 Pin 4 mm x 4 mm x 1 mm
Surface Mount Module

APPLICATIONS

- Dual Band WCDMA Wireless Handsets
- Dual Mode 3GPP Wireless Handsets

PRODUCT DESCRIPTION

The AWT6273 HELP3™ PA is the 3rd generation WCDMA product for UMTS handsets. This PA incorporates ANADIGICS' HELP3™ technology to provide low power consumption without the need for an external voltage regulator. The device is manufactured on an advanced InGaP HBT MMIC technology offering state-of-the-art reliability, temperature stability, and ruggedness. There are three selectable bias modes that optimize efficiency for different output power levels, and a shutdown mode with low leakage current, increase handset talk and standby time. The self-contained 4 mm x 4 mm x 1 mm surface mount package incorporates matching networks optimized for output power, efficiency, and linearity in a 50 Ω system.

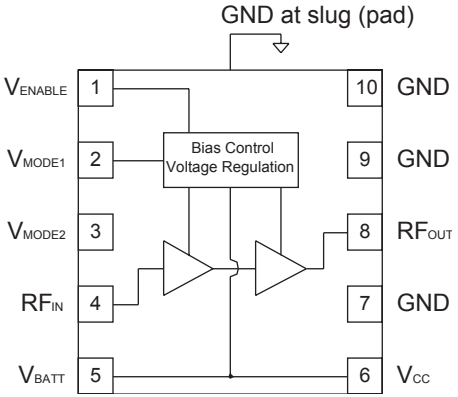


Figure 1: Block Diagram

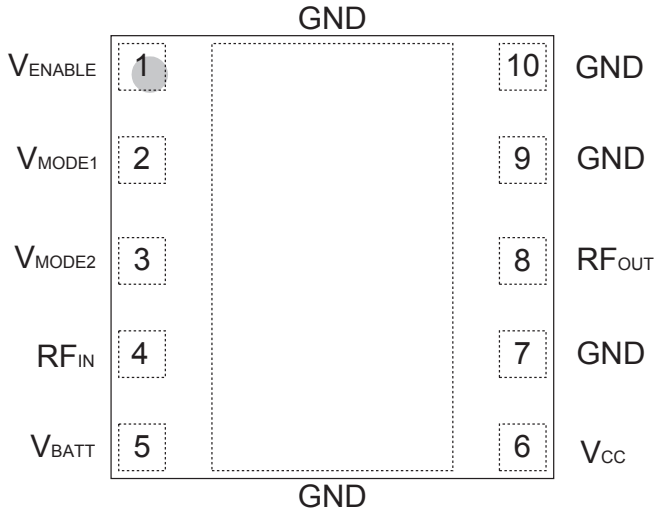


Figure 2: Pinout (X-ray Top View)

Table 1: Pin Description

PIN	NAME	DESCRIPTION
1	V_{ENABLE}	PA Enable Voltage
2	V_{MODE1}	Mode Control Voltage 1
3	V_{MODE2}	Mode Control Voltage 2
4	RF_{IN}	RF Input
5	V_{BATT}	Battery Voltage
6	V_{CC}	Supply Voltage
7	GND	Ground
8	RF_{OUT}	RF Output
9	GND	Ground
10	GND	Ground

ELECTRICAL CHARACTERISTICS

Table 2: Absolute Minimum and Maximum Ratings

PARAMETER	MIN	MAX	UNIT
Supply Voltage (V_{CC})	0	+5	V
Battery Voltage (V_{BATT})	0	+6	V
Control Voltages (V_{MODE1}, V_{MODE2}, V_{ENABLE})	0	+3.5	V
RF Input Power (P_{IN})	-	+10	dBm
Storage Temperature (T_{STG})	-40	+150	°C

Stresses in excess of the absolute ratings may cause permanent damage. Functional operation is not implied under these conditions. Exposure to absolute ratings for extended periods of time may adversely affect reliability.

Table 3: Operating Ranges

PARAMETER	MIN	TYP	MAX	UNIT	COMMENTS
Operating Frequency (f)	824	-	849	MHz	
Supply Voltage (V_{CC})	+3.2 -	+3.4 +1.5	+4.2 -	V	$P_{OUT} \leq$ +29 dBm TBD
Enable Voltage (V_{ENABLE})	+2.15 0	+2.4 -	+3.1 +0.5	V	PA "on" PA "shut down"
Mode Control Voltage (V_{MODE1},V_{MODE2})	+2.15 0	+2.4 -	+3.1 +0.5	V	Low Bias Mode High Bias Mode
RF Output Power (P_{OUT}) 3GPP HSDPA Case A HSDPA Case B HSDPA Case C	 +28.5 (1) +27.5 (1) +26.5 (1) +26.0 (1)	 +29.0 +28.0 +27.0 +26.5	 - - - -	dBm	 $1/15 \leq \beta_c/\beta_d \leq 12/15$ $13/15 \leq \beta_c/\beta_d \leq 15/8$ $15/7 \leq \beta_c/\beta_d \leq 15/0$
Case Temperature (T_C)	-10	-	+90	°C	

The device may be operated safely over these conditions; however, parametric performance is guaranteed only over the conditions defined in the electrical specifications.

Notes:

(1) For operation at V_{CC} = +3.2 V, P_{OUT} is derated by 0.5 dB.

Table 4: Electrical Specifications
(T_C = +25 °C, V_{CC} = +3.4 V, V_{BATT} = +3.4 V, V_{ENABLE} = +2.4 V, 50 Ω system)

PARAMETER	MIN	TYP	MAX	UNIT	COMMENTS		
					P_{OUT}	V_{MODE1}	V_{MODE2}
Gain	25 13 10.5	27.5 15 12.5	30 17.5 15	dB	+29 dBm +16 dBm +8 dBm	0 V 2.4 V 2.4 V	0 V 0 V 2.4 V
ACLR1 at 5 MHz offset (1)	- - -	-42 -44 -42	-38 -38 -38	dBc	+29 dBm +16 dBm +8 dBm	0 V 2.4 V 2.4 V	0 V 0 V 2.4 V
ACLR2 at 10 MHz offset	- - -	-55 -57 -62	-48 -48 -48	dBc	+29 dBm +16 dBm +8 dBm	0 V 2.4 V 2.4 V	0 V 0 V 2.4 V
Power-Added Efficiency (1)	40 17 6	43 20 8	- - -	%	+29 dBm +16 dBm +8 dBm	0 V 2.4 V 2.4 V	0 V 0 V 2.4 V
Quiescent Current (Icq)	- -	7 15	11 21	mA	V_{MODE1} = +2.4 V, V_{MODE2} = +2.4 V V_{MODE1} = +2.4 V, V_{MODE2} = 0 V		
Mode Control Current	-	0.3	0.8	mA	through V_{MODE} pins, V_{MODE} = +2.4 V		
Enable Current	-	0.5	1	mA	through V_{ENABLE} pin		
BATT Current	-	2.5	5	mA	through V_{BATT} pin, V_{MODE1} = +2.4 V, V_{MODE2} = +2.4 V or 0 V		
Leakage Current	-	<1	5	µA	V_{BATT} = +4.2 V, V_{CC} = +4.2 V, V_{ENABLE} = 0 V, V_{MODE1} = 0 V, V_{MODE2} = 0 V		
Noise in Receive Band (2)	-	-136	-134	dBm/Hz	P_{OUT} = +29 dBm, V_{MODE1} = 0 V, V_{MODE2} = 0 V		
Harmonics 2fo 3fo, 4fo	 - -	 -43 -50	 -35 -35	dBc	$P_{OUT} \leq$ +29 dBm		
Input Impedance	-	-	2:1	VSWR			
Spurious Output Level (all spurious outputs)	-	-	-70	dBc	See Note 3		
Load mismatch stress with no permanent degradation or failure	8:1	-	-	VSWR	Applies over full operating range		

Notes:
(1) ACLR and Efficiency measured at 836.5 MHz.
(2) 869 MHz to 894 MHz.
(3) $P_{OUT} \leq$ +29 dBm, In-band load VSWR < 5:1, Out-of-band load VSWR < 10:1. Applies over all operating conditions.

APPLICATION INFORMATION

To ensure proper performance, refer to all related Application Notes on the ANADIGICS web site: http://www.anadigics.com

Shutdown Mode

The power amplifier may be placed in a shutdown mode by applying logic low levels (see Operating Ranges table) to the V_{ENABLE}, V_{MODE1} and V_{MODE2} voltages.

Bias Modes

The power amplifier may be placed in either a Low Bias mode or a High Bias mode by applying the appropriate logic level (see Operating Ranges table) to the V_{MODE} voltages. The Bias Control table lists the recommended modes of operation for various applications.

Three operating modes are recommended to optimize current consumption. High Bias/High Power operating mode is for P_{OUT} levels ≥ 16 dBm. At ~16dBm - 7 dBm, the PA should be "Mode Switched" to Medium Bias Mode. For P_{OUT} levels ≤ ~8 dBm, the PA can be switched to Low Bias/Low Power Mode used for this P_{OUT} range for even lower quiescent current consumption.

Table 5: Bias Control

APPLICATION	P_{OUT} LEVELS	BIAS MODE	V_{ENABLE}	V_{MODE1}	V_{MODE2}	V_{CC}	V_{BATT}
WCDMA - low power (Low Bias Mode)	≤ +8 dBm	Low	+2.4 V	+2.4 V	+2.4 V	3.2 - 4.2 V	≥ 3.2 V
WCDMA - med power (Medium Bias Mode)	≤ +16 dBm	Med	+2.4 V	+2.4 V	0 V	3.2 - 4.2 V	≥ 3.2 V
WCDMA - high power (High Bias Mode)	> +16 dBm	High	+2.4 V	0 V	0 V	3.2 - 4.2 V	≥ 3.2 V
Optional lower V_{CC} in low power mode	≤ +7 dBm	Low	+2.4 V	+2.4 V	2.4 V	≥ 1.5 V	≥ 3.2 V
Shutdown	-	Shutdown	0 V	0 V	0 V	3.2 - 4.2 V	≥ 3.2 V

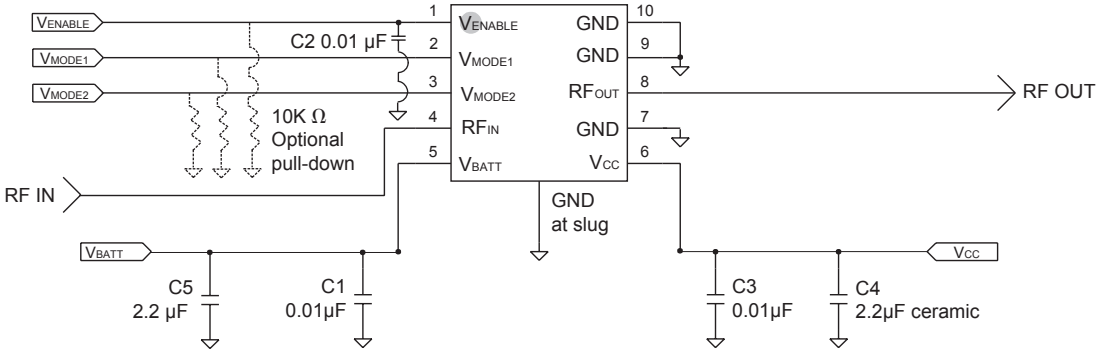
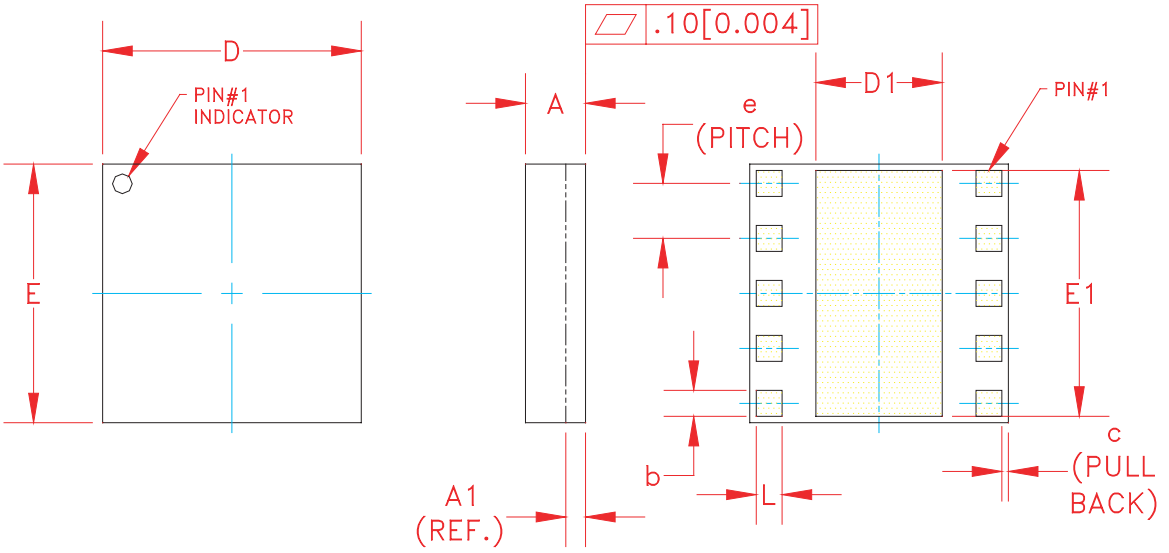


Figure 3: Application Circuit Schematic

PACKAGE OUTLINE

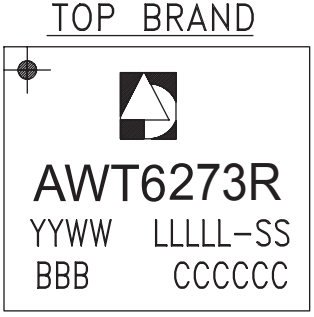


SYMBOL	MILLIMETERS			INCHES			NOTE
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.	
A	0.88	0.98	1.08	0.034	0.038	0.042	–
A1	0.32 (REF.)			0.0125 (REF.)			–
b	0.35	–	0.60	0.013	–	0.024	3
c	–	0.10	–	–	0.004	–	–
D	3.88	4.00	4.12	0.152	0.157	0.162	–
D1	1.90	–	2.25	0.075	–	0.088	–
E	3.88	4.00	4.12	0.152	0.157	0.162	–
E1	3.75	–	3.85	0.148	–	0.152	–
e		0.85			0.033		3
L	0.35	–	0.60	0.013	–	0.024	3

NOTES:

1. CONTROLLING DIMENSIONS: MILLIMETERS
2. UNLESS SPECIFIED TOLERANCE=±0.076[0.003].
3. PADS (INCLUDING CENTER) SHOWN UNIFORM SIZE FOR REFERENCE ONLY. ACTUAL PAD SIZE AND LOCATION WILL VARY WITHIN MIN. AND MAX. DIMENSIONS ACCORDING TO SPECIFIC LAMINATE DESIGN.

Figure 4: M20 Package Outline - 10 Pin 4 mm x 4 mm x 1 mm Surface Mount Module



NOTES:

1. ANADIGICS LOGO SIZE:	X=0.040±0.010 Y=0.048±0.010
2. PART #	AWT6273R
3. YEAR AND WORK WEEK:	YYWW: YY = YEAR, WW = WORK WEEK
4. LOT – WAFER I.D.:	LLLLL – SS = WAFER/LOT I.D.
5. PIN 1 INDICATOR:	MOLD NOTCH –or– INK DOT
6. BOM #	BBB
7. COUNTRY CODE:	CCCCCC
8. TYPE : ELITE SIZE : AS LARGE AS POSSIBLE LASER MARKED	

Figure 5: Branding Specification - M20 Package

COMPONENT PACKAGING

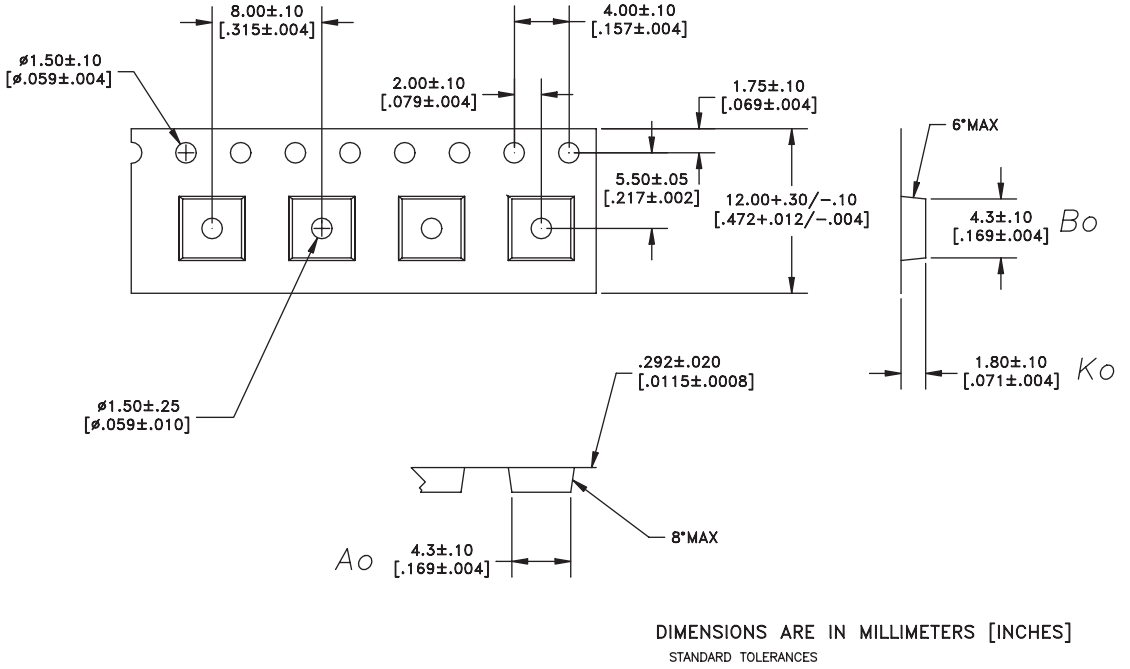


Figure 6: Tape & Reel Packaging

Table 6: Tape & Reel Dimensions

PACKAGE TYPE	TAPE WIDTH	POCKET PITCH	REEL CAPACITY	MAX REEL DIA
4 mm x 4 mm x 1 mm	12 mm	8 mm	2500	13"

ORDERING INFORMATION

ORDER NUMBER	TEMPERATURE RANGE	PACKAGE DESCRIPTION	COMPONENT PACKAGING
AWT6273RM20P8	-10 °C to +90 °C	RoHS Compliant 10 Pin 4 mm x 4 mm x 1 mm Surface Mount Module	Tape and Reel, 2500 pieces per Reel
AWT6273RM20P9	-10 °C to +90 °C	RoHS Compliant 10 Pin 4 mm x 4 mm x 1 mm Surface Mount Module	Partial Tape and Reel

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