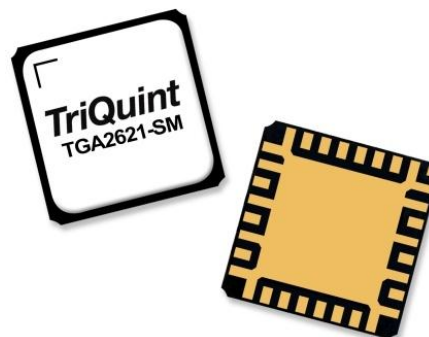


Applications

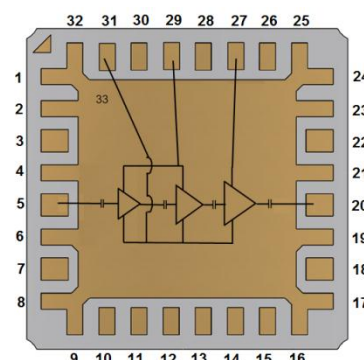
- Commercial and Military Radar
- Satellite Communications



Product Features

- Frequency Range: 16 – 18.5 GHz
- P_{SAT} : >30 dBm at P_{in} = 10 dBm
- PAE: >23 % at P_{in} = 10 dBm
- Small Signal Gain: >24.5 dB
- Input Return Loss: > 10 dB
- Bias: V_D = 6 V, I_{D0} = 500 mA, V_G = -0.6 V Typical
- Package Dimensions: 5.0 x 5.0 x 1.45 mm

Functional Block Diagram



General Description

TriQuint's TGA2621-SM is a packaged Ku-band Power Amplifier fabricated on TriQuint's TQPHT15 0.15 μ m GaAs pHEMT process. The TGA2621-SM operates from 16 to 18.5 GHz and typically provides greater than 1W of saturated output power with greater than 23% PAE and greater than 24.5 dB of small signal gain.

The TGA2621-SM is available in a low cost, surface mount 32 lead 5x5 mm air-cavity ceramic QFN. It is ideally suited to support both radar and satellite communications as a driver or low power amplifier.

Both RF ports have integrated DC blocking caps and are fully matched to 50 ohms allowing for simple system integration.

Lead-free and RoHS compliant

Evaluation Boards are available upon request.

Pad Configuration

Pad No.	Symbol
1, 2, 4, 6, 8-9, 16-17, 19, 21, 23-25, 32-33	Gnd
3, 7, 10-15, 18, 22, 26, 28, 30	N/C
5	RF _{IN}
20	RF _{OUT}
27	V _{D2}
29	V _{D1}
31	V _G

Ordering Information

Part	ECCN	Description
TGA2621-SM	EAR99	16 – 18.5 GHz 1 W GaAs PA

Absolute Maximum Ratings

Parameter	Value
Drain Voltage (V_D)	6.25 V
Gate Voltage Range (V_G)	-2 to 0 V
Drain Current (I_D)	1300 mA
Gate Current (I_G)	-5 to 5 mA
Power Dissipation, CW, 85 °C (P_{DISS})	3.0 W
Input Power, CW, 85 °C, 50 Ω , (P_{IN})	17 dBm
Input Power, CW, 85 °C, (3:1 VSWR), (P_{IN})	17 dBm
Channel temperature (T_{CH})	200 °C
Mounting Temperature (30 Seconds)	260 °C
Storage Temperature	-55 to 150 °C

Operation of this device outside the parameter ranges given above may cause permanent damage. These are stress ratings only, and functional operation of the device at these conditions is not implied.

Recommended Operating Conditions

Parameter	Value
Drain Voltage (V_D)	6 V
Drain Current (I_{DQ})	500 mA
Gate Voltage (V_G)	-0.6 V Typical
Temperature (T_{BASE})	-40 to 85 °C

Electrical specifications are measured at specified test conditions. Specifications are not guaranteed overall operating conditions.

Electrical Specifications

Test conditions unless otherwise noted: 25 °C, V_D = 6 V, I_{DQ} = 500 mA, V_G = -0.6 V typical, CW

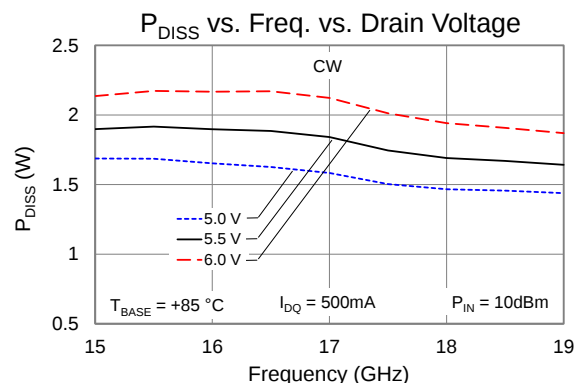
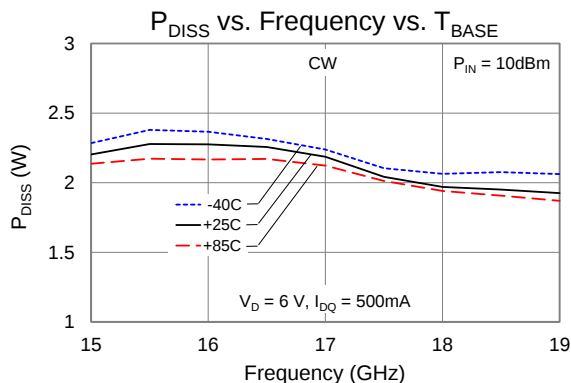
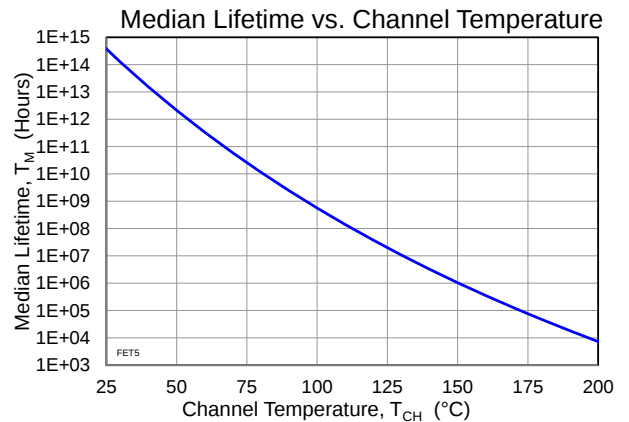
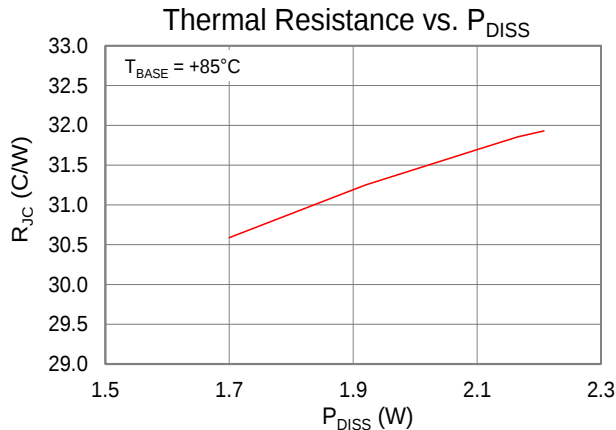
Parameter	Min	Typical	Max	Units
Operational Frequency Range	16		18.5	GHz
Small Signal Gain		>24.5		dB
Input Return Loss		>10		dB
Output Return Loss		6		dB
Output Power (P_{in} = 10 dBm)		>30		dBm
Power Added Efficiency (P_{in} = 10 dBm)		>23		%
Small Signal Gain Temperature Coefficient		-0.036		dB/°C
Output Power Temperature Coefficient		-0.02		dB/°C

Thermal and Reliability Information

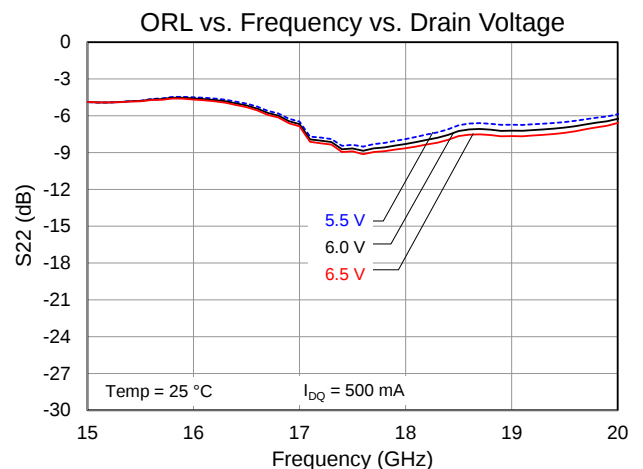
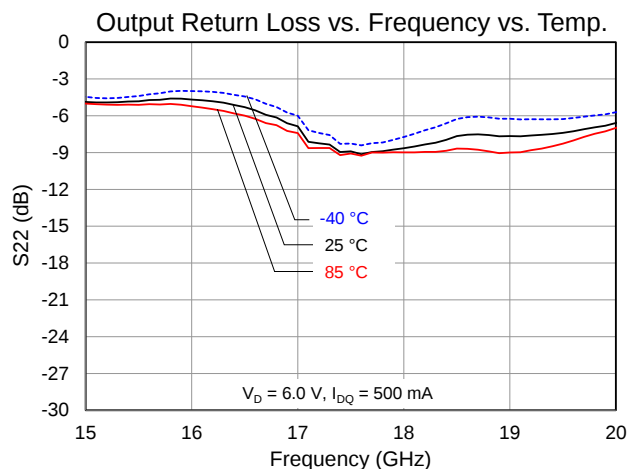
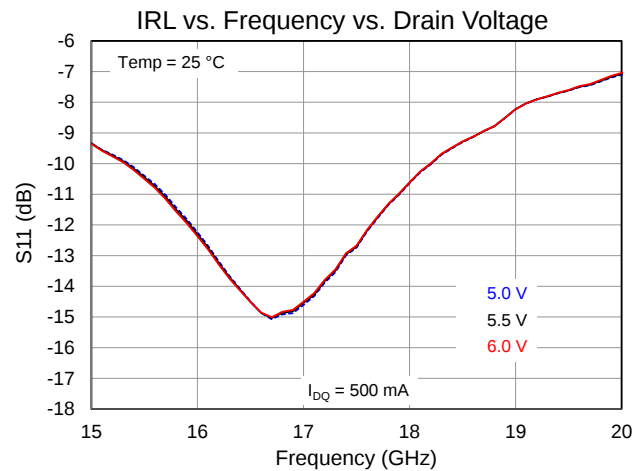
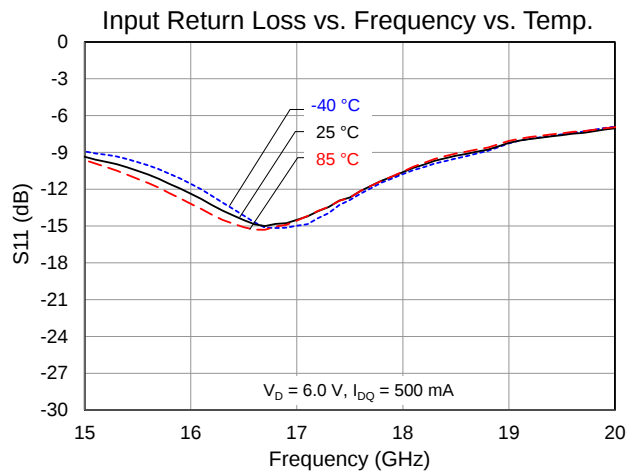
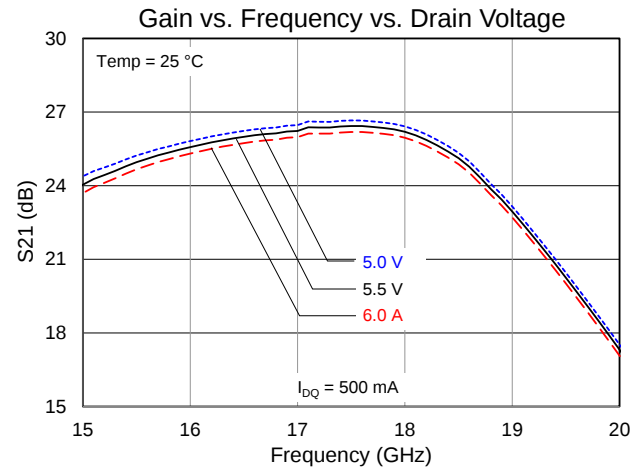
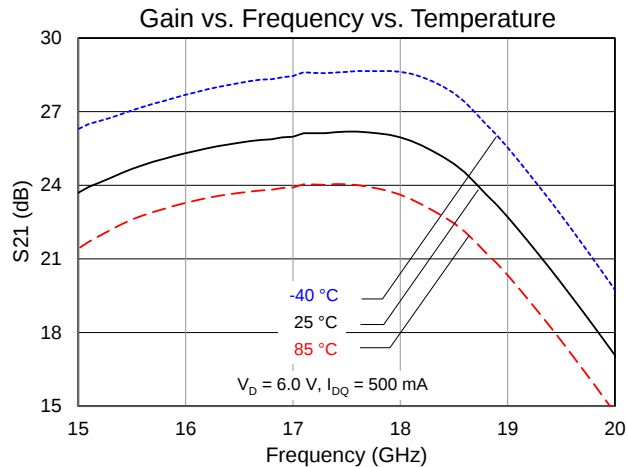
Parameter	Test Conditions	Value	Units
Thermal Resistance (θ_{JC}) ⁽¹⁾	$T_{base} = 85^\circ\text{C}$, $V_D = 5\text{ V}$	30.6	$^\circ\text{C/W}$
Channel Temperature (T_{CH}) (Under RF drive)	$I_{DQ} = 500\text{ mA}$, $I_{D Drive} = 627\text{ mA}$	137	$^\circ\text{C}$
Median Lifetime (T_M)	$P_{IN} = 10\text{ dBm}$, $P_{OUT} = 28.3\text{ dBm}$, Freq = 16 GHz, $P_{DISS} = 1.7\text{ W}$	4.6×10^6	Hrs
Thermal Resistance (θ_{JC}) ⁽¹⁾	$T_{base} = 85^\circ\text{C}$, $V_D = 5.5\text{ V}$	31.3	$^\circ\text{C/W}$
Channel Temperature (T_{CH}) (Under RF drive)	$I_{DQ} = 500\text{ mA}$, $I_{D Drive} = 645\text{ mA}$	145	$^\circ\text{C}$
Median Lifetime (T_M)	$P_{IN} = 10\text{ dBm}$, $P_{OUT} = 28.7\text{ dBm}$, Freq = 16 GHz, $P_{DISS} = 1.9\text{ W}$	1.8×10^6	Hrs
Thermal Resistance (θ_{JC}) ⁽¹⁾	$T_{base} = 85^\circ\text{C}$, $V_D = 6\text{ V}$	31.9	$^\circ\text{C/W}$
Channel Temperature (T_{CH}) (Under RF drive)	$I_{DQ} = 500\text{ mA}$, $I_{D Drive} = 665\text{ mA}$	154	$^\circ\text{C}$
Median Lifetime (T_M)	$P_{IN} = 10\text{ dBm}$, $P_{OUT} = 29\text{ dBm}$, Freq = 16 GHz, $P_{DISS} = 2.2\text{ W}$	6.7×10^5	Hrs

1. Resistance measured at back of the package.

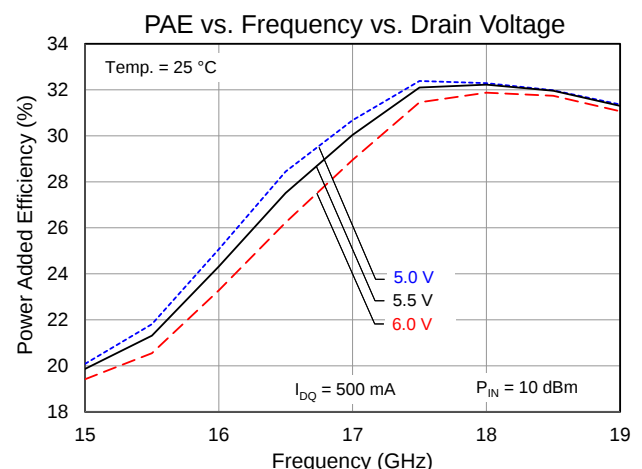
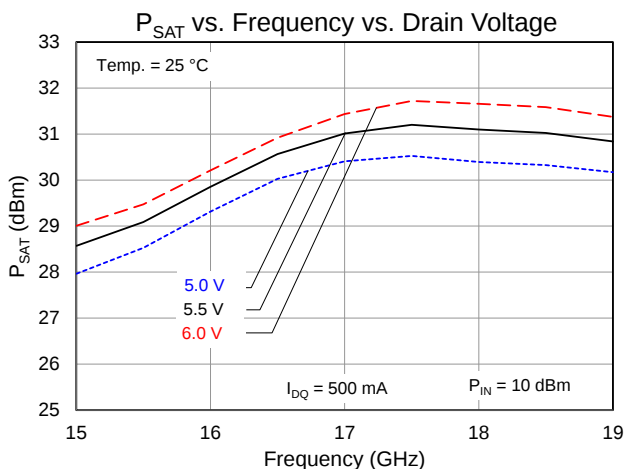
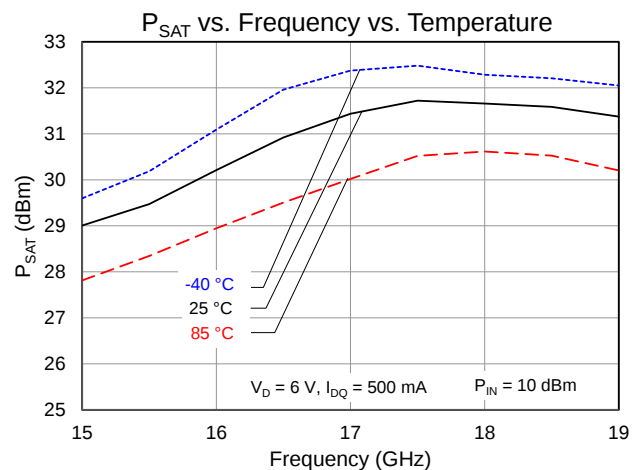
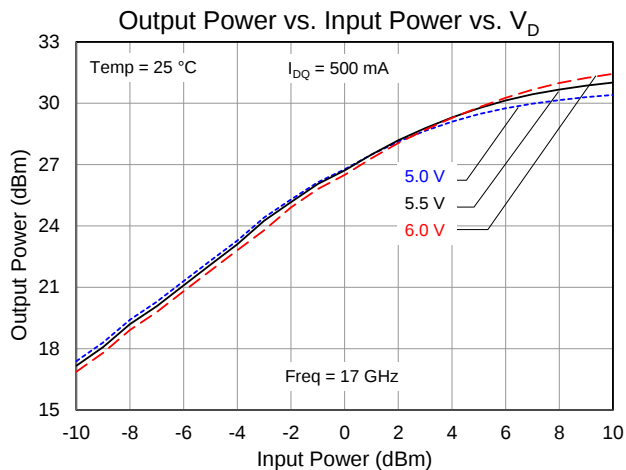
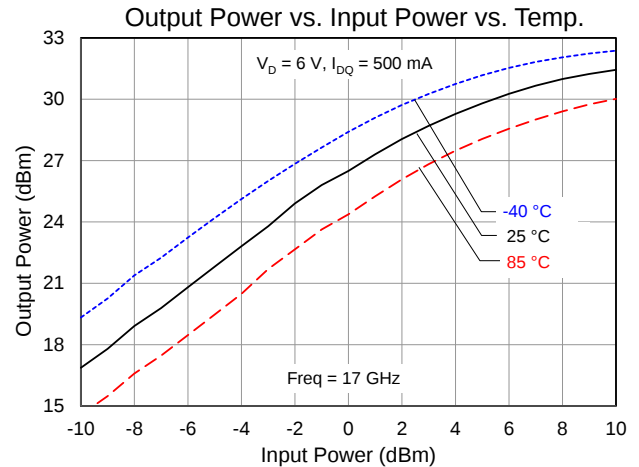
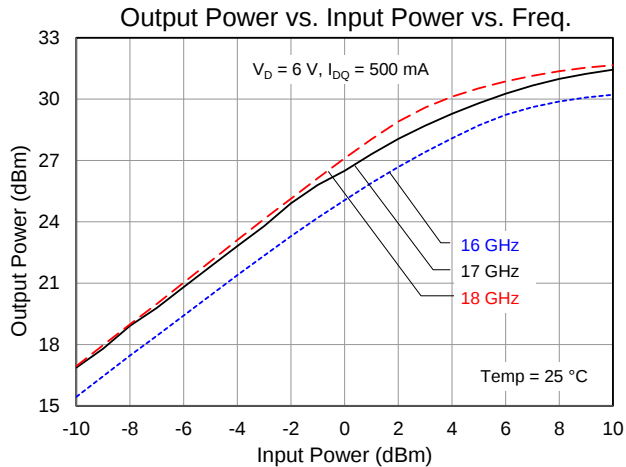
Test Conditions: $V_D = 6\text{ V}$; Failure Criteria is 10% reduction in $I_{D MAX}$



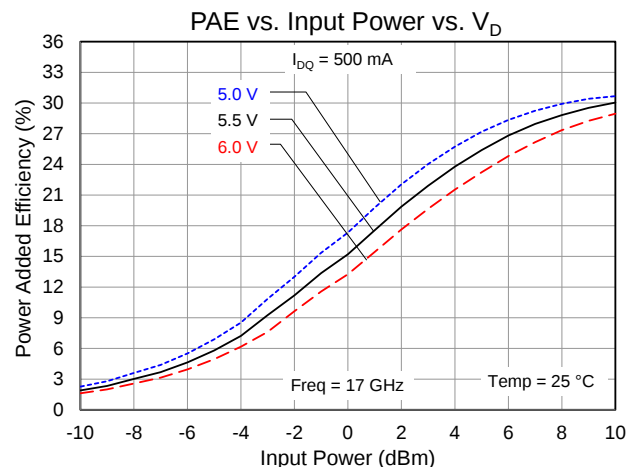
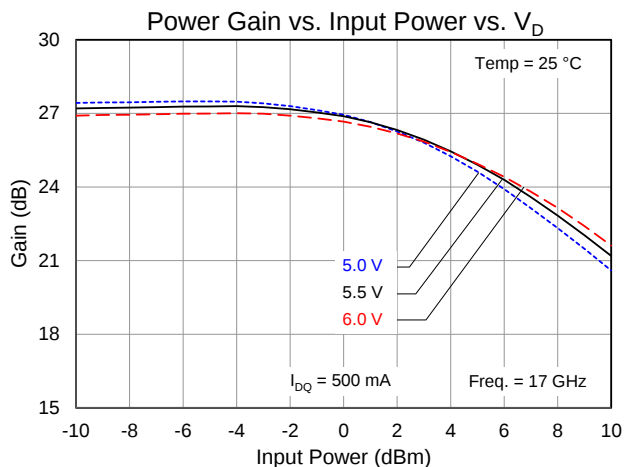
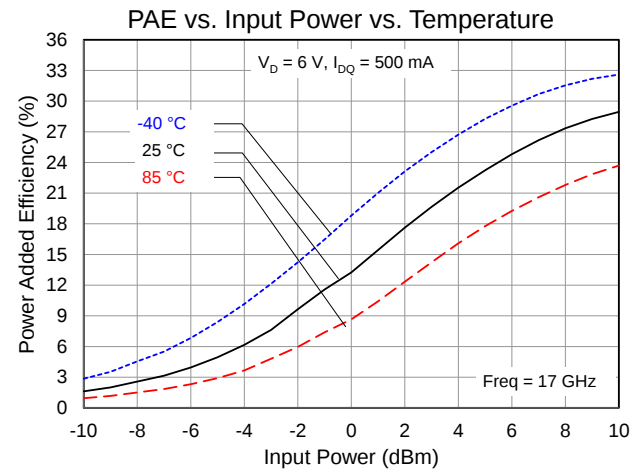
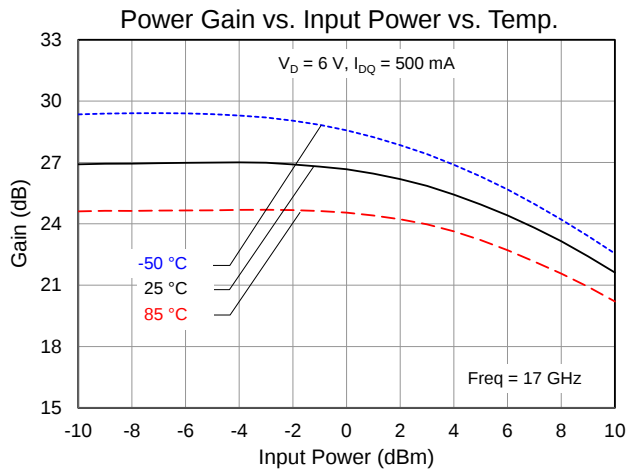
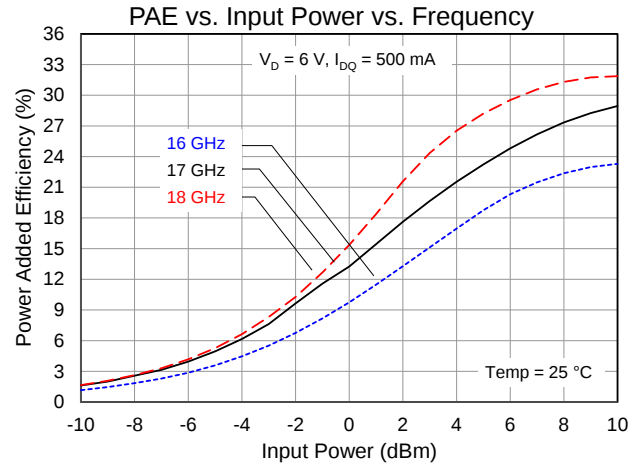
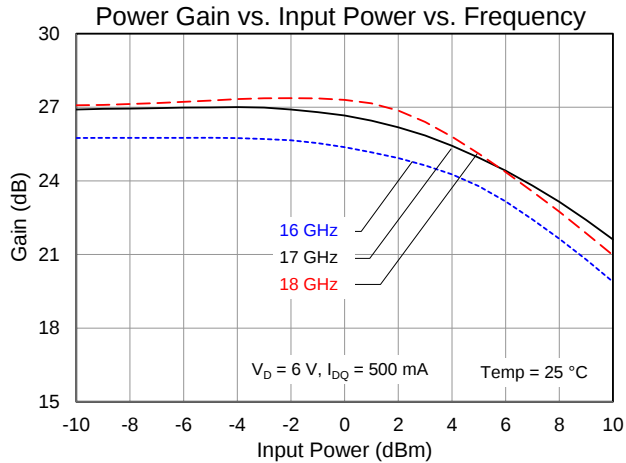
Typical Performance: Small Signal



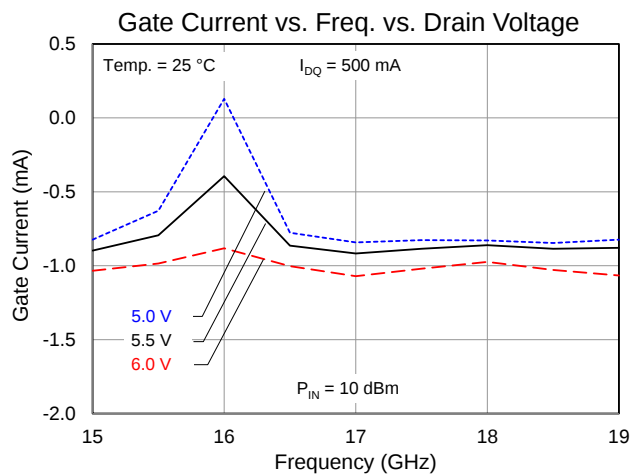
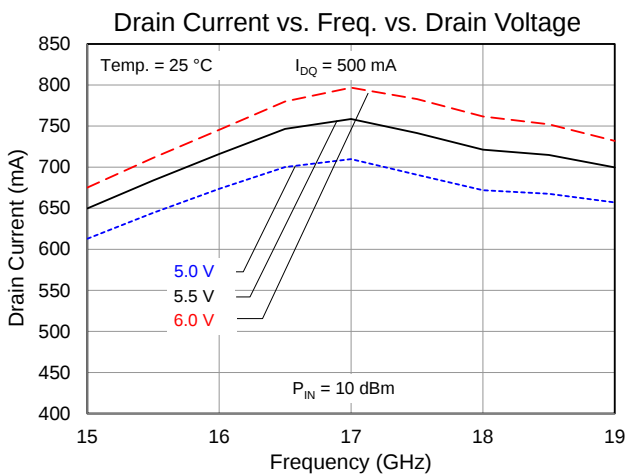
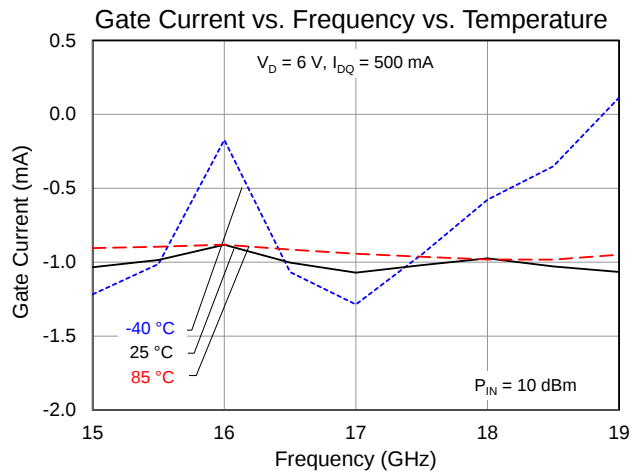
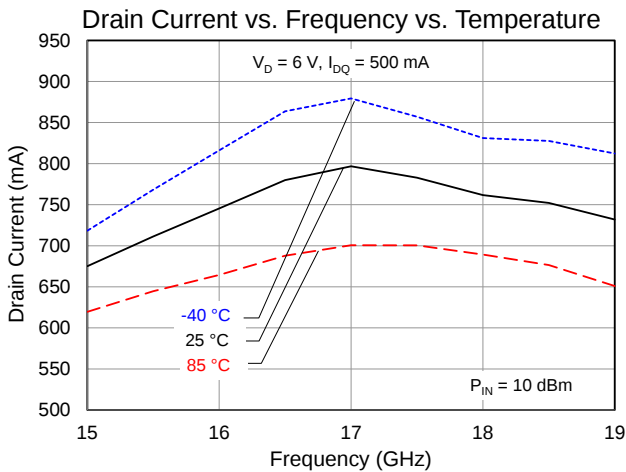
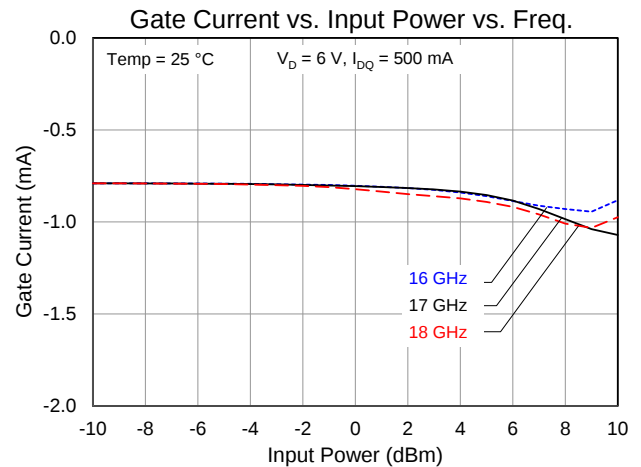
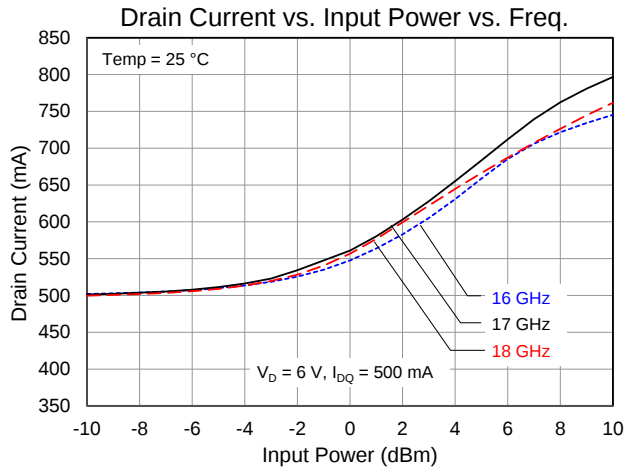
Typical Performance: Large Signal



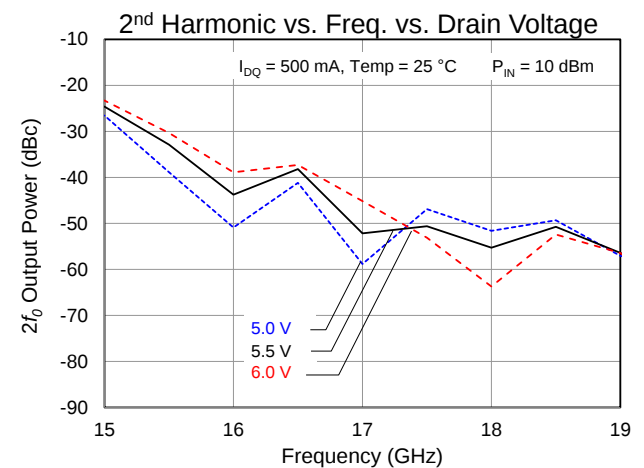
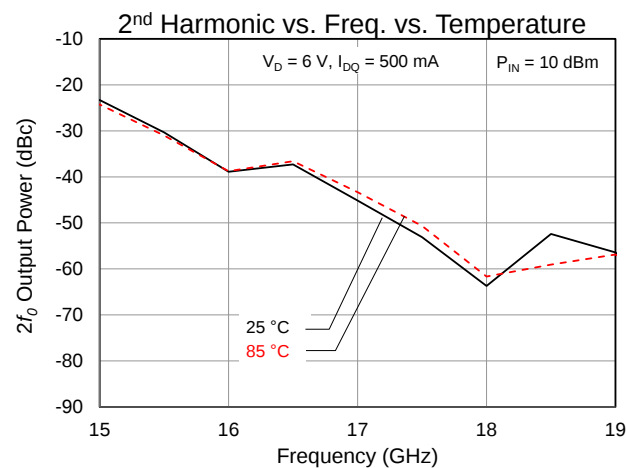
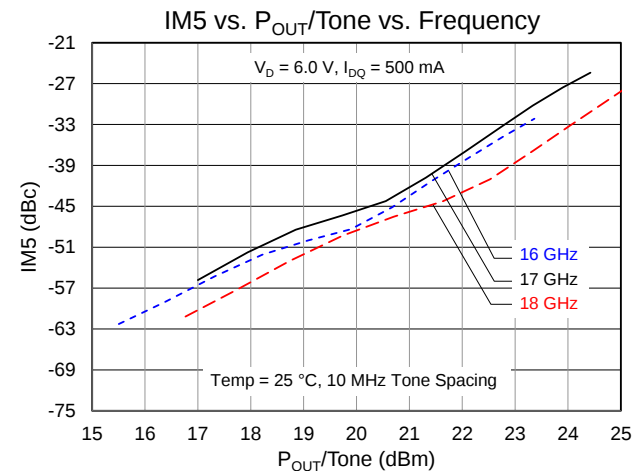
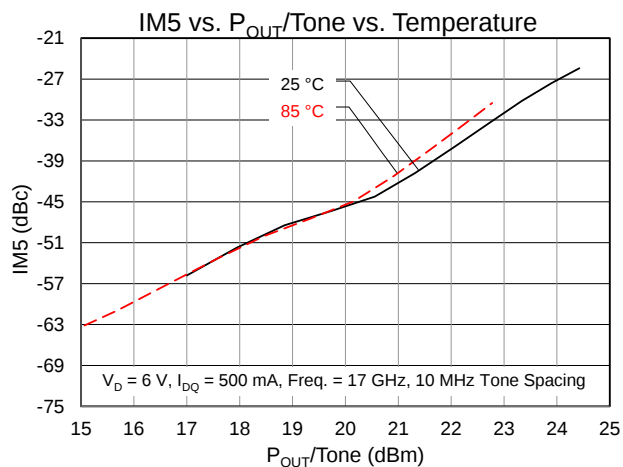
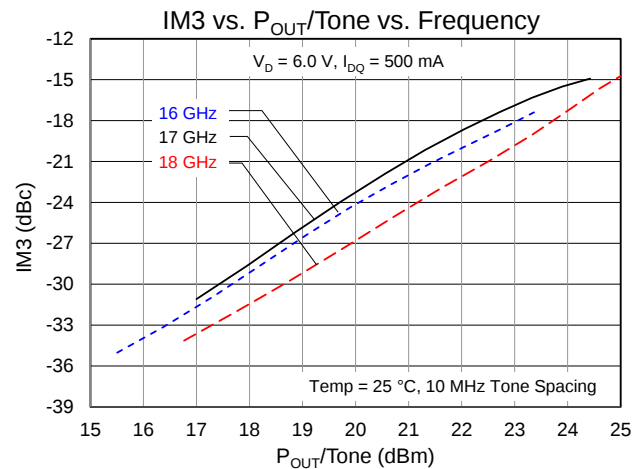
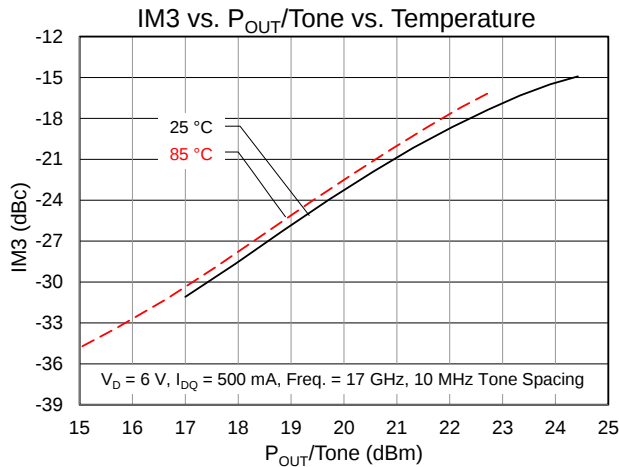
Typical Performance: Large Signal



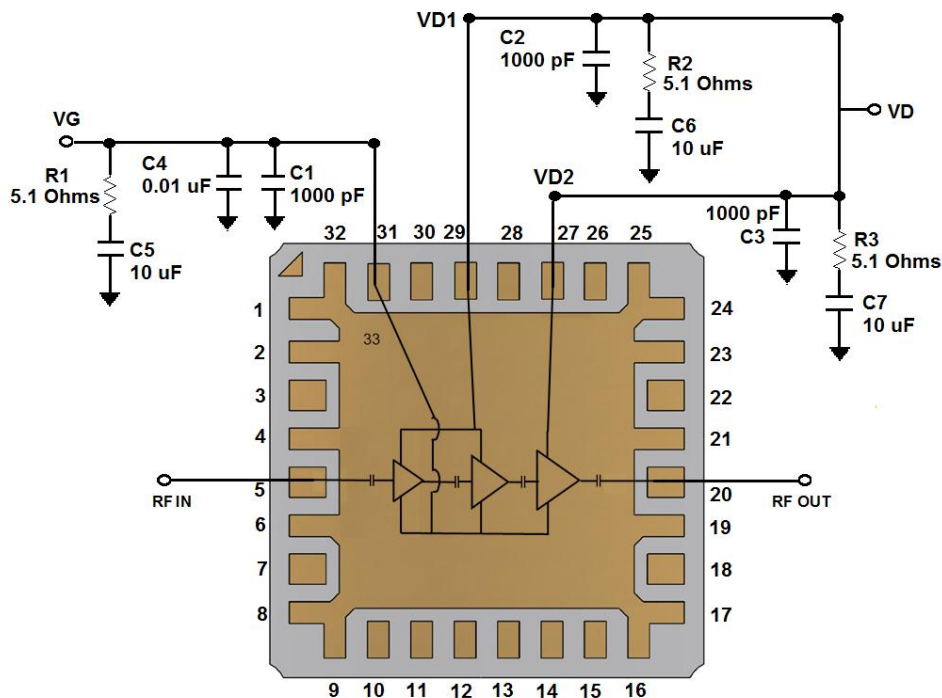
Typical Performance: Large Signal



Typical Performance: Linearity



Application Information



Bias-up Procedure

1. Set I_D limit to 1000 mA, I_G limit to 4 mA
2. Apply -2 V to V_G for pinch off
3. Apply +6 V to V_D
4. Adjust V_G more positive until $I_{DQ} = 500$ mA ($V_G \sim -0.6$ V Typical)
5. Apply RF signal

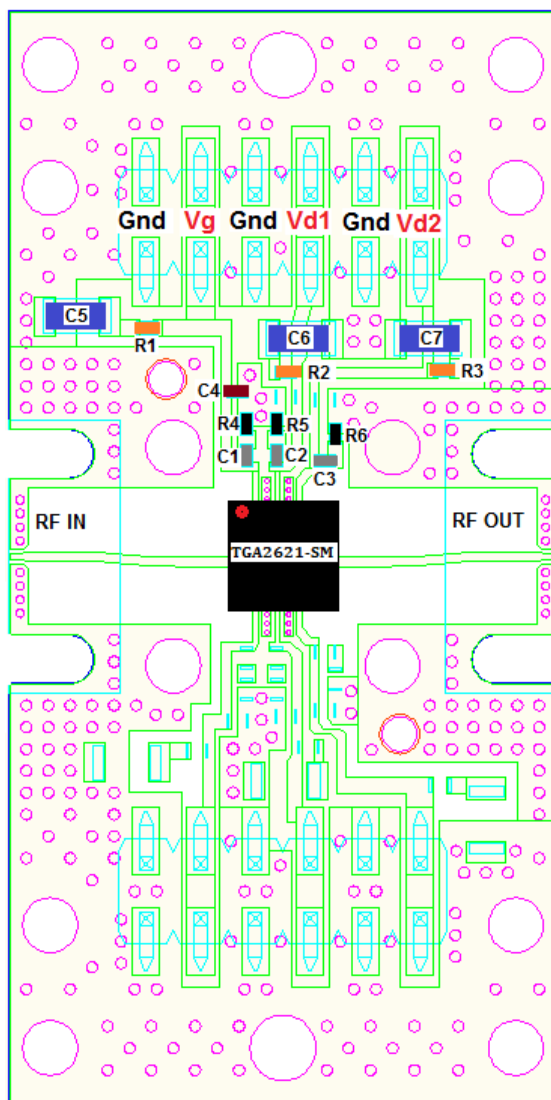
Bias-down Procedure

1. Turn off RF signal
2. Reduce V_G to -2 V. Ensure $I_{DQ} \sim 0$ mA
3. Set V_D to 0 V
4. Turn off V_D supply
5. Turn off V_G supply

Pin Description

Pin No.	Symbol	Description
1, 2, 4, 6, 8-9, 16-17, 19, 21, 23-25, 32	Gnd	Recommend grounding on PCB
3, 7, 10-15, 18, 22, 26, 28, 30	N/C	No Internal Connection
5	RF_{IN}	Input; matched to 50 Ω ; DC blocked
20	RF_{OUT}	Output; matched to 50 Ω ; DC blocked
27, 29	V_{D1}, V_{D2}	Drain voltage; bias network is required; see recommended Application Information above.
31	V_G	Gate voltage; bias network is required; see recommended Application Information above.
33	Gnd	Ground Paddle. Multiple vias should be employed to minimize inductance and thermal resistance.

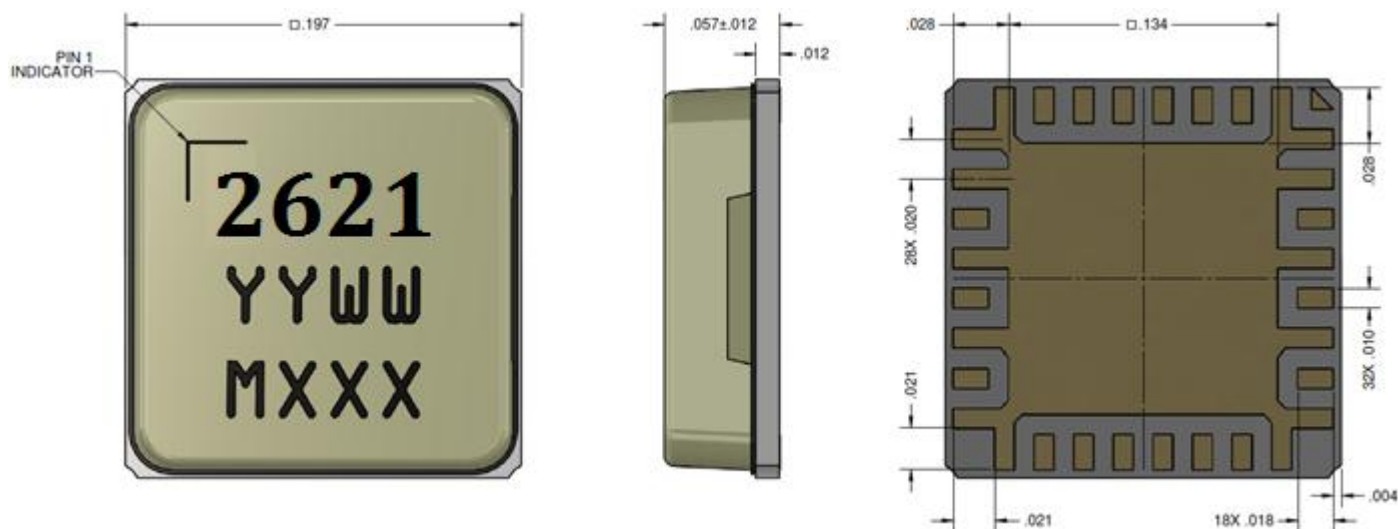
Evaluation Board



Bill of Material

Reference Des.	Value	Description	Manuf.	Part Number
C1 – C3	1000 pF	Cap, 0402, X7R	Various	
C4	0.01 uF	Cap, 0402, X7R	Various	
C5 – C7	10 µF	Cap, 1206, X5R	Various	
R1 – R3	5.1 Ohms	Res, 0402	Various	
R4 – R6	0 Ohms	Res, 0402 (Jumper required for above EVB design)	Various	

Mechanical Information



Units: inches

Tolerances: unless specified

x.xx = ± 0.01

x.xxx = ± 0.005

Materials:

Base: Ceramic

Lid: Plastic

All metalized features are Au plated

Part is epoxy sealed

Marking:

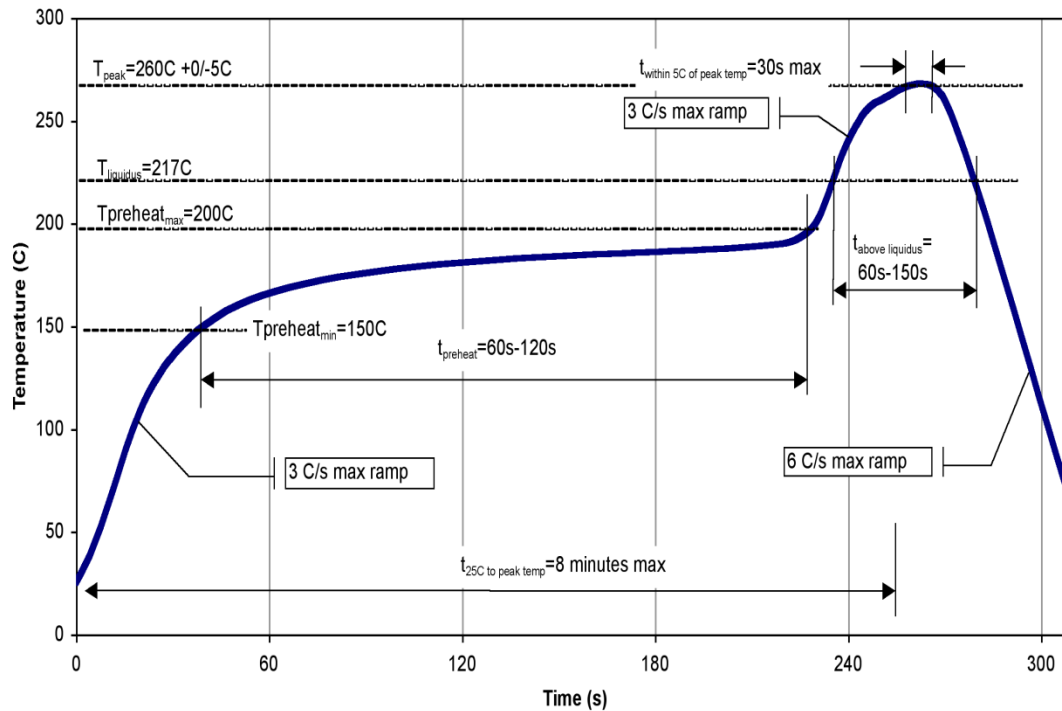
2621: Part number

YY: Part Assembly year

WW: Part Assembly week

MXXX: Lot ID

Recommended Soldering Temperature Profile



Product Compliance Information

ESD Sensitivity Ratings



Caution! ESD-Sensitive Device

ESD Rating: TBD
Value: TBD
Test: Human Body Model (HBM)
Standard: JEDEC Standard JESD22-A114

MSL Rating

Level 3 at 260 °C convection reflow
The part is rated Moisture Sensitivity Level 3 at 260 °C per JEDEC standard IPC/JEDEC J-STD-020.

ECCN

US Department of Commerce: EAR99

Solderability

Compatible with the latest version of J-STD-020, Lead-free solder, 260 °C.

RoHS Compliance

This part is compliant with EU 2002/95/EC RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment).

This product also has the following attributes:

- Lead Free
- Halogen Free (Chlorine, Bromine)
- Antimony Free
- TBBP-A (C₁₅H₁₂Br₄O₂) Free
- PFOS Free
- SVHC Free

Contact Information

For the latest specifications, additional product information, worldwide sales and distribution locations, and information about TriQuint:

Web: www.triquint.com
Email: info-sales@triquint.com

Tel: +1.972.994.8465
Fax: +1.972.994.8504

For technical questions and application information: Email: info-products@triquint.com

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