# **UF2815B**



## RF Power MOSFET Transistor 15W, 100-500 MHz, 28V

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### **Features**

- N-Channel enhancement mode device
- DMOS structure
- Lower capacitances for broadband operation
- · Common source configuration
- Lower noise floor
- RoHS Compliant
- 100 MHz to 500 MHz operation

### **ABSOLUTE MAXIMUM RATINGS AT 25° C**

Parameter	Symbol	Rating	Units
Drain-Source Voltage	$V_{DS}$	65	V
Gate-Source Voltage	V <sub>GS</sub>	20	V
Drain-Source Current	I <sub>DS</sub>	4.2	Α
Power Dissipation	P <sub>D</sub>	48.6	W
Junction Temperature	TJ	200	°C
Storage Temperature	T <sub>STG</sub>	-55 to 150	°C
Thermal Resistance	$\theta_{\text{JC}}$	3.6	°C/W

## **TYPICAL DEVICE IMPEDANCES**

F (MHz)	Z <sub>IN</sub> (Ω)	Z <sub>LOAD</sub> (Ω)			
100	6.4-j25.0	22.0+j16.0			
300	6.5-j12.0	15.0+j14.0			
500	1.7-j10.5	8.0=j10.5			
V <sub>DD</sub> =28V, I <sub>DQ</sub> =150 mA, P <sub>OUT</sub> =15.0 W					

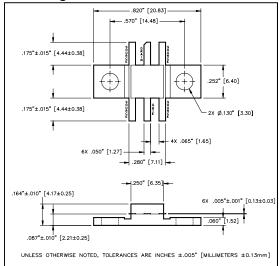
 $Z_{\mbox{\scriptsize IN}}$  is the series equivalent input impedance of the device from gate to source.

 $Z_{\text{LOAD}}$  is the optimum series equivalent load impedance as measured from drain to ground.

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Parameter	Symbol	Min	Max	Units	Test Conditions
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	65	-	V	V <sub>GS</sub> = 0.0 V , I <sub>DS</sub> = 6.0 mA
Drain-Source Leakage Current	I <sub>DSS</sub>	-	3.0	mA	V <sub>GS</sub> = 28.0 V , V <sub>GS</sub> = 0.0 V
Gate-Source Leakage Current	I <sub>GSS</sub>	-	3.0	μA	V <sub>GS</sub> = 20.0 V , V <sub>DS</sub> = 0.0 V
Gate Threshold Voltage	$V_{GS(TH)}$	2.0	6.0	V	V <sub>DS</sub> = 10.0 V , I <sub>DS</sub> = 30.0 mA
Forward Transconductance	$G_{M}$	.240	-	S	$V_{DS}$ = 10.0 V , $I_{DS}$ 300.0 mA , $\Delta$ $V_{GS}$ = 1.0V, 80 $\mu$ s Pulse
Input Capacitance	C <sub>ISS</sub>	-	21	pF	V <sub>DS</sub> = 28.0 V , F = 1.0 MHz
Output Capacitance	Coss	-	15	pF	V <sub>DS</sub> = 28.0 V , F = 1.0 MHz
Reverse Capacitance	C <sub>RSS</sub>	-	7.2	pF	V <sub>DS</sub> = 28.0 V , F = 1.0 MHz
Power Gain	G <sub>P</sub>	10	-	dB	V <sub>DD</sub> = 28.0 V, I <sub>DQ</sub> = 150.0 mA, P <sub>OUT</sub> = 15.0 W F =500 MHz
Drain Efficiency	ŋ <sub>D</sub>	50	-	%	V <sub>DD</sub> = 28.0 V, I <sub>DQ</sub> = 150.0 mA, P <sub>OUT</sub> = 15.0 W F =500 MHz
Load Mismatch Tolerance	VSWR-T	-	20:1	-	V <sub>DD</sub> = 28.0 V, I <sub>DQ</sub> = 150.0 mA, P <sub>OUT</sub> = 15.0 W F =500 MHz

## **Package Outline**



LETTER	MILLIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	20.70	20.96	.815	.825	
В	14.35	4.35 14.61		.575	
С	14.73	15.24	.580	.575	
D	6.27	6.53	.247	.257	
Е	6.22	6.48	.245	.255	
F	1.14	1.40	.045	.055	
G	1.52	1.78	.060	.070	
Н	2.92	3.17	.115	.125	
J	1.40	1.65	.055	.065	
К	2.03	2.39	.080	.094	
L	3.66	4.32	.144	.170	
М	.10	.15	.004	.006	

• Europe Tel: 44.1908.574.200 / Fax: 44.1908.574.300

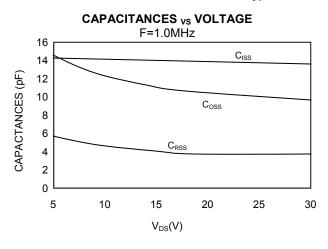
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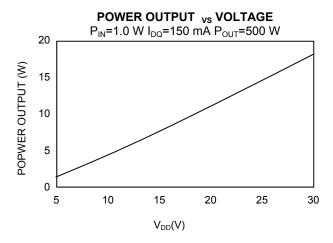


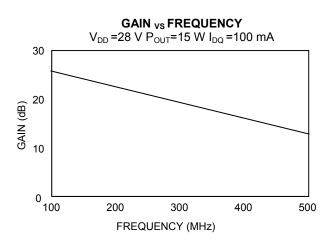
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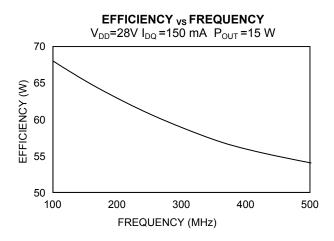
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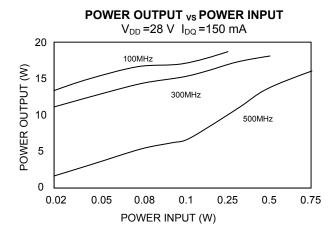
## **Typical Broadband Performance Curves**











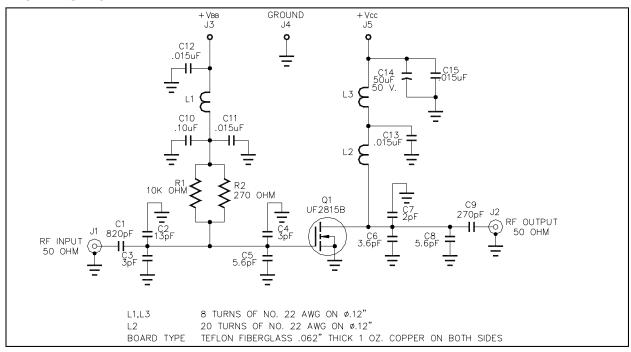
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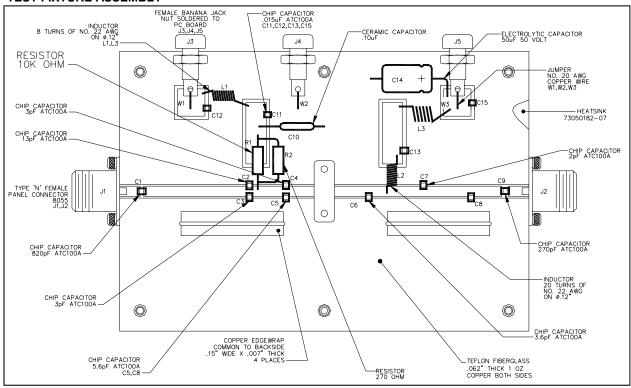
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## **TEST FIXTURE SCHEMATIC**



## **TEST FIXTURE ASSEMBLY**



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