

10.0-21.0 GHz GaAs MMIC Buffer Amplifier

October 2008 - Rev 04-Oct-08

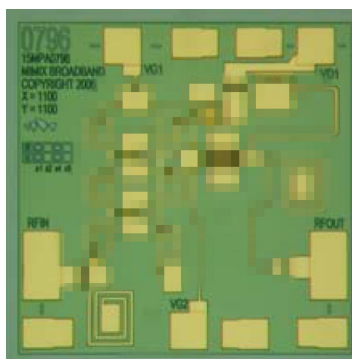
Features

- X Excellent Transmit LO/Output Buffer Stage
- X Compact Size
- X 18.0 dB Small Signal Gain
- X +20.0 dBm P1dB Compression Point
- X 5.5 dB Noise Figure
- X Variable Gain with Adjustable Bias
- X 100% On-Wafer RF, DC and Output Power Testing
- X 100% Commercial-Level Visual Inspection Using Mil-Std-883 Method 2010

General Description

Mimix Broadband's two stage 10.0-21.0 GHz GaAs MMIC buffer amplifier has a small signal gain of 18.0 dB with a +20.0 dBm P1dB output compression point. The device also provides variable gain regulation with adjustable bias. This MMIC uses Mimix Broadband's GaAs PHEMT device model technology, and is based upon electron beam lithography to ensure high repeatability and uniformity. The chip has surface passivation to protect and provide a rugged part with backside via holes and gold metallization to allow either a conductive epoxy or eutectic solder die attach process. This device is well suited for Microwave and Millimeter-wave Point-to-Point Radio, LMDS, SATCOM and VSAT applications.

Chip Device Layout



Absolute Maximum Ratings

Supply Voltage (Vd)	+6.0 VDC
Supply Current (Id1)	180 mA
Gate Bias Voltage (Vg)	+0.3 VDC
Input Power (Pin)	+20.0 dBm
Storage Temperature (Tstg)	-65 to +165 °C
Operating Temperature (Ta)	-55 to MTTF Graph ¹
Channel Temperature (Tch)	MTTF Graph ¹

(1) Channel temperature affects a device's MTTF. It is recommended to keep channel temperature as low as possible for maximum life.

Electrical Characteristics (Ambient Temperature T = 25 °C)

Parameter	Units	Min.	Typ.	Max.
Frequency Range (f)	GHz	10.0	-	21.0
Input Return Loss (S11)	dB	-	15.0	-
Output Return Loss (S22)	dB	-	17.0	-
Small Signal Gain (S21)	dB	-	18.0	-
Gain Flatness ($\Delta S21$)	dB	-	+/-2.0	-
Reverse Isolation (S12)	dB	-	35.0	-
Noise Figure (NF)	dB	-	5.5	-
Output Power for 1dB Compression (P1dB) ²	dBm	-	+20.0	-
Saturated Output Power (Psat)	dBm	-	+22.0	-
Drain Bias Voltage (Vd)	VDC	-	+4.0	+5.5
Gate Bias Voltage (Vg2)	VDC	-1.0	-0.5	0.0
Supply Current (Id) (Vd=4.0V, Vg2=-0.5V Typical)	mA	-	90	-

(2) Measured using constant current.

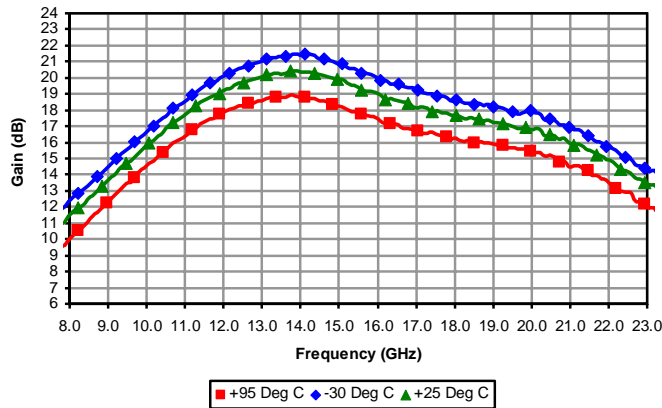
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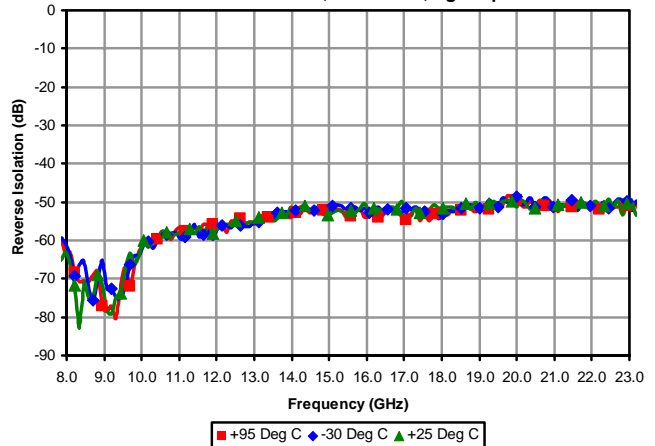
B1008-BD

Buffer Amplifier Measurements

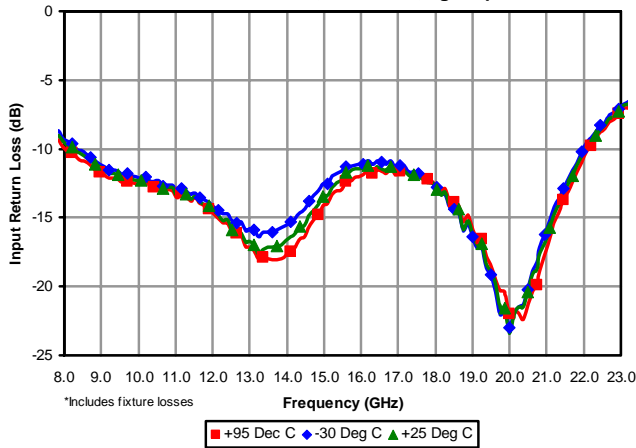
XB1008-BD Vd=4.0 V, Id=130 mA, Vg2=Open



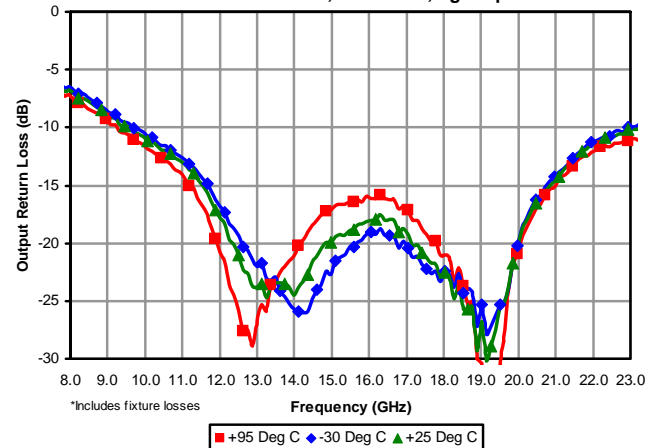
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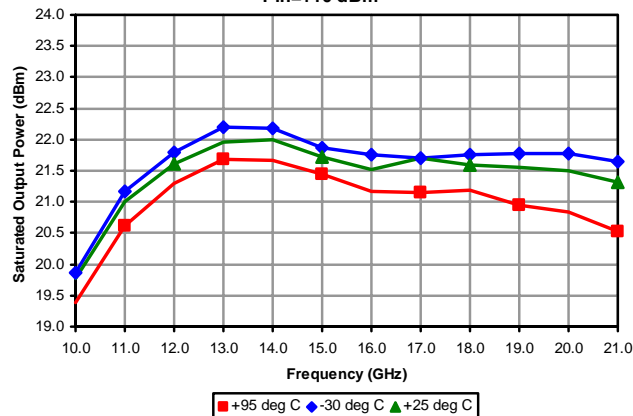
XB1008-BD Vd=4.0 V, Id=130 mA, Vg2=Open



XB1008-BD Vd=4.0 V, Id=130 mA, Vg2=Open



XB1008-BD Vd=4.0 V, Id=130 mA, Vg2=Open
Pin=+10 dBm

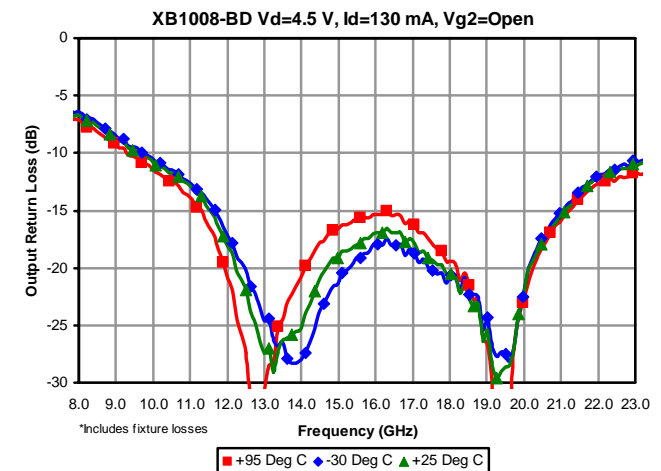
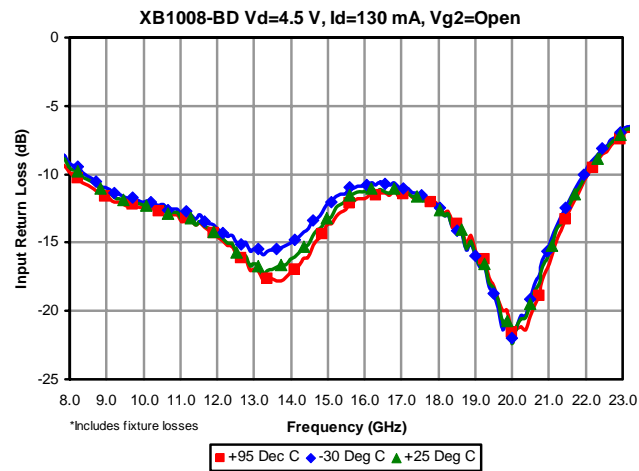
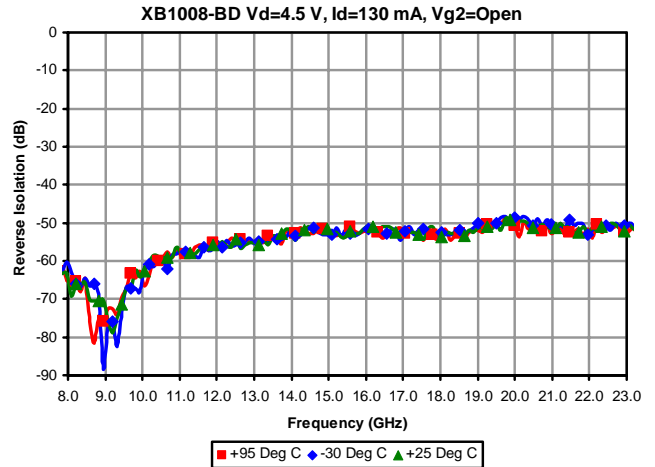
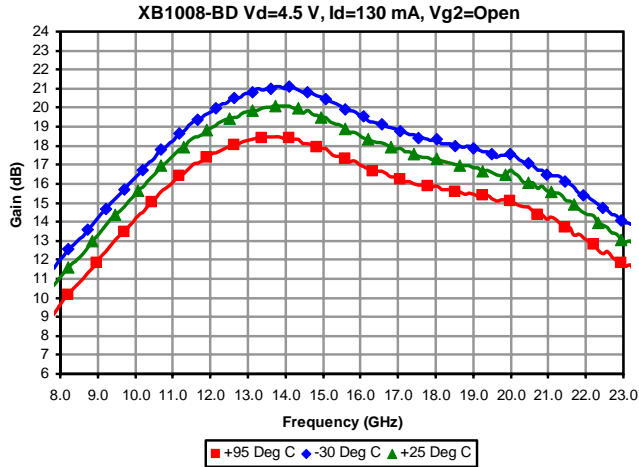


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Buffer Amplifier Measurements (cont.)

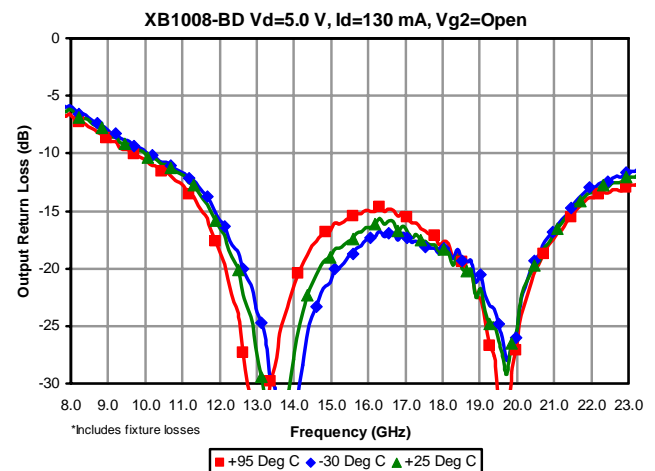
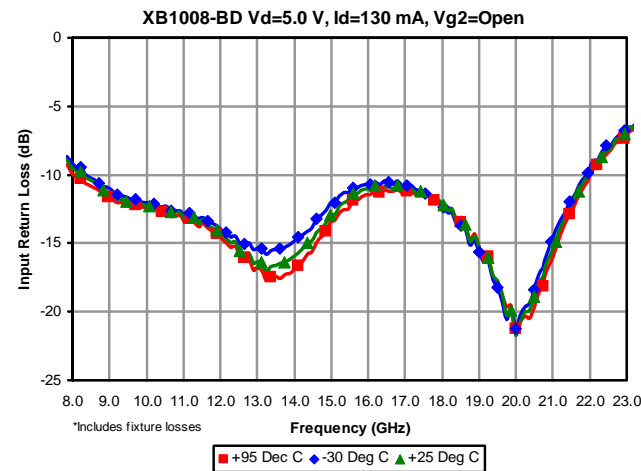
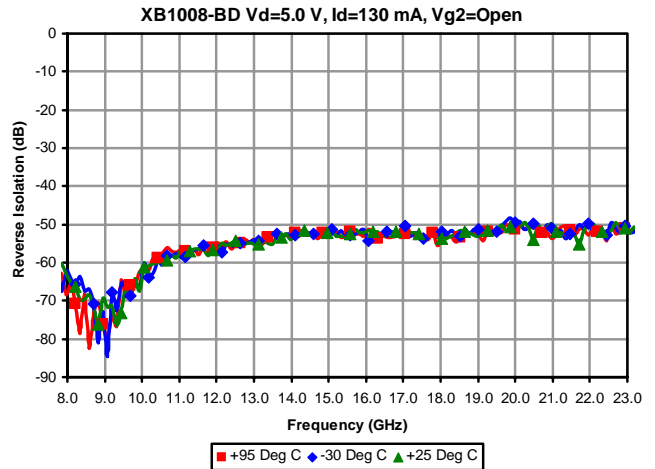
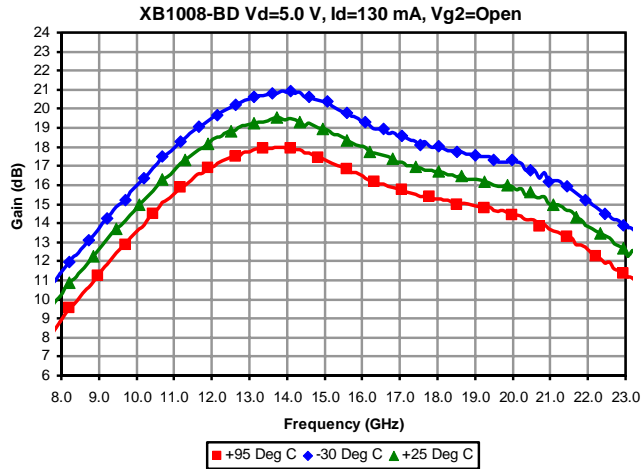


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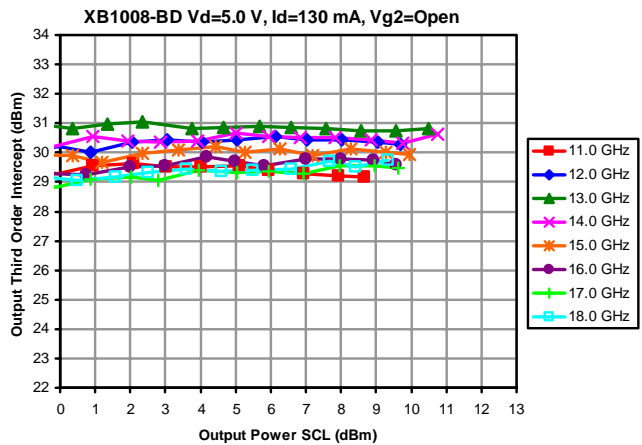
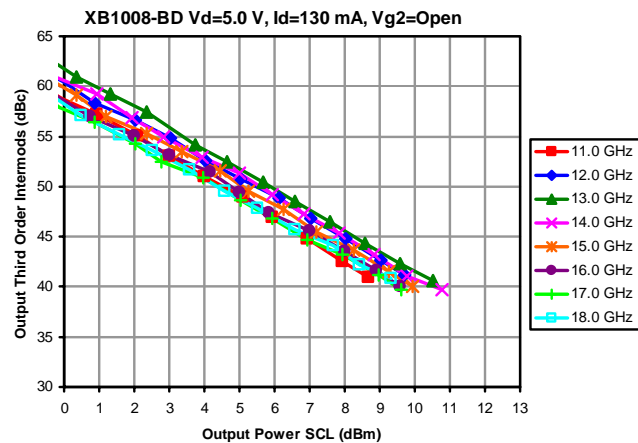
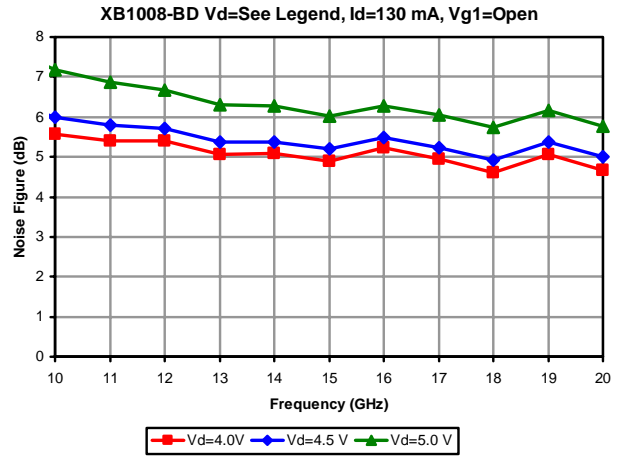
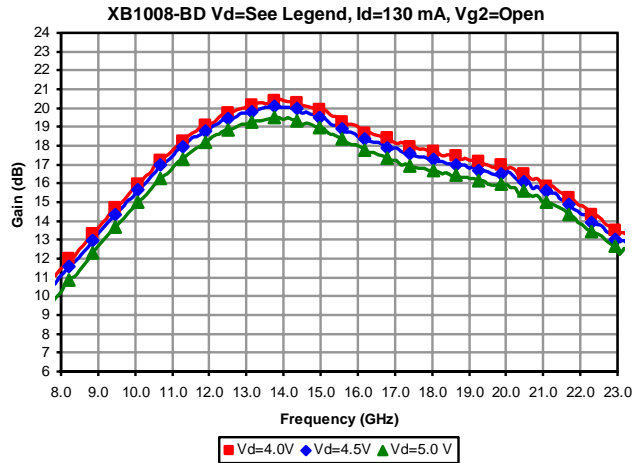
Buffer Amplifier Measurements (cont.)



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Buffer Amplifier Measurements (cont.)



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S-Parameters

Typical S-Parameter Data for XB1008-BD

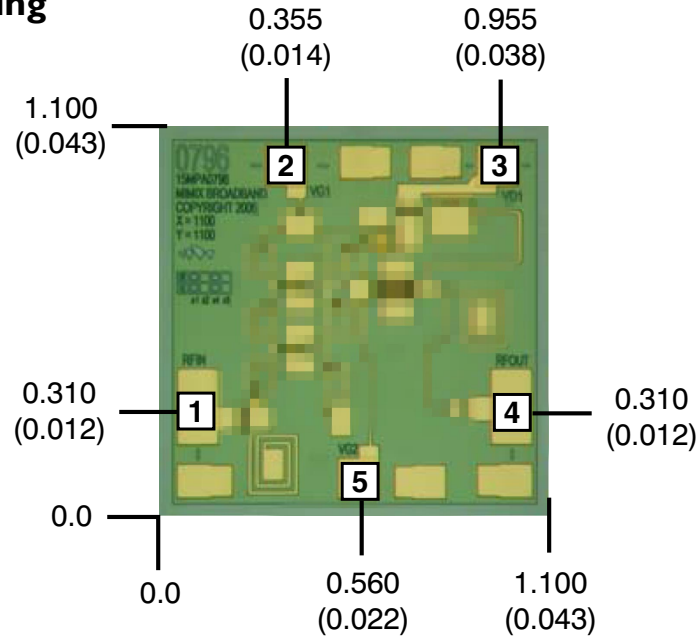
Vd=4.5 V, Id=130 mA

Frequency (GHz)	S11 (Mag)	S11 (Ang)	S21 (Mag)	S21 (Ang)	S12 (Mag)	S12 (Ang)	S22 (Mag)	S22 (Ang)
1.0	0.944	-60.98	0.000	18.80	0.0001	-36.08	0.997	-24.90
2.0	0.848	-108.53	0.002	13.67	0.0001	-150.55	0.985	-50.82
3.0	0.771	-146.17	0.014	5.52	0.0001	-72.16	0.957	-80.78
4.0	0.704	-178.31	0.063	-12.67	0.0004	-135.80	0.906	-116.97
5.0	0.631	152.65	0.283	-23.76	0.0015	-136.07	0.840	-161.09
6.0	0.539	124.99	1.207	-78.09	0.0029	164.77	0.734	146.65
7.0	0.431	100.75	2.408	-139.54	0.0027	106.15	0.598	97.34
8.0	0.344	80.18	3.489	173.52	0.0013	51.18	0.494	56.23
9.0	0.282	59.71	4.703	135.62	0.0010	-97.63	0.402	20.48
10.0	0.239	37.23	6.164	100.64	0.0027	-139.22	0.295	-12.31
11.0	0.209	10.36	7.669	65.97	0.0046	-164.96	0.181	-42.75
12.0	0.185	-20.70	8.900	31.63	0.0058	177.10	0.076	-75.93
13.0	0.173	-53.67	9.595	-1.05	0.0076	159.37	0.029	-173.57
14.0	0.158	-82.88	9.675	-31.28	0.0081	145.97	0.075	110.08
15.0	0.150	-104.65	9.420	-58.29	0.0090	137.26	0.117	87.98
16.0	0.154	-119.28	9.144	-82.19	0.0092	134.02	0.132	69.11
17.0	0.166	-133.48	8.787	-104.96	0.0099	126.95	0.140	54.17
18.0	0.178	-153.38	8.366	-127.67	0.0118	122.92	0.126	39.88
19.0	0.179	-172.17	7.787	-149.56	0.0149	115.81	0.086	30.73
20.0	0.137	-176.89	6.957	-167.30	0.0179	94.96	0.068	9.28
21.0	0.179	-175.27	6.681	174.38	0.0152	84.98	0.044	-113.48
22.0	0.186	177.55	6.060	156.51	0.0166	74.38	0.102	-170.80
23.0	0.206	169.96	5.474	140.27	0.0173	62.82	0.172	172.17
24.0	0.236	162.19	4.967	125.45	0.0165	47.90	0.253	158.99
25.0	0.266	150.98	4.551	111.76	0.0145	36.28	0.330	146.55
26.0	0.288	139.84	4.172	98.08	0.0140	30.85	0.387	135.74
27.0	0.309	129.39	3.935	86.20	0.0127	16.37	0.444	126.78
28.0	0.327	117.66	3.752	73.15	0.0110	3.68	0.485	118.34
29.0	0.335	107.31	3.599	59.84	0.0099	-5.58	0.515	111.39
30.0	0.336	95.98	3.463	45.69	0.0080	-16.75	0.536	106.13

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Mechanical Drawing



(Note: Engineering designator is 15MPA0857)

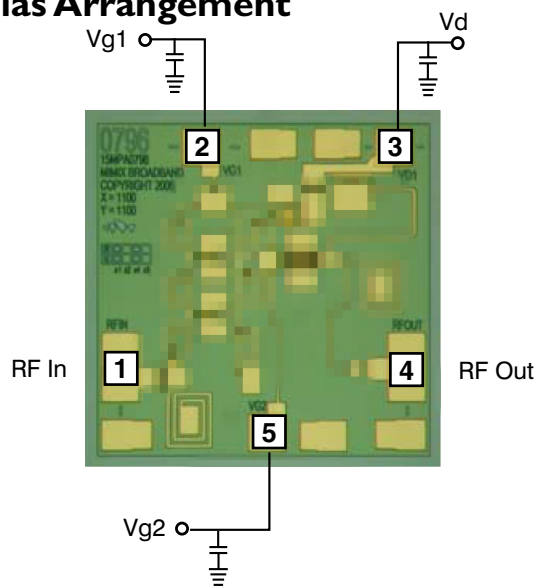
Units: millimeters (inches) Bond pad dimensions are shown to center of bond pad.
 Thickness: 0.110 +/- 0.010 (0.0043 +/- 0.0004), Backside is ground, Bond Pad/Backside Metallization: Gold
 All DC Bond Pads (except Vd3) are 0.100 x 0.100 (0.004 x 0.004). All RF Bond Pads (and Vd3) are 0.100 x 0.200 (0.004 x 0.008)
 Bond pad centers are approximately 0.109 (0.004) from the edge of the chip.
 Dicing tolerance: +/- 0.005 (+/- 0.0002). Approximate weight: 0.75 mg.

Bond Pad #1 (RF In)
Bond Pad #2 (Vg1)

Bond Pad #3 (Vd)
Bond Pad #4 (RF Out)

Bond Pad #5 (Vg2)

Bias Arrangement



Bypass Capacitors - See App Note [2]

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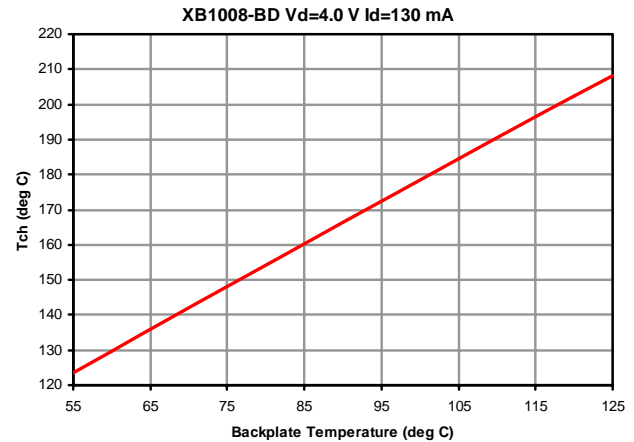
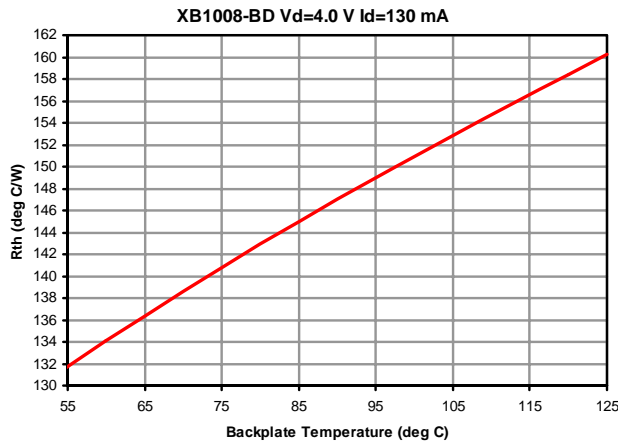
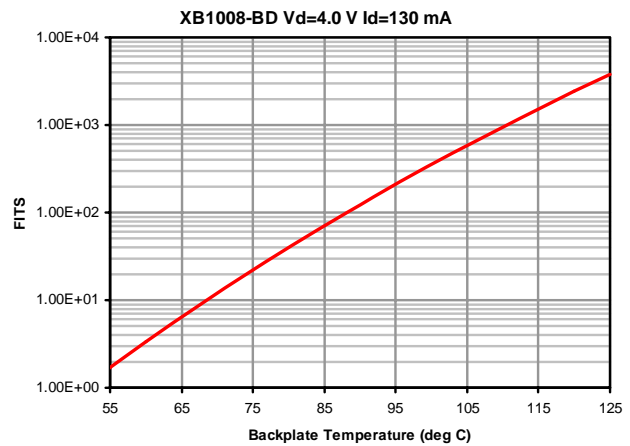
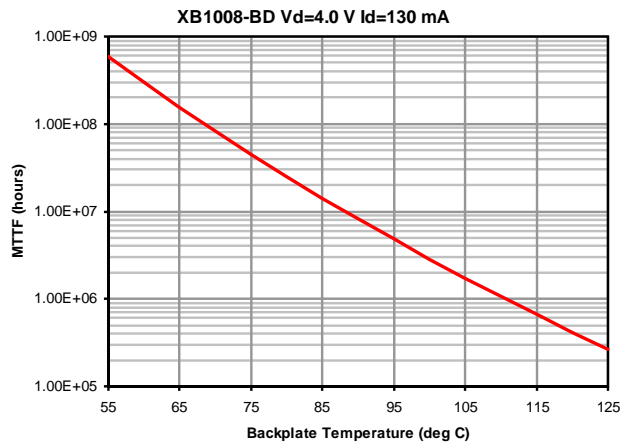
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App Note [1] Biasing - The device provides variable gain with adjustable bias regulation. For optimum linearity performance, it is recommended to bias this device at $V_d=4V$ with $I_d=90\text{ mA}$ (V_{g2} at approximately $-0.5V$ and V_{g1} left open). It is also recommended to use active biasing to control the drain currents because this gives the most reproducible results over temperature or RF level variations. Depending on the supply voltage available and the power dissipation constraints, the bias circuit may be a single transistor or a low power operational amplifier, with a low value resistor in series with the drain supply used to sense the current. The gate of the pHEMT is controlled to maintain correct drain current and thus drain voltage. The typical gate voltage needed to do this is $-0.5V$. Typically the gate is protected with Silicon diodes to limit the applied voltage. Also, make sure to sequence the applied voltage to ensure negative gate bias is available before applying the positive drain supply.

App Note [2] Bias Arrangement - For Individual Stage Bias (Recommended for saturated applications) -- Each DC pad (V_d and $V_{g1,2}$) needs to have DC bypass capacitance ($\sim 100\text{-}200\text{ pF}$) as close to the device as possible. Additional DC bypass capacitance ($\sim 0.01\text{ }\mu\text{F}$) is also recommended.

MTTF Graphs

These numbers were calculated based on accelerated life test information and thermal model analysis received from the fabricating foundry.



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Handling and Assembly Information

CAUTION! - Mimix Broadband MMIC Products contain gallium arsenide (GaAs) which can be hazardous to the human body and the environment. For safety, observe the following procedures:

- Do not ingest.
- Do not alter the form of this product into a gas, powder, or liquid through burning, crushing, or chemical processing as these by-products are dangerous to the human body if inhaled, ingested, or swallowed.
- Observe government laws and company regulations when discarding this product. This product must be discarded in accordance with methods specified by applicable hazardous waste procedures.

Life Support Policy - Mimix Broadband's products are not authorized for use as critical components in life support devices or systems without the express written approval of the President and General Counsel of Mimix Broadband. As used herein: (1) Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user. (2) A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

ESD - Gallium Arsenide (GaAs) devices are susceptible to electrostatic and mechanical damage. Die are supplied in antistatic containers, which should be opened in cleanroom conditions at an appropriately grounded anti-static workstation. Devices need careful handling using correctly designed collets, vacuum pickups or, with care, sharp tweezers.

Die Attachment - GaAs Products from Mimix Broadband are 0.100 mm (0.004") thick and have vias through to the backside to enable grounding to the circuit. Microstrip substrates should be brought as close to the die as possible. The mounting surface should be clean and flat. If using conductive epoxy, recommended epoxies are Tanaka TS3332LD, Die Mat DM6030HK or DM6030HK-Pt cured in a nitrogen atmosphere per manufacturer's cure schedule. Apply epoxy sparingly to avoid getting any on to the top surface of the die. An epoxy fillet should be visible around the total die periphery. For additional information please see the Mimix "Epoxy Specifications for Bare Die" application note. If eutectic mounting is preferred, then a fluxless gold-tin (AuSn) preform, approximately 0.001 thick, placed between the die and the attachment surface should be used. A die bonder that utilizes a heated collet and provides scrubbing action to ensure total wetting to prevent void formation in a nitrogen atmosphere is recommended. The gold-tin eutectic (80% Au 20% Sn) has a melting point of approximately 280 °C (Note: Gold Germanium should be avoided). The work station temperature should be 310 °C +/- 10 °C. Exposure to these extreme temperatures should be kept to minimum. The collet should be heated, and the die pre-heated to avoid excessive thermal shock. Avoidance of air bridges and force impact are critical during placement.

Wire Bonding - Windows in the surface passivation above the bond pads are provided to allow wire bonding to the die's gold bond pads. The recommended wire bonding procedure uses 0.076 mm x 0.013 mm (0.003" x 0.0005") 99.99% pure gold ribbon with 0.5-2% elongation to minimize RF port bond inductance. Gold 0.025 mm (0.001") diameter wedge or ball bonds are acceptable for DC Bias connections. Aluminum wire should be avoided. Thermo-compression bonding is recommended though thermosonic bonding may be used providing the ultrasonic content of the bond is minimized. Bond force, time and ultrasonics are all critical parameters. Bonds should be made from the bond pads on the die to the package or substrate. All bonds should be as short as possible.

Ordering Information

Part Number for Ordering

XB1008-BD-000V
XB1008-BD-EV1

Description

Where "V" is RoHS compliant die packed in vacuum release gel paks
XB1008 die evaluation module



Proper ESD procedures should be followed when handling this device.

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